VIDHARSHANA J

ELECTRICAL ENGINEER

CONTACT

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TECHNICAL SKILLS

- C   - Medium
- C++   - Medium
- HTML, CSS   - Medium
- Problem Solving   - Medium
- JavaScript   - Basic
- STM32 Microcontroller - Basic
- MySQL   - Basic

PERSONAL SKILLS

- Adaptability
- Diligence
- Self- motivated

AREAS OF INTEREST

- Integrated Circuits & Applications
- Operating Systems

CARRER OBJECTIVE

To work in a challenging environment that will enable me to use my strong organizational skills and educational background with the ability to work well in various streams and learn exciting technologies. To bring out the best of my talents with the organization and be a part of its growth.

EDUCATIONAL QUALIFICATION

B.E- ELECTRICAL AND ELECTRONICS ENGINEERING (2020 - 2024)
Bannari Amman Institute of Technology, Sathyamangalam, Erode, India.
CGPA: 9.21(Current)

HSC: (2019 - 2020)
Bharathi Vidya Mandir Matric Higher Secondary School, Theni, India.
Percentage: 79%

SSLC: (2017 - 2018)
Theni Kammavar Sangam Matric Higher Secondary School, Theni, India.
Percentage: 96%

PROJECT

"Soil Moisture Monitoring System using STM32"
Role Played: Testing
Team Size: 3
Duration: Apr 2022 - May 2022
Description:
Prototype model that monitors soil moisture level which is helpful in the agricultural field by using relay.

TECHNICAL EVENTS

- Idea got shortlisted in **Amazon smbhav Hackathon 2021**, Amazon in collaboration with Skillenza.
- Participated in **Open Idea Challenge 2021**, IIIT Nagpur.
- Participated in round 2 **L&T Techgium** 2022.

TOOLS

- STM32CubeMX
- Visual Studio Code
- Figma, Canva

LANGUAGES KNOWN

- Tamil (R W S)
- English (R W S)
- Telugu (S)

HOBBIES

- Listening to Music
- Cooking
- Painting

PERSONAL DETAILS

Date Of Birth : 10.06.2002
Gender : Female
Father's Name : Mr. Jeevanandam K
Mother's Name : Sudha J
Nationality : Indian

INTERNSHIPS

- **Website Developer Intern,** Pofi Tehnologies
  14/04/2022 - 28/04/2022| Coimbatore,India
  Managed design and planning throughout the development process of web-based applications.

- **Embedded Systems-STM32 (ARM) Intern,** T-PEP
  15/05/2022 - 02/06/2022| Coimbatore, India
  Worked on several small and doable projects based on real-time application models using STM32 Microcontroller.

ACHIEVEMENTS

- **First Place** - *A National Level Technical Symposium 2021* in AMRITA School of Engineering, Chennai | March 2021.
- **First Place**, *VISHISTA 2K21*, A National Level Technical Symposium in KGiSL Institute of Technology, Coimbatore | MAY 2021.
- **Second Place** – *PAPERANZA 2021* in SRM TRP Engineering College, Trichy | JUNE 2021.
- **Second Place** – *EMBARK'21* Circuit Designing in Bannari Amman Institute of Technology, Erode | JAN 2022.

CO-CIRRICULAR ACTIVITIES

Paper Presentations
- Presented a paper entitled **"Home Automation using Microcontroller"** in the National Conference on Mechatronics and Automation Systems (NCMAS 2021) on 27.03.2021.
- Presented a paper entitled **"Overcoming Limits of IoT using Blockchain Technology"** at Rajalakshmi Institute of Technology on 15.06.2021.

CERTIFICATES

The Fundamentals of Digital Marketing**,** Google
HTML Workshop in GeeksforGeeks

DECLARATION

I VIDHARSHANA J, hereby declare that all the details furnished above are true to the best of my knowledge.

DATE : 22/02/2023
PLACE: Theni

J. Vidharshay.
VIDHARSHANA J