

## Autograder Results

Results

Code

Files Submitted Properly: File Check (1/1)

Microcode: ldarChangedRegisters (8/8)

Microcode: ldarUnchangedRegisters (1/1)

Microcode: ldarPC (1/1)

Microcode: ldarClockCycles (1/1)

Microcode: ldarCC (5/5)

Microcode: stpciUnchangedRegisters (1/1)

Microcode: stpciPC (1/1)

Microcode: stpciChangedMemory (8/8)

Microcode: stpciClockCycles (1/1)

Microcode: stpciCC (1/1)

Microcode: mult8UnchangedRegisters (1/1)

Microcode: mult8ChangedRegisters (7/7)

Microcode: mult8PC (1/1)

Microcode: mult8ClockCycles (1/1)

Microcode: mult8CC (5/5)

Microcode: cchChangedRegisters (11/11)

Microcode: cchUnchangedRegisters (1/1)

Microcode: cchCC (1/1)

Microcode: cchPC (1/1)

Microcode: cchClockCycles (1/1)

Microcode: incClockCycles (1/1)

Microcode: incChangedRegisters (7/7)

Microcode: incUnchangedRegisters (1/1)

Microcode: incCC (4/4)

Microcode: incPC (1/1)

Microcode: ljsrClockCycles (1/1)

Microcode: ljsrChangedRegisters (6/6)

Microcode: ljsrUnchangedRegisters (1/1)

Microcode: ljsrCC (4/4)

Microcode: ljsrPC (1/1)

Microcode: ldpcClockCycles (1/1)

Microcode: ldpcUnchangedRegisters (7/7)

Microcode: ldpcCC (5/5)

Microcode: ldpcPC (1/1)

## Timed Lab 2

● Graded

### Student

Vidit Dharmendra Pokharna

### Total Points

100 / 100 pts

### Autograder Score

100.0 / 100.0

### Passed Tests

Files Submitted Properly: File Check (1/1)

Microcode: ldarChangedRegisters (8/8)

Microcode: ldarUnchangedRegisters (1/1)

Microcode: ldarPC (1/1)

Microcode: ldarClockCycles (1/1)

Microcode: ldarCC (5/5)

Microcode: stpciUnchangedRegisters (1/1)

Microcode: stpciPC (1/1)

Microcode: stpciChangedMemory (8/8)

Microcode: stpciClockCycles (1/1)

Microcode: stpciCC (1/1)

Microcode: mult8UnchangedRegisters (1/1)

Microcode: mult8ChangedRegisters (7/7)

Microcode: mult8PC (1/1)

Microcode: mult8ClockCycles (1/1)

Microcode: mult8CC (5/5)

Microcode: cchChangedRegisters (11/11)

Microcode: cchUnchangedRegisters (1/1)

Microcode: cchCC (1/1)

Microcode: cchPC (1/1)

Microcode: cchClockCycles (1/1)

Microcode: incClockCycles (1/1)

Microcode: incChangedRegisters (7/7)

Microcode: incUnchangedRegisters (1/1)

Microcode: incCC (4/4)

Microcode: incPC (1/1)  
Microcode: ljsrClockCycles (1/1)  
Microcode: ljsrChangedRegisters (6/6)  
Microcode: ljsrUnchangedRegisters (1/1)  
Microcode: ljsrCC (4/4)  
Microcode: ljsrPC (1/1)  
Microcode: ldpcClockCycles (1/1)  
Microcode: ldpcUnchangedRegisters (7/7)  
Microcode: ldpcCC (5/5)  
Microcode: ldpcPC (1/1)