

## Autograder Results

Results

Code

NOT: not (3/3)

NAND: nand (3/3)

NOR: nor (3/3)

AND: and (3/3)

OR: or (3/3)

Decoder: decoder (5/5)

Mux: multiplexer (5/5)

Sign Evaluation: nzp (5/5)

DeMorgan's Law Circuit: demorganCircuit (10/10)

1-Bit Adder: oneBitAdder (5/5)

8-Bit Adder: eightBitAdder (5/5)

Basic ALU: add (5/5)

Basic ALU: and (5/5)

Basic ALU: not (5/5)

Basic ALU: pass (5/5)

Intermediate ALU: isQuadruple (7.5/7.5)

Intermediate ALU: isDivisibleBy16 (7.5/7.5)

Intermediate ALU: multiplyBy9 (7.5/7.5)

Intermediate ALU: minimum (7.5/7.5)

## Homework 2: Digital Logic and DeMorgan's Law

● Graded

### Student

Vidit Dharmendra Pokharna

### Total Points

100 / 100 pts

### Autograder Score

100.0 / 100.0

### Passed Tests

NOT: not (3/3)

NAND: nand (3/3)

NOR: nor (3/3)

AND: and (3/3)

OR: or (3/3)

Decoder: decoder (5/5)

Mux: multiplexer (5/5)

Sign Evaluation: nzp (5/5)

DeMorgan's Law Circuit: demorganCircuit (10/10)

1-Bit Adder: oneBitAdder (5/5)

8-Bit Adder: eightBitAdder (5/5)

Basic ALU: add (5/5)

Basic ALU: and (5/5)

Basic ALU: not (5/5)

Basic ALU: pass (5/5)

Intermediate ALU: isQuadruple (7.5/7.5)

Intermediate ALU: isDivisibleBy16 (7.5/7.5)

Intermediate ALU: multiplyBy9 (7.5/7.5)

Intermediate ALU: minimum (7.5/7.5)