

24VTO 3.3V REGULATOR SECTION

The schematic diagram illustrates the 24VTO 3.3V Regulator Section. The circuit is powered by a 24V input from CON2 (PWR) and J3. The input passes through a diode D3 and a resistor R4 (1E,0805) to the non-inverting input of an MC33063AD voltage regulator (U1). The regulator's output is connected to a 150uH inductor (L1) and a 470uF 50V capacitor (C3). The output is then regulated by a feedback network consisting of a 3.8K resistor (R5) and a 1K resistor (R1) connected to a 5V reference (D1, RED LED, 0805). The output is filtered by a 100nF capacitor (C7). The circuit also includes a 100uF 50V capacitor (C4) and a 470pF capacitor (C8).

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