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Electronics Sample Questions S et 1

Competitive Exams

What is D-FF?

What is the basic difference between Latches and Flip flops?

What is a multiplexer?

How can you convert an SR Flip-flop to a JK Flip-flop?

How can you convert an JK Flip-flop to a D Flip-flop?

What is Race-around problem?

How can you rectify it?

Which semiconductor device is used as a voltage regulator and why?

Explain an ideal voltage source?

Explain zener breakdown and avalanche breakdown?

What are the different types of filters?

What is the need of filtering ideal response of filters and actual response of filters?

What is sampling theorem?

What is impulse response?

Explain the advantages and disadvantages of FIR filters compared to IIR counterparts. What is CMRR?

Explain half-duplex and full-duplex communication?

Which range of signals is used for terrestrial transmission?

Why is there need for modulation?

Which type of modulation is used in TV transmission?

Why we use vestigial side band (VSB-C3F) transmission for picture?

When transmitting digital signals is it necessary to transmit some harmonics in addition to fundamental frequency?

For asynchronous transmission, is it necessary to supply some synchronizing pulses additionally or to supply or to supply start and stop bit?

BPFSK is more efficient than BFSK in presence of noise. Why?

What is meant by pre-emphasis and de-emphasis?

Explain 3 dB cutoff frequency? Why is it 3 dB, not 1 dB?

Explain ASCII, EBCDIC?

How to manufacture the CMOS inverter?

What are the advantages of using C band for satellite communication?

What frequency bands are used in Satellite Communication?

what is the difference between latch and flipflop?

what is the main difference between 8085 and 8086 processors?

What is the purpose of the package around a microprocessor silicon die?

How can we identify how many states the ripple counter is having by looking at the figure as it can also have some invalid states?

Design gray to binary code converter?

What is a BCD?

What are its advantages and disadvantages?

Why is an excess-3 code is called an unweighted code?

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Describe various steps in photo etching Tc fabrication?

State and Prove Norton's theorem?

Classify different types of Oscillators?

Mention Barkhausen's Criterion of Oscillation?

Explain the block diagram of PLL?

Write the difference between Closed And Open loop system?

What is Positive logic and Negative Logic?

State Thevenin's theorem & Superposition theorem?

What is Multivibrator?

Classify them Explain different types of Memory?

What are Conditional operators in C?

Explain various storage classes in C?

Define Characteristic Impedance, Input impedance and Standing Wave Ratio of Transmission line?

Write a short note on Logic State Analyzer?

Define Numerical aperture and Critical angle?

Explain the working of A/D converter?

Describe the interrupts in 8085 Microprocessor?

Explai n the working of LASER & LED in optical fibers?

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When will you use a latch and a flipflop in a sequential design?

Design a 1-bit fulladder using a decoder and 2 “or” gates?

You have a circuit operating at 20 MHz and 5 volt supply.

What would you do to reduce the power consumption in the circuit-reduce the operating frequency of 20Mhz or reduce the power supply of 5Volts and why? In a nmos transistor, how does the current flows from drain to source in saturation region when the channel is pinched off? In a SRAM circuit, how do you design the precharge and how do you size it? In a PLL, what elements (like XOR gates or Flipflops) can be used to design the phase detector? While synthesis of a design using synopsys design compiler, why do you specify input and output delays? What difference do you see in the timing reports for a propogated clock and an ideal clock? What is timeborrowing related to Static timing anaylsis in Primetime?  
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