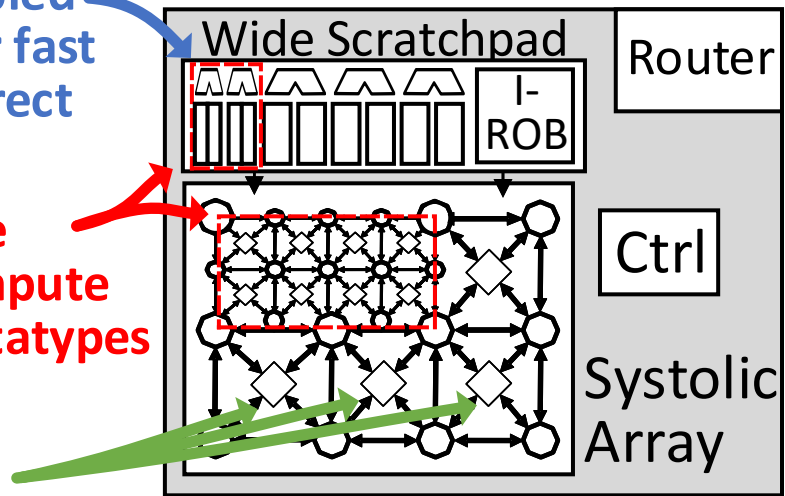


(a) Stereotypical Dense Accelerator Core

**Compute-Enabled  
Scratchpad for fast  
Alias-free indirect  
access**

**Decomposable  
mem/net/compute  
for flexible datatypes**

**Systolic array  
supporting  
stream-join  
control**



(b) SPU & its specialization for data-dependence