

## Centralized

- All executable Tasks  $\begin{cases} \text{Common Central Data Structure} \\ \text{Special process (Master + slaves)} \\ \text{subset of processes} \end{cases}$
- A process with no work takes portion of available work from the central data structure / Master
- New tasks are added to Central data structure / reported to the Master
- Easier to implement than distributed
- Limited Scalability

e.g. Quick Sort each row of an  $n \times n$  matrix

$$\begin{array}{cc} \swarrow & \searrow \\ O(n) & O(n \log n) \\ (?) & \end{array}$$

- ⇒ Equal number of rows to each process - imbalance
- ⇒ Maintain Central pool of indices of rows yet to be sorted
- Process when idle can pick an index from this pool (self-scheduling)

→ Good load balancing but bottlenecks in accessing shared work queue if each task does not have large enough computation

∴ To ease the bottleneck, assign more than one task at a time ⇒ chunk scheduling

But what if there is a load-imbalance due to large chunk sizes?

⇒ Reduce chunk size as the program progresses (linear, non-linear, ...)

## Distributed

→ Set of executable tasks are distributed among processes

→ Processes exchange tasks at run time to balance work

→ No bottleneck of centralized schemes

Its critical parameters are:

→ Pairing of sender/receiver processes

→ Initiation of work transfer - Sender or Receiver?

→ Amount of work transfer

→ Point of work transfer

To be effective in message passing platforms, size of tasks (computations) should be larger than size of data associated with it.

## Reduce Process Interaction Overheads

- Volume of data
- Frequency of Interaction
- Spatial & Temporal pattern of Interactions

### Techniques to Reduce Interaction Overheads:

- Promote use of recently fetched data (Temporal locality)
- Minimise the Overall volume of shared data that needs to be accessed by concurrent processes

$$\frac{2n^2}{p} \quad \text{Vs} \quad \frac{n^2}{p} + n^2$$

(1-d)                      (2-d)

- Locally Compute, store intermediate results, perform shared data access only to place final results

```
for (i = 0; i < N; i++)  
    dp += A[i] * B[i];
```

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```
for (i = id * N / p; i < (id+1) * N / p; i++)  
    ldp += A[i] * B[i];  
dp += ldp;
```

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- Minimise frequency of interactions as each interaction involves relatively high startup cost

- Restructure the algorithm such that shared data are accessed and used in large pieces  
Amortizes startup cost over multiple accesses  
(akin to large cache lines for spatial locality)  
Leads to fewer messages on Message Passing Systems

e.g. Sparse Matrix Vector Multiplication

P0 needs  $b[3]$ ,  $b[5]$  &  $b[7]$ , say  
 $\swarrow \quad \searrow \quad \downarrow$   
 $P_1 \quad \quad P_2$

Combine interaction with  $P_1$  to get  $b[3]$  &  $b[5]$   
instead of 2 separate interactions.

→ Minimize Contention

Why? Multiple Tasks accessing same resource  
concurrently

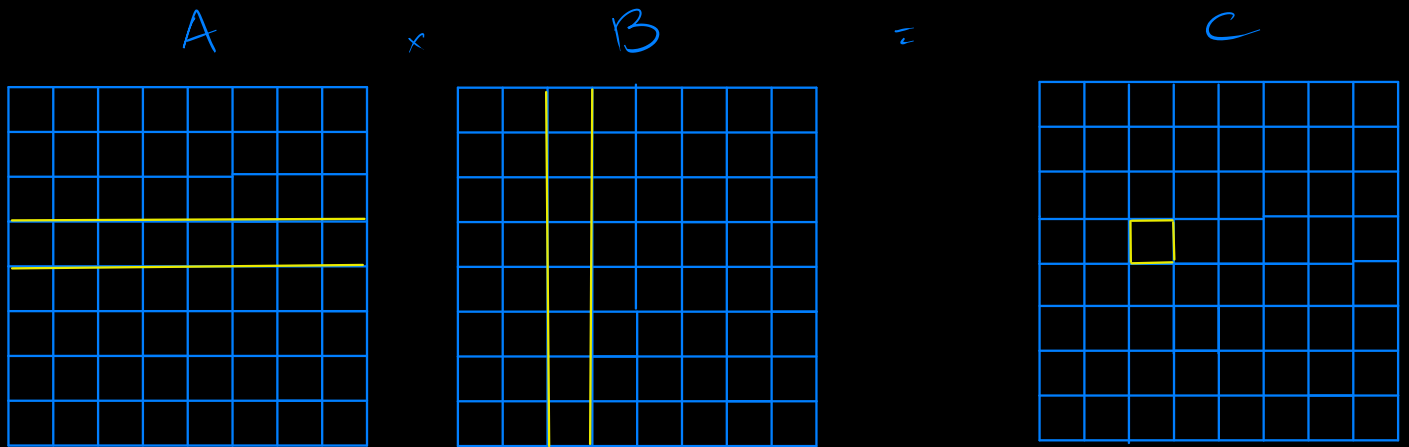
only one of the operations can proceed at a time,  
others are queued, proceed sequentially

e.g. Multiple simultaneous transmission of data over  
the same interconnect link

Multiple simultaneous access to same memory  
block

Multiple processes sending messages to the  
same process at the same time

Let  $p = 64$ ,  $c_{ij} = \sum_{k=0}^{\sqrt{p}-1} A_{ik} * B_{kj}$



⇒ Contention in NUMA shared address space  
& Message passing platforms

∴ Redesign the algorithm to access data in Contention-free patterns.

Process computing

$c_{0,0}$	$n$	queries:	$A_{0,0}$
$c_{0,1}$	$n$	:	$A_{0,1}$
$\vdots$			
$c_{0,7}$	$n$	:	$A_{0,7}$

Process Computing	$C_{5,0}$	accesses:	$A_{5,5}$
	$C_{5,1}$	:	$A_{5,6}$
	$C_{5,2}$	:	$A_{5,7}$
	$C_{5,3}$	:	$A_{5,0}$
	$C_{5,4}$	:	$A_{5,1}$
	$C_{5,5}$	:	$A_{5,2}$
	$C_{5,6}$	:	$A_{5,3}$
	$C_{5,7}$	:	$A_{5,4}$

[Centralized schemes in Dynamic Mapping are frequent source of contention for shared data structures  $\Rightarrow$  choose distributed mapping]

$\rightarrow$  Overlap Computations with Interactions

- Perform useful computations while waiting for shared data to arrive after initiating the interaction.
  - Initiate interaction early enough by identifying computations that can be performed before the interaction.
  - If one Task blocks on an interaction, and if there is another Task available to the same process, execute the other Task.

- Initiate advance work transfer if the Process can anticipate running out of current work.

- Requires support from the Programming Paradigm and underlying hardware

e.g. Non-Blocking Message Passing Primitives  
Prefetching hardware/compiler to advance loads

## → Replicating Data or Computations

- For Read-only shared data access, Replicate it in each process, thereby avoiding interaction

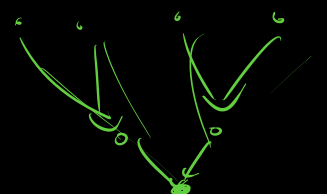
- But it increases memory requirement linearly with number of Concurrent processes.

- Instead of sharing intermediate results, each process can compute them.

- Trade-off between interaction overhead to Replicated Computation

## → Optimized Collective Operations

- Static and regular pattern of interaction between a group of tasks e.g. Broadcast  
Reduce



- Used for
  - Sharing data
  - Communication intensive Computations
  - Synchronization
- Optimized implementation of Collectives are available in libraries from parallel platform vendors
- In exceptional scenarios, develop one's own collective implementation.

## → Overlap Interactions with Other Interactions

- With hardware support, overlapping interactions between multiple pairs of processes can reduce effective volume of communication.

e.g. One to all broadcast from  $P_0$  to  $P_1, P_2$  &  $P_3$

Naive algorithm:

$$P_0 \xrightarrow{1} P_1 \xrightarrow{2} P_2 \xrightarrow{3} P_3 = 3 \text{ units of time}$$

Overlapped:

$$\begin{array}{cc} P_0 \xrightarrow{1} P_1 & P_0 \xrightarrow{\quad} P_2 \\ & P_1 \xrightarrow{2} P_3 \end{array} = 2 \text{ units of time}$$

What if 4 messages need to be sent?

$$\begin{array}{cc} P_0 \xrightarrow{m_0} P_1 & \begin{array}{l} P_0 \xrightarrow{m_0} P_2 \\ P_1 \xrightarrow{m_0} P_3 \end{array} \end{array} \quad \begin{array}{cc} P_0 \xrightarrow{m_1} P_1 & \begin{array}{l} P_0 \xrightarrow{m_1} P_2 \\ P_1 \xrightarrow{m_1} P_3 \end{array} \end{array}$$



