Address space

M
P
P
P

INTER CONNECT

=) Each data element must belong to one of the partitions

=) Needs explicit partitioning

=) Complexity in programming

=> But locality of rues for high performance

=) All interactions need Cooperation of two processes

=> Process that has the data must participate even if it has no logical connection

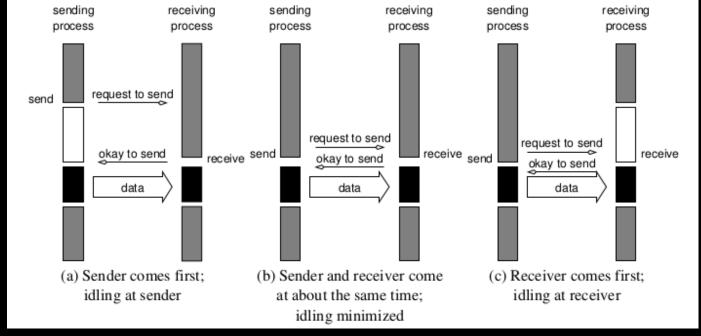
=) But programmer in fully aware of non-local interactions and hence design to minimize

I can be efficiently implemented on a nide variety of architectures

Building Blocks: Send & receive operations Send (void * Sendluf, int n, int dest)
receive (void * receive, int n, int Src) P₁ _____ int a = 25; int a = 100; Send (&a, 1, 1); Aeceive (&a, 1,0); print ("1.d", a); =) Sementics seem obvious but implementations Can differ what if Send (-) network interface that can DMA? What if Greeeve (...) Returns after indicating &a to the network interface that can DMA? => Need to know different modes of operation Blocking

Non-Buffered Buffered Non-Blocking Non-Buffered Bruffered

I - Blocking + Non-Buffered -) Send does not newn until marching greenve is posted -> Message sent & send returns of the communication operation - Involves a handshake between - No huffers used at sending receiving sending receiving sending receiving process process process process process process request to send send



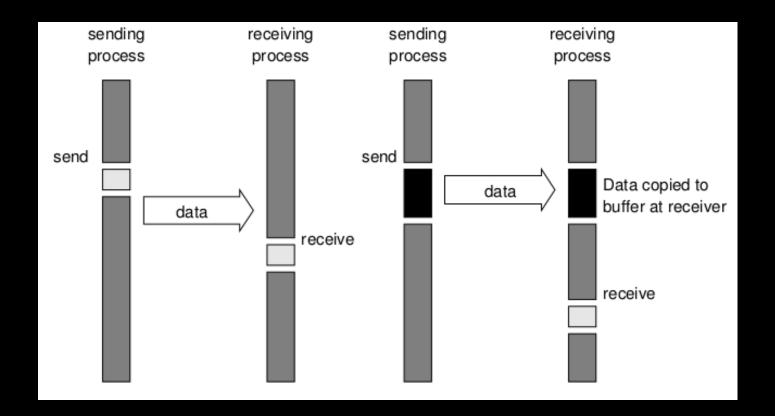
-> Process Idling

- Best Snited when send and receive are posted at troughly the Same time

int a = 100, b = 200; Send (&a, 1, 1); receive (&b, 1, 1); print ("/d/d", a,b); int a = 200, b = 100; Send (bb, 2, 0); reletve (ba, 1, 0); printf ("1-d1-d", a, b) I Blocking Bonffered -> Assume pre-allocated bruffers at sender and on receiver -> Send (--) Copies the data into the designated buffer and returns -> Sender can Continue - data change cannot influence Semantics of Communication -) Asynchronous Communication capability of handware - At receiver, data is copied into a hiffer -) When the destination process calls receive (---), huffer is cheeled and if available, lopies message to its hoffer -> Almost NO Idling -> If such hardware is not available on both sides, some overhead can be avvided by uning only one side buffers. -> On encountering a send (---), the sender interripts the receiver, both interact and message is deposited in a hiffer at the receiver

-> When receiver calls releive (...), message is

Copied from the buffer into the target location



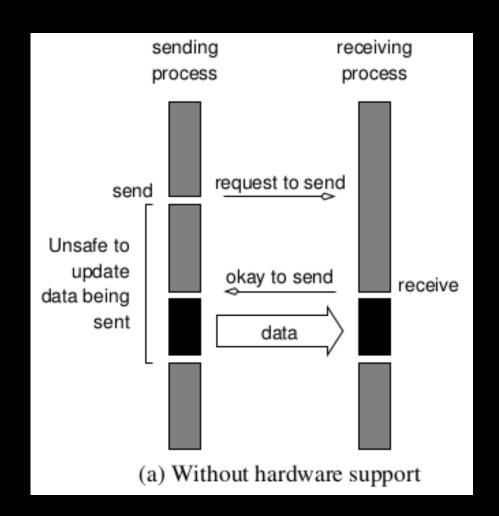
int a= 100, b= 200; bend (ba, 1, 1); receive (86, 1, 1); print ("/-d/-d", a,b);

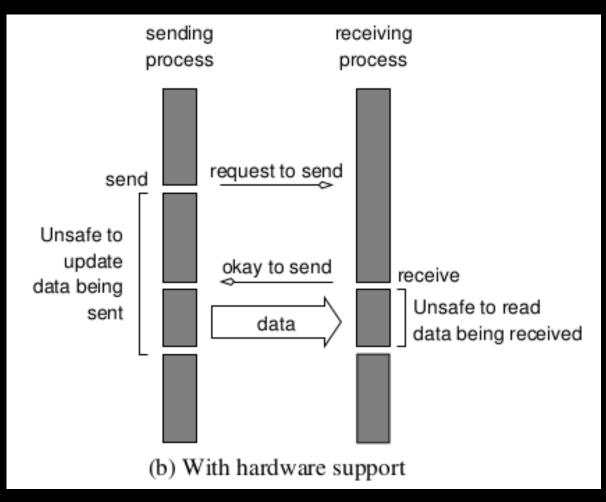
int a= 200, b= 100; Send (66, 1, 0); releive (&a, 1,0); prints (" 1.d 1.d", a,b);

int a= 100, b= 200; Leceive (La, 1, 1); send (86, 1, 1); print ("/-d/-d", a,b); print ("/-d/-d", a,b);

int a= 200, b= 100; receive (bb, 1, 0); Send (&a, 1, 0);

for (i=0; i < N; i++) for(i=0; i<N; i++) 2 receive (&a, 1, 0); Consume-data (&a); 2 produce deta (&a); Send (&a, 1, 1); What if P, reaches the loop much later? In Blocking mode, Semantic Correctness is guaranteed, but paid in the form of idling or buffer management. J III. Non-Blocking + Non-Buffered -> Programmer la ensure Dementic Concerners - Provides fast bend receive with little overhead Semantically Safe to do to -> Programmes to check status of completion - But program is free to Continue after a grick bend or receive





IV. Mon-Blocking + Buffered
- Sender initiates DMA and neturns
- Data unsafe till DMA operation is complete
- Receiver initiates transfer from sender's
Receiver institutes transfer from sender's huffer to the receiver's target location
-> Reduces the time during which the
data is unsafe
MPI: Message Parsing Interface - Standard bibrary for Message passing
- Portable message passing in confortran
- Defines syntan & semanties of the nontines
- Almost all vendors provide their implementat
- > 125 functions
- Used in academia & industry
- Widely popular