

Vighnesh V

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ABOUT ME

Currently pursuing Electronics for Bachelor's, with experience in Software Development, Computer Architecture and basic Front End/Design.

EDUCATION

- **National Institute of Technology Karnataka, Surathkal** Mangalore
Bachelor of Technology in Electronics and Communication Engineering; GPA: 7.31/10 Aug. 2015 – Present
- **St Joseph's Higher Secondary School** Trivandrum, Kerala
Computer Science; 98.25% July 2013 – March. 2015

EXPERIENCE

- **HEPIA** Geneva, Switzerland
Research Intern May 2018 - July 2018
 - **Digital System Design:** Designed a modular VHDL block to model an LSTM Cell optimized for minimum clock cycles and verified the simulation results. Also worked on a fast access BRAM configuration to make the cell scalable and to avoid bottlenecks.
- **Spericorn Technologies** Technopark, Trivandrum
Intern May 2016 - July 2016
 - **Front End:** Worked with Angular and jQuery frameworks.
 - **Back End:** Worked on a food delivery application project based on Laravel framework(PHP) with a MySQL database.

PROJECTS

- **IEEE NITK Official Website:** Worked on a jQuery based responsive website with a Jekyll blog for IEEE NITK.
- **ECTF Server:** MEAN Stack based scoring application for an internal ctf competition, deployable in the institute intranet. GitHub link for the repository : <https://github.com/viggi-v/ectf>
- **Smart Walking Stick:** Prototype for a smart walking stick using an array of ultrasonic sensors, with emphasis on the FPGA architecture and interfacing.
- **Vector Quantization:** A python implementation of Lossy Image Compression using Vector Quantization based on Otsu's thresholding algorithm, to study the effect of different sizes of the codebook for compression and overall loss in quality of the image.

SKILLS

- **Languages:** C, C++, Python(basic), JavaScript
- **Web Development:** MEAN and LAMP Stack, jQuery, Bootstrap
- **Embedded Systems:** Arduino and C6416 DSP kits.
- **Digital System Design:** Experience with VHDL, familiar with the workflow of Vivado.

POSITIONS OF RESPONSIBILITIES

- **Internship Co-ordinator:** Worked as the IC representing the class and assisting CDC during the recruitment processes. Currently re-elected as Placement Co-ordinator.
- **NITKRacing:** Working as Electronics Subsystem Head for NITKRacing, the college team participating in students edition of the F1 races in a National Level.

EXTRA CURRICULAR ACTIVITIES

- **Chess:** Participated in National School Games twice. FIDE rating 1529.
- **Technical Clubs:** Executive member of IEEE NITK and WebClub NITK, and worked in organizing mentorship programs and other events in the campus.