

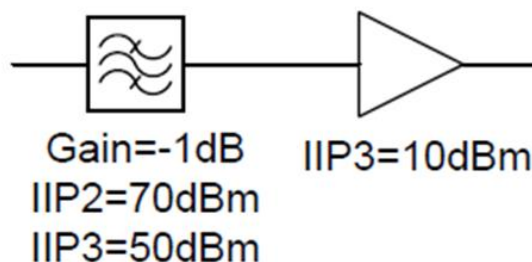
EE142 Problem Set 8

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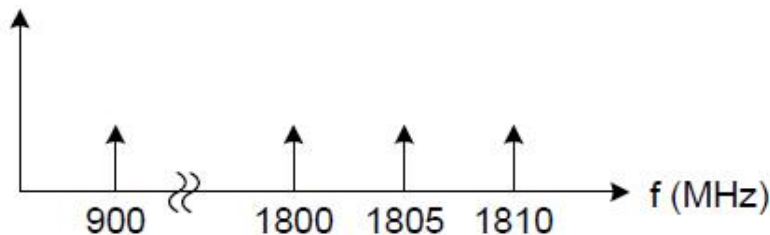
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1 System Analysis

A wireless receiver front-end is shown below:



We would like to receive the channel at 1800MHz, while we have additional channels at 900MHz, 1805MHz, and 1810MHz.



The minimum detectable signal at 1800MHz is -100dBm. The required signal to distortion ratio at the front-end output is 9dB.

- (a) If the signal power at the 1810MHz channel is -33dBm, what is the maximum allowed power at the 1805MHz channel?

We can find the cascaded IIP3 of the front-end:

$$\frac{1}{IIP3^2} = \frac{1}{IIP3_A^2} + \frac{a_1^2}{IIP3_B^2}$$

where the $IIP3$ terms in the above formula are voltages or currents.

Here are some useful equations to convert power to voltage, and convert power gain to voltage gain:

$$\text{Power Gain in dB} = 10 \log_{10}(\text{Power Gain in Linear Units})$$

$$\text{Power Gain in Linear Units} = 10^{\text{Power Gain in dB}/10}$$

$$\text{Voltage Gain} = \sqrt{\text{Power Gain in Linear Units}} \text{ assuming same } R_{in}, R_{out}$$

$$\text{Power in dBm} = 10 \log_{10}\left(\frac{\text{Power in Watts}}{10^{-3}}\right)$$

$$\text{Voltage Induced} = \sqrt{10^{-3} \cdot 10^{\text{Power in dBm}/10} \cdot 2R}$$

$$\text{Power Delivered in dBm} = 10 \cdot \log_{10}\left(\frac{V^2}{2R}/10^{-3}\right)$$

We will assume operation in a 50Ω environment.

$$IIP3_A = 50 \text{ dBm} \rightarrow VIIP3_A = 100 \text{ V}$$

$$IIP3_B = 10 \text{ dBm} \rightarrow VIIP3_B = 1 \text{ V}$$

$$a_1 = 0.8912 \text{ V/V}$$

$$VIIP3 = 1.1219 \text{ V}$$

$$IIP3 = 11 \text{ dBm}$$

As expected, the second stage's IIP3 dominates the cascaded IIP3. The power present at 1800MHz is caused by the intermodulation products of 1805MHz and 1810MHz:

$$V_{out,1800} = \frac{3a_3}{4} A_{1805}^2 \cdot A_{1810}$$

where A_x is the voltage at x MHz. We can find a_3 from IIP3:

$$IIP3 = \sqrt{\frac{4}{3} \frac{|a_1|}{|a_3|}}$$

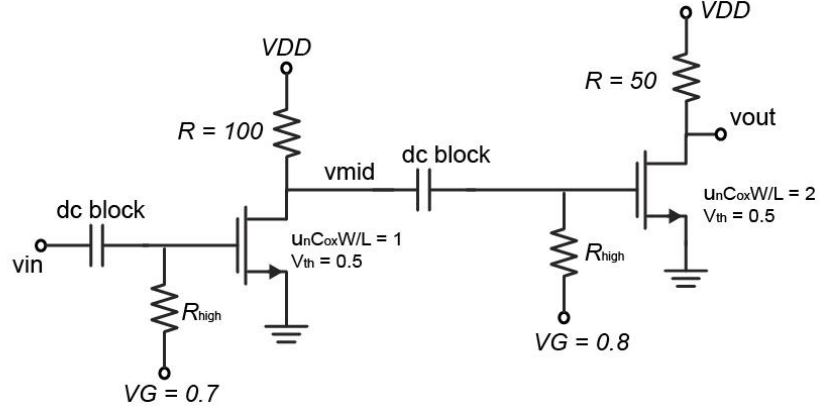
$$a_3 = 0.944$$

$$V_{out,1800} \leq V(-109\text{dBm}) \rightarrow P_{1805} \leq -26.5 \text{ dBm}$$

- (b) What is the required spec for the amplifier IIP2 if the signal power at the 900MHz channel can be as high as -30dBm?

2 Distortion Analysis

In this problem you will do distortion analysis for a frequency-independent amplifier.



- (a) For the above amplifier, derive a power series to express the small-signal output voltage v_{out} as a function of the small-signal input voltage v_{in} . Assume the transistors are long-channel devices.

We first derive the results for a single stage in general:

$$\begin{aligned}
 I_D &= \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{gs} - V_{th})^2 \\
 I_Q + i_{out} &= \frac{1}{2} \mu_n C_{ox} \frac{W}{L} (V_{GSQ} + v_{in} - V_{th})^2 \\
 I_Q + i_{out} &= 0.5 \mu_n C_{ox} W/L (V_{GSQ} - V_{th})^2 + \mu_n C_{ox} W/L (V_{GSQ} - V_{th}) v_{in} + 0.5 \mu_n C_{ox} W/L \cdot v_{in}^2 \\
 i_{out} &= \mu_n C_{ox} W/L (V_{GSQ} - V_{th}) v_{in} + 0.5 \mu_n C_{ox} W/L \cdot v_{in}^2 \\
 v_{out} &= i_{out} \cdot R
 \end{aligned}$$

Now consider the cascade:

$$\begin{aligned}
 v_{mid} &= (0.2v_{in} + 0.5v_{in}^2) \cdot 100 = 20v_{in} + 50v_{in}^2 \\
 v_{out} &= (0.6v_{mid} + 1v_{mid}^2) \cdot 50 \\
 v_{out} &= 600v_{in} + 21500v_{in}^2 + 100000v_{in}^3 + 125000v_{in}^4
 \end{aligned}$$

- (b) Calculate IIP3 for the first stage (v_{in} to v_{mid}), the second stage (v_{mid} to v_{out}), and the overall cascade two-stage amplifier.

$$\begin{aligned}
 VIIP3_A &= \sqrt{4/3|a_1/a_3|} = \infty \\
 VIIP3_B &= \infty \\
 VIIP3_{cascade} &= 0.089 \text{ V} \rightarrow -11 \text{ dBm}
 \end{aligned}$$

- (c) Find IIP2 for the first stage, the second stage, and the cascade two-stage amplifier.

$$\begin{aligned}
 VIIP2_A &= \frac{a_1}{a_2} = 0.4 \text{ V} \rightarrow 2.04 \text{ dBm} \\
 VIIP2_B &= 0.6 \text{ V} \rightarrow 5.56 \text{ dBm} \\
 VIIP2_{cascade} &= 0.0279 \text{ V} \rightarrow -21 \text{ dBm}
 \end{aligned}$$

- (d) Apply the cascade IIP3 and IIP2 formula introduced in the lecture. Explain if the results are different from that obtained via your analysis in part (b) and (c)

The cascade IIP2 formula yields the same result as derived explicitly, but the cascade IIP3 formula breaks down with infinite values and doesn't give the same answer as found in part (b).

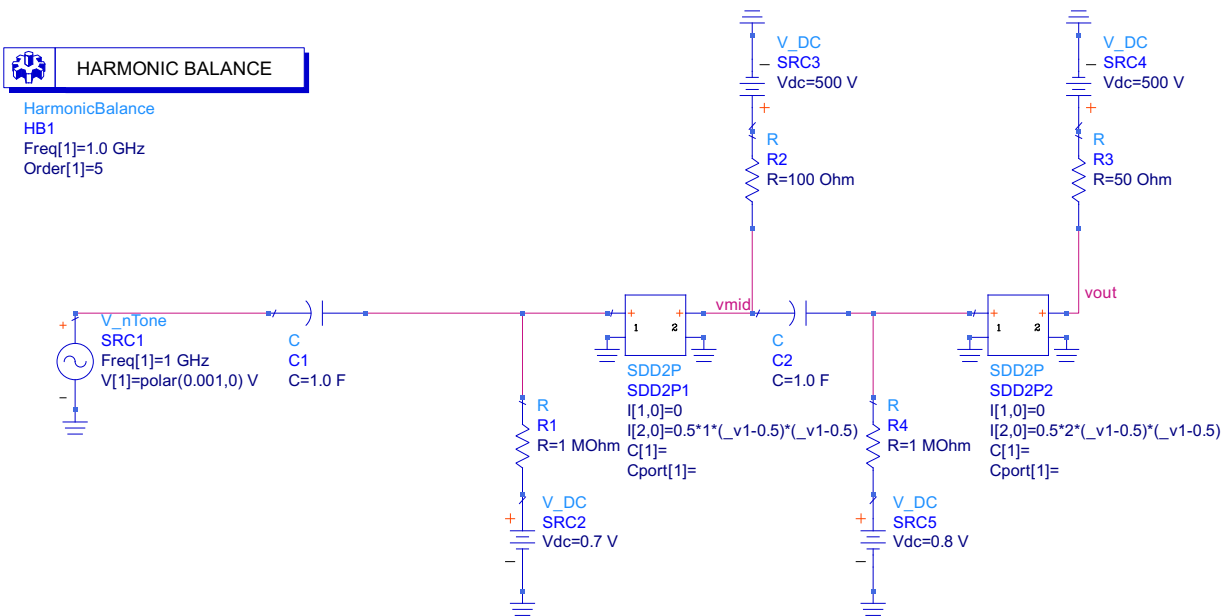
- (e) Calculate HD2 and HD3 of this two-stage amplifier.

$$HD_2 = \frac{1}{2} \frac{a_2}{a_1} S_{in} = 18$$

$$HD_3 = \frac{1}{4} \frac{a_3}{a_1} S_{in}^2 = 41.7$$

- (f) Use Harmonic Balance in ADS to simulate IIP2, IIP3, HD2, HD3 of this two-stage amplifier.

This is the setup in ADS:



We find from simulation that $a_1 = 600$, $a_2 = 18000$, and $a_3 = 100000$, which matches with the results derived by hand. From here, the IIP and HD numbers must also be the same.

3 Resonant Distortion Analysis

To achieve a higher output voltage swing, the two load resistors in the previous problem are replaced with 2 LC tanks, as illustrated. Now perform distortion analysis for a tuned amplifier. Keep in mind that in the passband of the amplifier (resonance), the memoryless assumption is valid.

