EE142 Problem Set 6

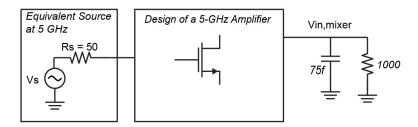
Vighnesh Iyer

October 14, 2017

1 Design of a 5-Ghz Linear Microwave Amplifier

Use the 32nm PTM HP NMOS model with:

V_D	0.9 V
V_G	0.6 V
L	32 nm
W_{tot}	$20~\mu\mathrm{m}$
$N_{fingers}$	5



1.1 Mixer Input Impedance

The mixer input is not 50Ω . What will be the problem if this mixer is used as a connector module like the ones sold by MiniCircuits?

The connector modules assume operation in a 50Ω environment. Since the mixer input is 1000Ω , any modules driving it will not be able to achieve the maximum possible power transfer to the mixer. Also, the reflections produced will distort the input waveform to the mixer.

1.2 Stability Factor

Plot the stability factor (k), of this device versus frequency. Is the device unconditionally stable at 1 Ghz? If not, find a (passive) device load impedance such that the real part of the device input impedance is negative at 1 Ghz.

The stability factor (k) provides a necessary condition for unconditional stability of a 2-port network. It enforces that the input admittance/impedance of the two-port doesn't have a negative real part.

EE142 Problem Set 6

$$Y_{in} = y_{11} - \frac{y_{12}y_{21}}{y_{22} + Y_L}$$
$$Y_{out} = y_{22} - \frac{y_{12}y_{21}}{y_{11} + Y_S}$$

For instability: $\Re(Y_{in}) < 0$ and $\Re(Y_{out}) < 0$

We set up a S-parameter simulation in ADS to calculate the stability factor versus frequency, using the provided transistor sizing and biasing.

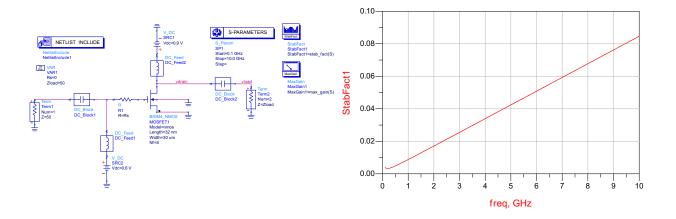
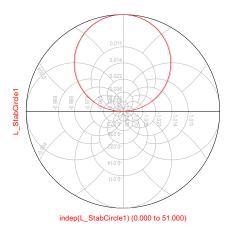


Figure 1: Stability Factor Testbench

Figure 2: Stability Factor vs. Frequency

The transistor is not unconditionally stable at 1 Ghz. To find a passive load impedance that gives a negative real impedance, we can use the stability analysis (circle) feature of ADS.



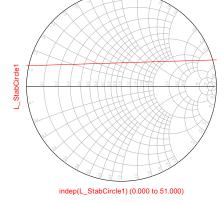


Figure 3: Stability Circle (not scaled to unit impedance circle)

Figure 4: Stability Circle (in unit impedance circle)

EE142 Problem Set 6

It can be seen that for sufficiently inductive loads, the transistor isn't stable. For instance a load of $Z_L = 50 + 1.2j$ will lead to a negative input impedance.

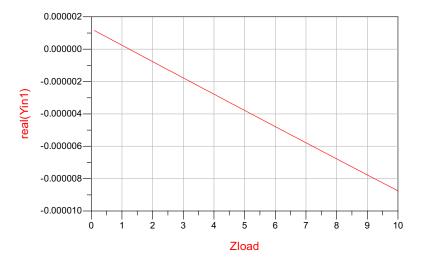


Figure 5: $Z_L = 50 + jZ_{load}$