Digital SoC Architecture BoomTile RocketTile Rocket Boom **PTW PTW** RoCC Accelerator Core Core L1D\$ L11\$ L11\$ L1D\$ MMIO Accelerator Tile Bus Tile Bus Constellation Network-on-Chip Interconnect **Control Bus** BootROM Serdes **UART GPIOs** Bank Bank PLIC CLINT Memory Bus Debug DRAM **DRAM** Chan. Chan.