Digital SoC Architecture RocketTile BoomTile Rocket Boom **PTW PTW** Accelerator Core Core RoCC L1D\$ L11\$ L1D\$ L11\$ MMIO Accelerator Tile Bus Tile Bus System Bus Front Bus Periphery Bus Control Bus BootROM Serdes **UART** GPIOs Bank Bank PLIC CLINT Memory Bus Debug DRAM **DRAM** Chan. Chan.