

07th Aug'2025**CERTIFICATE OF TRAINING**

This is to certify that **Mr. Vignesh Senthilkumar (T1768)** has undergone internship from **27th Jan'2025 to 27th Jul'2025** at **Tessolve Semiconductor Pvt Ltd**, Electronic City, Bangalore.

During the internship, he was trained on below topics:

1. Basic understanding of Bench characterization and bring-up activities.
2. To read schematics and gained hands-on experience with lab instruments such as Power Supply, SMU, Oscilloscope, DMM, Function Generator, and Electronic Load.
3. Developed Python libraries for instrument control using OOPS concepts.
4. Automated test cases and reporting based on validation plans using Python.
5. Performed mixed-signal IP (ADC/DAC) characterization across PVT corners.
6. Optimized test case programs, reducing overall Test cycle time from 13 hours to 8 hours.
7. Learning Electrical validation of high-speed interfaces including PCIe, DDR, and USB. and Instruments including Infinium UXR Scope, BERT, and DCA.
8. Getting Familiar with Compliance testing and Keysight Automated Compliance Test Apps.

Vignesh Senthilkumar has shown enthusiasm during the whole period of his internship and keen interest in acquiring knowledge, which will be helpful for his academic and future career prospects in Engineering. We wish him success in the future.

Thanking you,

For Tessolve Semiconductor Pvt. Ltd.,



Ms. Savitha Ramaswamy Reddy
Associate Director - HR