

[1] Power Management Data Line **ESD Protection** Differential pairs 90 Ohm Differential pairs 90 Ohm USB-C Power Delivery Protection U201 USC-C Receptacle USB_OTG_D-+VBUSc +V_USB uC_OTG_D-+VBUSc J201 USB_C_Receptacle_USB2.0_16P Q201 STL6N3LLH6 VBUS= USB_CC1c USB_CC2c USB_OTG_D+ uC_OTG_D+ S_1 USB_OTG_D-USBLC6-2P6 USB_OTG_D-C201 USB_OTG_D+ 100n USB_OTG_D+ eFUSE Protection SBU1= A8 × SBU2= × PD_GATE +5٧ +5V_EXT +V_USB +5٧ D201 U203 U202 R202 STEF05SGR FSE_I_LIM C202 **♦** 120R SOURCE BAT30KFILM 10k 100n EN/FAULT VCC_1 I_LIM ~ 2.1A I_SHORT ~ 0.55A VCC DV/DT PD_VBUS_CTRL SOURCE_2 VBUS_CTRL FLT, SOURCE_1 10 D202 DB/ R203 2k7 CC1 USB_CC1c C203 C204 CC1C CC2 USB_CC2c 9 CC2C GND EP 13 Only connect +5V_EXT or +V_USB TCPP01-M12 **EXT 5V Connector Mounting Holes Fiducial** +5V_EXT H201 H202 H203 H204 FM201 FM202 FM203 0 0 **Test Points** +V_USB +51 Sheet: /USB/

TP201

TP202

File: usb.kicad sch

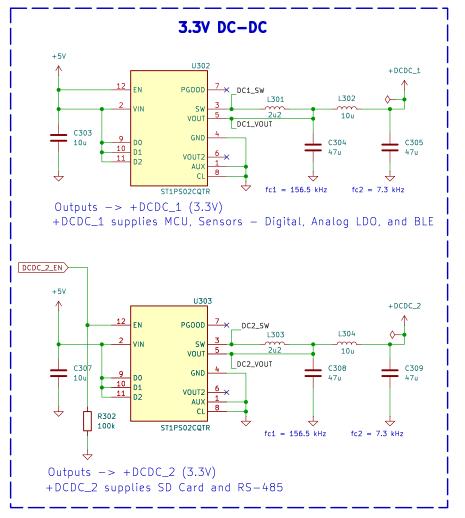
KiCad E.D.A. 8.0.5

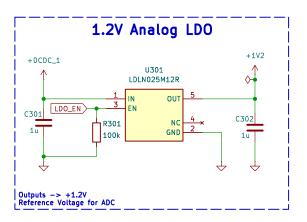
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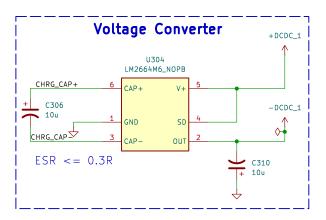
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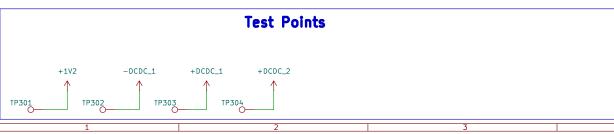
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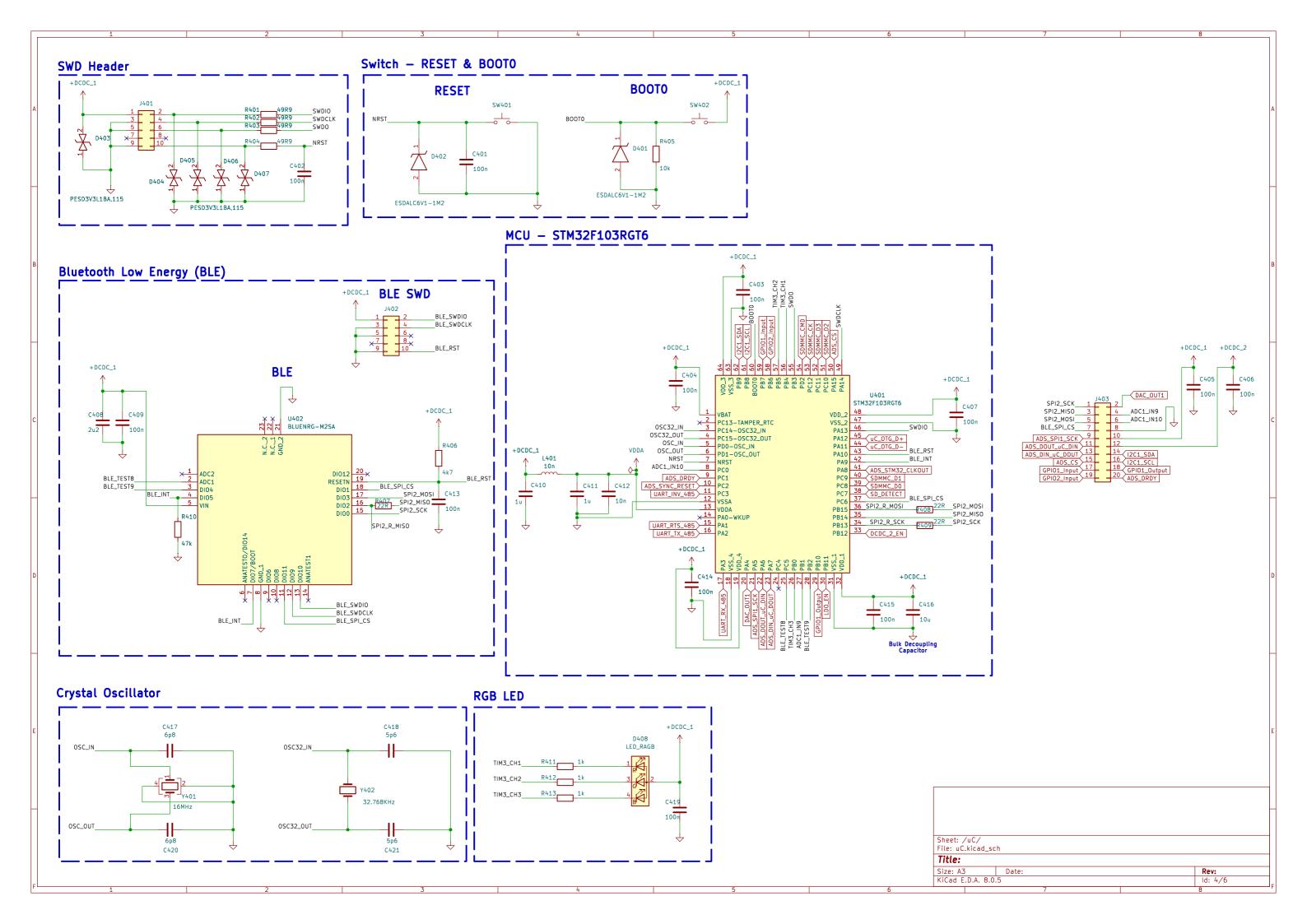
[1] Power Management

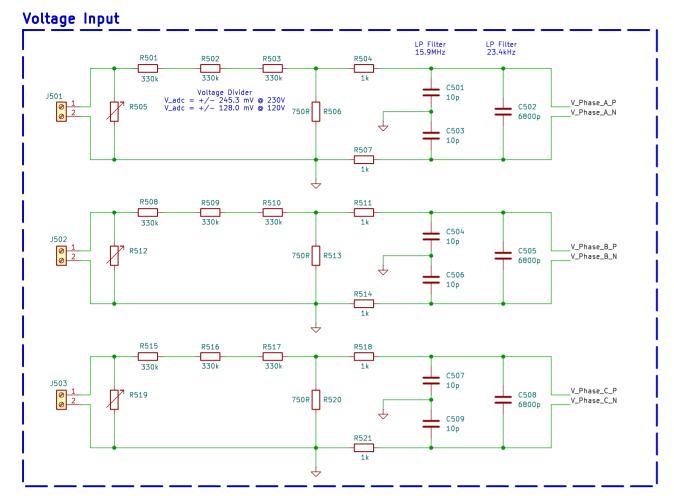












CT(0.3333 Vac) Input For single-ended inputs, either AINXP and AINXN are connected to ground. Refer to the forum e2e.ti.com +DCDC_1 -DCDC_1 LP Filter 11.7kHz CT_PhaseA_Amp_OUT C514 10p C515 6800p +DCDC_1 -DCDC_1 CT_PhaseB_Amp_OUT J505 C517 6800p +DCDC_1 -DCDC_1 CT_PhaseC_Amp_OUT J506 CT_PhaseC_IN

+DCDC_1 U501 ADS131M08IRSNR 23 CLKIN_ADS_T ADS_STM32_CLKOUT V_Phase_A_P_ V_Phase_A_N_ XTAL2 22 X C513 +DCDC_1 +DCDC_1 DIN 21 DIN_ADS_T R523 33R ADS_DIN_uC_DOUT +DCDC_1 DOUT 20 DOUT_ADS_T R524 ADS_DOUT_uC_DIN SCLK 19 SCK_ADS_T R526 33R ADS_SPI1_SCK I_Phase_A_P_ DRDY_N 18 DRDY_ADS_T R529 33R 100k R530 I_Phase_B_P_ ADS_CS SYNC_RESET_N 16 SYNC_RESET_ADS_T R531 33R I_Phase_C_P_

Synchronous ADC

Manufacturing Innovation Network Laboratory (MINLab)

Sheet: /ADC/
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Size: A2 Date: Rev: v1.0.0

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