

2 Verification Approaches

Automation





Exhaustive Symbolic Execution (E**SE)**
(e.g. KLEE, S2E)

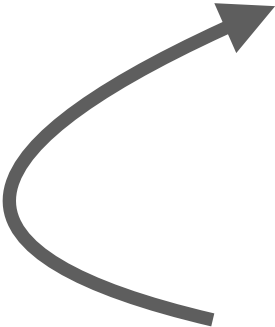


Power

Theorem Proving (TP)
(as seen in seL4, Ironclad, HAACL^{*})

NF logic

Data structure library





E

S

E

T

P



Verification Steps









