

**TIME TABLE ( AUG -DEC 2016)**

F-02(MSIT-FAC-PA-02)

**SEM- 3rd, #305**

**BRANCH- EEE**

HOURS	1	2	3	4		5	6
PERIODS/ DAYS	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30-1:30	1:30-2:30
MON	CnS Lab (G1), #502, RK S.C Lab(G2) ,# 306(B), SG-II		AE (L),DD	AM (L) ,# 301, DN	LUNCH TIME	CnS (L) , MR	DS (T) G2, #302
TUES	DS (L) ,#305 , PR	EM (L), JJ	DS LAB , G2, #LG-8, MS AE Lab, G1, # 02, AP			AM (L) , #301	CnS (G2) (T) ,# 302 EM (G1) (T) ,# 305
WED	DS (T) G1, #302, PR	EEM (L) , NF	DS LAB , G1, #LG-8, MS AE Lab, G2, # 02, AP			CnS (G2)Lab, #502, RK S.C Lab(G1) ,# 306(B), SG-II	
THU	EEM (L)	CnS (L), MR	DS (L), PR	AE (L) ,DD		AM (L)	CnS (G1) (T) ,# 301 EM (G2) (T) ,# 302
FRI	E-M/C Lab (G2), # 506, JJ		AE (L) ,# 302	EM (L) , # 302		EEM (L) , NF,#301	AE (T) G1 # 202
	XX	AM (T) G1,D N #301					
SAT	E-M/C Lab (G1), # 506, JJ		CnS (L) , MR	DS (L), PR		EM (L),JJ	AE (T) G2 # 211
	XX	AM (T) G2, DN #302					

SUBJECT ABBREVIATION	DS	AE	EM/C	CnS	AE	S.C	AM	EEM
FULL NAME	Data structure	Analog Electronics	Electrical Machines	Circuits & Systems	Analog Electronics Lab	Scientific Computing	Applied Maths	Electrical Engg.Materi als
FACULTY NAME ABBREVIAT	MS ,PR	DD	JJ	RK , MR	AP	SG-II	DN	NF
FULL NAME	Meena Siwach Preeti Rathee	Deepti Deshwal	Jyoti Jain	Rakhi Kamra Mamta Rani	Anshul Pareekh	Sonia Goel	Deeba Naqvi	Ms.Renu

TIME TABLE ( AUG -DEC 2016)

F-02(MSIT-FAC-PA-02)

SEM- 5th , #301

BRANCH- EEE

HOURS	1	2	3	4	12:00-12:30          <b>LUNCH TIME</b>	5	6
PERIODS/ DAYS	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00		12:30-1:30	1:30-2:30
MON	STLD (G1), # 301 PE (G2) , 305	STLD (L),PS	CSC (L),PT	IM (L) # 305,VR		S&T (L)	XX
TUES	PElab(G2), # 506, NG S&TLab (G1) # 306, SG-II		STLD (L) , PS	CSC (L) , PT		IM (L) # 305	XX
WED	PELab (G1), # 506, NG S&TLab (G2) # 306, SG-II		PE (L) , NG	S&T (L) ,KPC		CSP LAB (G2), # 01, NF	
THU	STLD (G2), #301	CSP (L) , NF	PE (L) , NG	CSC (L)		CSC (G1), DD, #301	S&T (G1), #301,KPC
FRI	STLDLab (G1), #202, PS CSCLab (G2), #102 , PT		S&T (L)	PE (L)		STLD (L)	XX
SAT	CSP LAB (G1), # 01, NF		STLDlab (G2), #202, PS CSCLab (G1), #102 , PT			IM (L) # 305	S&T (G2), # 301 PE (G1), # 305
	XX	CSC (G2), DD, #301				CSP (L) , NF	XX

SUBJECT ABBREVIATION	STLD	CSC	CSP	S&T	IM	PE
FULL NAME	Switching Theory Logic Design	Communication Systems	Communication Skills for Professionals	Sensors and Transducers	Industrial Management	Power Electronics
FACULTY NAME ABBREVIATION	PS	PT	NF	KPC	VR	NG
FULL NAME	Preeti Sehrawat	Prinkle Tehlan	New Faculty	Prof.K.P.Chaudhary	Vinita Rohilla	Nidhi Gupta

# TIME TABLE ( AUG -DEC 2016)

F-02(MSIT-FAC-PA-02)

SEM- 7th, #302

BRANCH- EEE

HOURS	1	2	3	4		5	6
PERIODS/ DAYS	8:00-9:00	9:00-10:00	10:00-11:00	11:00-12:00	12:00-12:30	12:30-1:30	1:30-2:30
MON	ACS (G1), # 302, ED (G2 ) #306(A)	P.D (L), SG-I	EDLab (G1), SM,# 306 (A) ACSLab (G2), MT, # 306(B)		LUNCH TIME	ED (L),SM	XX
TUES	Opto Lab (G2) #104, SS Seminar (G1), SG-I, # 405		EHVAC (L),SG-2	Opto (L), # 305,SS		P.D (L), SG-I	XX
WED	XX	P.D (L), SG-I	ED (L),SM	ACS (L),MT		Opto (L), # 307,SS	XX
THU	EDLab(G2), SM,# 306 (A) ACSLab (G1), MT, # 306(B)		EHVAC (L)	ACS (L)		ED (L)	xx
FRI	ACS (L)	Opto (L), # 305	Opto Lab (G1) #104, SS Seminar (G2), SG-I, # 305			EHVAC (L)	ACS (G2), #302 ED (G1),#306(B)
SAT	Project						

SUBJECT ABBREVIATION	EHVAC	PD	ACS	ED	Opto
FULL NAME	Extra High Voltage AC Transmission	power distribution	Advanced Control System	Electrical Drives	Optoelectronics
FACULTY NAME ABBREVIATION	SG-II	SG-I	MT	SM	SS
FULL NAME	Sonia Goel	Sunil Gupta	Meena Tushir	Shilpam Malik	Sapna Singh