After constructing the circuit, I had to verify whether the power supplies are connected correctly. For that there were inbuilt test points on EVM board to check the connectivity. Those test points voltages were available in the EVM manual. So, I measured the voltage values of the test points using the multimeter and verified the circuit I constructed was powered correctly.

As for the programming, I had to follow the ADS1298 microcontroller datasheet and get the information. Since that was a SPI communication, I had to write data on ADS1298 microcontroller registers. When writing data on a ADS1298 microcontroller register, WREG command should be sent first.

WREG command is a two-byte opcode. First byte is assigned for the command opcode and the registered address. Second byte is to specify the number of registers that need to be written. So, the "number of registers to write -1" value was assigned to the second byte.

ADS1298 microcontroller datasheet mentions that throughout the communication, the inverse chip selection pin should be kept logic low. So, to write data on the register, at first, I made the inverse chip selection pin logic low. Next, I send two opcodes as mentioned in the datasheet before sending data. OPCODE1 is written in the following format.

OPCODE1 => 010r rrrr

Here r rrrr refers to the starting register address. Then the OPCODE2 is written in the following format.

OPCODE2 => 000n nnnn

Here n nnnn refers to the "number of registers to write -1" value.

After the OPCODEs are sent, I sent the register input data in the Most Significant Bit (MSB) first format. My supervisor advised me to send data for CONFIG1, CONFIG2 and CONFIG3 at first. CONFIG1, CONFIG2 and CONFIG3 register bit assignments were also available in the datasheet. When sending data for three registers I can use WREG command one time to send data for all three registers. For that I can change the OPCODE2 value to 00010.

$$\begin{aligned}\text{number of registers to write - 1} &= 3\text{-}1\\ &= 2_{10}\\ &= 00010_2\end{aligned}$$

Instead of doing it separately, by assigning 00000 to the OPCODE2, it is possible to send data to each register separately.

$$\begin{aligned} \text{number of registers to write - 1} &= 1-1 \\ &= 0_{10} \\ &= 00000_2 \end{aligned}$$

So, I used that method for my program in order to get a better understanding for myself. Following Arduino code lines shows that method.

```
SPI.transfer(0b01000001);
delayMicroseconds(5);
SPI.transfer(0b00000000);
delayMicroseconds(5);
int a = SPI.transfer(0b10100000);
delayMicroseconds(10);
```

As shown in above, I firstly transferred the OPCODE1 in binary and have a 5 microseconds delay. Then I transferred the OPCODE2 in binary and have another 5 microseconds delay. Next, I transferred the necessary data in binary.

According to the above part of the code it is clear that I have sent the WREG command because the first binary value I sent **010**00001 is in the format of OPCODE1 in WREG register (**010**r rrrr) and the second binary value I sent 00000000 is in the format of OPCODE2 in WREG register (010n nnnn).

Here the last five bits of the OPCODE1 (00001) refers to the starting register address. Then the last five bits of the OPCODE2 (00000) refers to the "number of registers to write – 1" value, which I was shown earlier in the calculations

After sending opcodes, I sent the data as per the requirement. Since I only sent data for three registers, and since I was going to send separate OPCODE2 bits for the three registers, I wrote three sets of programs which is in similar format as thee above Arduino program lines. Then I initialized the required other settings in the program.

Further there was a Power-Up flowchart in the datasheet which indicates the sequence of programming. According to that diagram, I completed the rest of my SPI communication Arduino program and uploaded it to the Arduino MEGA board. Then I was able to observe the output using the Arduino Serial Monitor as shown in figure 2.10. The complete register level program I created is attached in the appendices.

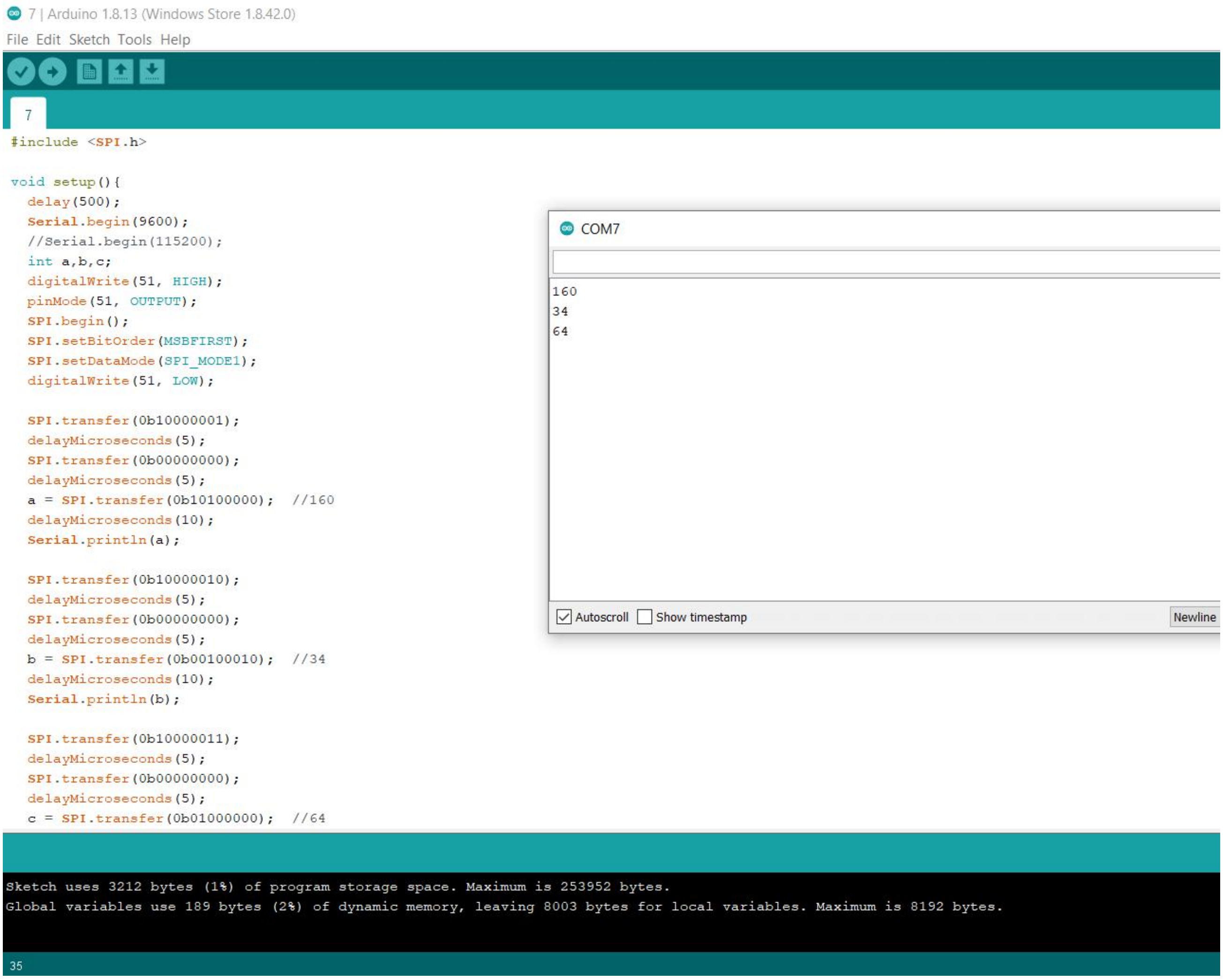

Figure 2.10 SPI Communication Program Output