

V M S R KRISHNA BABU

IIT PATNA

BIHTA,PATNA-801103

Email-id : bathina.ee13@iitp.ac.inMobile No.: **08292347023****ACADEMIC DETAILS**

Examination	University	Institute	Year	CPI/%
Under Graduate Specialization:	<i>Electrical Engineering</i>			
Under Graduation(upto 4 th sem)	IIT Patna	IIT Patna	2015	7.74
Intermediate/+2	Board of Intermediate(A.p)	srichaitanya Jr College	2013	98.00
Matriculation	SSC	Goutham Concept School	2011	92.16

FIELDS OF INTEREST

- Microprocessor, VLSI, Verilog Programming, Analog and Digital Design

TECHNICAL SKILLS

- **Languages:** C, Verilog HDL, JAVA
- **Tools:** MATLAB, XLINX, PSPICE, Arduino
- Analog and Digital circuit design

KEY COURSES UNDERTAKEN

- **INTRODUCTION TO VLSI**
- **Analog Design**
- **Digital Design**
- **Semiconductor devices and properties**

POSITIONS OF RESPONSIBILITY

- Sub-Coordinator in ECO AND WELFARE Committee at Techno-Cultural fest Anwesha'15(also fetched a handsome sponsorship amount for the same committee)

ACHIEVEMENT and ACTIVITIES

- Selected for kvpy scholarship in 2013
- Very active participant in technical competitions held in our college

STRENGTHS

- Good Communication skills
- Quick learner
- Positive attitude and can handle stress

INTERESTS AND HOBBIES

- Current affairs and politics
- Debating and writing short stories
- Solving Puzzles