Madari Kurva Pravallika

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Education

Raghunatha Model High School - 2010

Hyderabad, Telangana, India Matriculation, Senior Secondary PERCENTAGE: 95%

Sri Chaitanya Junior Kalasala, Hyderabad - 2012

Hyderabad, Telangana, India Matriculation, Higher Secondary PERCENTAGE: 97%

Indian Institute of Technology Patna – 2016*
Patna, Bihar, India
Electrical Engineering, Bachelor of Technology
CPI: 7.67

Academic Projects

Summer Research Intern - (May, 2015 - July, 2015)

Defence Research and Development Organisation, Hyderabad, Telangana (India)
Analytical study of a project titled "A study on Inertial Navigation System requirements for Ballistic Missiles" has been carried out during the Internship period.

Fog Removal in Images - (December, 2014 - April, 2015)

Indian Institute of Technology Patna, Patna, Bihar (India)
Various techniques to defog an image are studied and also implemented one on MATLAB.

Seizure Detection in EEG- (July, 2014 - April, 2015)

Indian Institute of Technology Patna, Patna, Bihar (India)
Analytical study has been carried out on different feature extraction techniques.

Generation and Synchronous Detection of BPSK Modulation- (November, 2014)

Indian Institute of Technology Patna, Patna, Bihar (India)
Schematic and PCB designing was done using a software called Diptrace.

Technical Skills

- Programming Languages: C, MATLAB, Verilog
- Electronics and Electrical: Digital circuit design, Analog circuit design, FPGA implementation and testing.

Positions of Responsibility

Secretary, Eco and Welfare affairs - (July, 2014 - March, 2015)
 Student Gymkhana, Indian Institute of Technology Patna, Patna, Bihar (India)
 Secretary for all the Eco and welfare related issues of Girls Hostel.

Extra-Curricular Activities

- Volunteer of "10th Society" a team of 15 who helped the poor children around Patna city -(January, 2013).
- Volunteer of Eco Team, Anwesha (Techno-cultural fest), Patna, India (Bihar) (February, 2013).
- Organizer of several events at Anwesha (February, 2014).