Abhilash Kumar

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Education

- **Indian Institute of Technology Patna**
  Bachelor of Technology, Department of Electrical Engineering
  CPI: 7.67/10
- **Kendriya Vidyalaya No.1, Darbhanga, Bihar**
  Class XII, Central Board of Secondary Education
  Performance: 92.2%
- **Kendriya Vidyalaya No.1, Darbhanga, Bihar**
  Class X, Central Board of Secondary Education
  Performance: 9.2(CGPA)

Internship and Projects

- **Vocational training** at **National Thermal Power Corporation Limited (NTPC), Barh**   **Summer'14**
  Process for generating Electricity, **Company: NTPC, Barh**
  - Undergone vocational training regarding generating and transmitting electricity.
  - Familiarized with the cause of power loss and the ways to dwindle it.
- **Project title: Moving Object Detection**   **January'15- May'15**
  IIT PATNA, Guide: Dr. Maheshkumar H. Kolekar, Asst. Professor, IIT Patna
  - Familiarized with the image and video processing using MATLAB
  - Designing algorithm for detecting a specific object based on finding point correspondences between the reference and the target image.
- **Project title: Position Control System using Servo Motor**   **January'15- May'15**
  IIT PATNA, Guide: Dr. Shovan Bhaumik, Asst. Professor, IIT Patna
  - Designing Position Control system which takes the input position using input potentiometer and changes the position of output potentiometer accordingly.
- **Project title: Smart Parking System**   **Summer'15**
  Summer Internship at **Masamb Electronics Systems, Noida, Uttar Pradesh**
  - Designing Parking System (in embedded system) to detect arrived vehicles and the available slots for parking.
  - Developing an Android App for the display of available parking slots to the user.

Technical Proficiency

- **Programming languages**: C, C++, Java
- **Android App Development**.
- **Software packages**: Verilog, Matlab, Simulink, PLC Programming.
- **Microprocessor**: Intel 8085
- **Microcontroller**: PIC18F4550.

Relevant courses and labs undertaken

- **Courses**: Object Oriented Programming, Data Structures, Semiconductor Devices and Circuits, Digital Circuits and Microprocessors, Signal System and Network, Analog Integrated Circuits, Principles of Communication, Introduction to VLSI Design, Digital System Processing, Control Systems, Electronic Instrumentation, Advanced Control System, Embedded Systems, Electromagnetic Theory, Power System Operation and Control, Communication Networks.
- **Laboratories**: Digital Circuits lab, Analog Circuits Lab, Verilog Lab, Communication Lab, Instrumentation and Control Lab, Embedded Systems Lab, Digital Signal Processing Lab.

Awards and Achievements

- Secured **10th position** in the district in Class XII examination.
- **100 Marks** in **Physics** subject of **Class XII**, awarded with **Merit Certificate (top 0.1%)** by **CBSE**.
- Secured **3rd position** in **INTACH Heritage Quiz 2014-2015** aired on **DD Bharati**.

Extra-Curricular Activities

- **Captaincy** and **Guidance** of the **Volleyball Team of IIT Patna**.
- **Student Coordinator** of **Training and Placement Cell, IIT Patna**.
- Taken part in **48th**, **49th** and **50th Inter- IIT Sports Meet** in **Volleyball Game**.
- Active Participant in **Quiz Competitions**.