# DEVESH DEVENDRA

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#### **EDUCATION**

Indian Institute of Technology, Patna Expected
Bachelor of Technology, Major: Electrical Engineering CPI: 8.12 Apr 2017
Loyola High School, Patna 2012

Senior Secondary School Examination (CBSE) Marks Secured: 88.6%

Loyola High School, Patna 2010

High School Examination (CBSE) CGPA Secured: 9.8

#### **PROJECTS UNDERTAKEN**

### Video Surveillance using MATLAB

Worked on Video Processing for the purpose of Surveillance using the software MATLAB. It
involves tracking the moving objects using various algorithms and also used in live video
Acquisition.

### Maglev Train Model (Undergoing)

Involves the building of a maglev model, using the technology used worldwide for locomotion of trains

### **POSITION OF RESPONSIBILITY**

- Training and placement Coordinator of Electrical Engineering 3<sup>rd</sup> year.
- Class Representative in School in Class 12<sup>th</sup>.

#### **AREAS OF INTEREST**

• Digital Electronics

Electrical Machines

VLSI

## **TECHNICAL KNOWLEDGE**

- TECHNICAL SOFTWARES: A working knowledge of MATLAB, Xilinx ISE Design Suite, Autocad, NetBeans, Blue J.
- Programming Languages: Knowledge of C, JAVA, Verilog, Arduino, MySql, HTML Basic Design.
- Knowledge of Algorithm and Data Structures.

### **EXTRA-CURRICULAR ACHIEVEMENTS**

- Created a ball picking robot using DC Motors and DPDT Switches.
- Created a line following robot using arduino board and light sensors.
- Taken part in various activities during college fest and in school fests.

#### **COURSES UNDERTAKEN**

Signals, Systems and Networks

Principles of Communication(+lab)

Electrical Machines (+lab)

 Digital Design and Microprocessor (+lab)

 Object Oriented Programming and Data Structures Control Systems(+lab)

Analog Electronics(+lab)

Probability And Random Processes

Electronic Instrumentation(+lab)

Power Systems

VLSI Design(+lab)

Numerical Methods