RAGHAV RASTOGI

Dept. of Electrical Engineering Indian Institute of Technology Patna Undergraduate Final Year (B.Tech)

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Education			
Examination/Degree	Year	Board/University	% Marks/CGPA
B.Tech.	2016(expected)	IIT Patna	8.64* (on scale of 10)
Intermediate(12")	2012	Board of Intermediate Education, A.P	96.3%
Matriculation(10")	2010	I.C.S.E, New Delhi	93.7%

* upto 6th Semester.

Work Experience

Research Intern at Robert Bosch Engineering and Business Solutions Ltd.(RBEI), Bangalore.

[May'15-July'15]

- Worked on "Ultrasonic Ranging System Development" involving sensor interfacing, hardware-board bring-up, data analysis and visualization, under the mentorship of Mr. Kumar Saurabh of Bosch Research and Technology Center.
- Involved in programming the sensor, conducted ultrasound profiling experiments of various real and lab-simulated situations and environment and performed data analysis on the collected readings.

Summer Trainee at Navanidhi Electronics Pvt. Ltd, Hyderabad.

[May'14-July'14]

- Analysis of the firm's ongoing projects in the field of RF communication, including the chief product lines: Power Amplifiers (Air-borne Power Amplifier and Signal Jammers), Filters (L-band DTF and V/U HF DTF) and Antennas.
- Gained hands-on experience by conducting physical, environmental and electrical testing of the products.

Academic Projects

Radio Frequency Energy Harvesting

[Jan'15-April'15]

- Designed, simulated and fabricated an RF energy harvesting circuit, as a proof of concept, to produce considerable DC voltage as output for practical usage, under the supervision of Dr. Yatendra Kumar Singh at IIT Patna.
- Achieved conversion of RF waves of frequencies around 700MHz, to produce DC voltages upto 180 mV.

Infrared Music Transmitter and Receiver

[Oct'13]

- Developed a working model of a music transmitter and receiver using the Infrared waves.
- Achieved wireless transmission of music signal generated from a melody generator, to a receiver circuit with speaker .

Programming Knowledge and Other Skills

- Fair knowledge of JAVA ,C, Python, R, Assembly Language and Verilog HDL programming languages,
- Workable skills on software and development environments like MATLAB, Simulink, R-Studio, Raspbian OS, AutoCAD, ADS, Xilinx ISE, Pyxis Schematic, Synopsys Design Vision, MPLab and MultiSim.
- Experience with Network Analyzers, Signal Generators, DSO, Test-jigs and Spectrum analyzers and Raspberry Pi.
- Creditable experimental temperament with extensive laboratory work on VLSI & Embedded Systems, Communication systems, Analog and Digital circuits, Control and instrumentation.
- Good analytic, logic, reasoning, presentation and communication skills with significant experience working in teams.

Awards, Scholarships and Scholastic Achievements

- Statement of Accomplishment with Distinction from Stanford University on successful completion of online theory and laboratory course on "GPS: An Introduction to Satellite Navigation." [Oct-Nov'14]
- Participated in-
 - Workshop by IETE and Govt. of India on Electronic System Design and Manufacturing (ESDM)'15
 - Chanakya National Law University MUN(Model United Nations)'13
 - Indian School of Mines, Dhanbad MUN(Model United Nations)'13
 - INTACH Heritage Quiz, 2014.
- Qualified for Scholarship under INSPIRE Scheme of Govt. of Andhra Pradesh, by virtue of performance within top 1% of the Board of Intermediate Education, Andhra Pradesh.
 [2012]
- Secured City Rank 1 and All India Rank 129 in National Level Science Talent Search Examination (NLSTSE)'12.
- South Zone finalist in MNRE Energy Quiz'2010, Mumbai, conducted by MNRE, Govt. of India.
- N.C.C (National Cadet Corps) 'A' Certificate and received the Best Cadet Award at school.

Positions of Responsibility and Extra-curricular activities

Entrepreneurship Club

[May'14-present]

- **Club Coordinator** for the academic year 2014-15. Supervised all the activities of the club and streamlined coordination among various committee members and 6 departments of the club.
- The team organized workshop on Business model development (with Tata-NEN), the first Pitching event for students and entrepreneurs from across Bihar (with Villgro Foundation) and various guest seminars with entrepreneurs.

Techno-cultural Fest 'Anwesha'

[2013-14]

 Worked as Sub-Coordinator, Marketing and Sponsorship Committee, for the annual techno-cultural fest of IIT Patna-ANWESHA'14, bringing in 5 new sponsors for the fest and developed relations with many potential sponsors.

Other involvements

- Active member of Sparkonics (the Electrical Club of IIT Patna) and HoSCA(the Cultural Club of IIT Patna).
- Selected as Vice-President of School Parliament and Asst. Head Boy for the academic year 2009-2010.
- Passionate about: Reading articles on Technology, Entrepreneurship and Leadership, listening to music.