

RAMLAKHAN MEENA Electrical Engineering Indian Institute of Technology, Patna

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1301EE28 UG Third Year (B.Tech.) Male DOB:14JUL 1995

| Examination | Board | Institute | Year | CPI / % |
|---------------------|-------------------|-----------------------------------|------|---------|
| Graduation | Central | IIT Patna | 2013 | 6.95 |
| Intermediate/+2 | State Board(RBSE) | Jagatpura central academic School | 2012 | 80 |
| Matriculation(10th) | State Board(RBSE) | AVM School | 2011 | 84 |

(At the end of 4th semester)

ACHIEVEMENTS:

- Secured Rank 363 in Rajasthan Pre- Engineering Test.
- Stood second in the AVM School in Class X Board of Rajasthan (RBSE) Examinations.
- selected in Merit- Cum- Means (MCM) Scholarship in 2014.

SOFTWARE SKILLS:

Software: MATLAB, Xilinx, AutoCAD

• Hardware Programming language : Verilog HDL

Programming: C, C++, javaOther Utilities: MS-Office

Operating Systems : Windows, Linux

COURSE:

Core:

- Electrical Sciences
- Semiconductor Devices and Circuits
- Digital Circuits
- Signal, Systems and Networks
- Principle of Communication
- Electrical Machine
- Analog Integrated Circuit

Lab Course:

- Digital Circuit Laboratory
- Analog Circuit Laboratory
- Electrical Machine Laboratory
- Basic Electronics
- Workshop I

Breadth:

- Calculus
- Differential Equations
- Linear Algebra
- Numerical analysis
- Engineering Drawing
- Introduction to probability theory
- Introduction Microeconomic
- Introduction Macroeconomic

EXTRA-CURRICULAR ACTIVITIES:

- Worked as volunteer (cultural and technical fest of IIT-Patna)
- Participated in inter year tournament at IIT-Patna in Cricket ,Volleyball

PERSONAL INFORMATION:

• Father's name: Gulab chand Meena

• Mother's name: Sushila Devi

• Address: Room no:212,B-Block,Boys Hostel,IIT PATNA,Bihta,Patna.

• Permanant Address: G-31, Triputi Nager, Jagatpura

Jaipur(302017) (Rajasthan)

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DECLARATION:

I hereby declare that all the information given above is true to the best of my knowledge.

Date:23-09-2015 Place:Patna