

CURRICULUM VITAE

TULASI CHANDAN BEHERA

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Third year Undergraduate (Semester 5)

Electrical Engineering, Indian Institute of Technology (IIT) Patna

Address: Boys Hostel, Room number A703, IIT Patna, Bihta, Patna, Bihar, India. Pin - 801103

Date of Birth: 7th May, 1995

Nationality: Indian

OJECTIVE:

To obtain a summer internship which provides me an opportunity to gain exposure to modern technologies and explore new solutions to global problems through innovative technologies available at present.

ACADEMIC RECORD:

Degree/Certificate	Institute	CPI/Percentage obtained	Year of Passing
Bachelor of Technology (at the end of 4th Semester)	Indian Institute of Technology Patna	8.42/10	2017
Intermediate/ +2	Adyant +2 Science College BBSR.(Odi) (Odisha state board)	76.00	2012
Matriculation	Jawahar Navodaya Vidyalaya Angul(Odi) (C.B.S.E.)	9.80/10	2010

INTERNSHIP & RESEARCH EXPERIENCE:

Summer intern at IBM in Java Web Technology

(18-05-2015 To 17-07-15)

Mentor: Mr Pritam Chopdar, IBM, Pune

Duration: 4th semester

- Developed a Web Application to solve the issue of children education in developing countries with crowd source concept.
- Technology used: HTML, XML, Java Script, Java, Servlets, JSP and Database.

Design a video surveillance system to detect a person

with or without any baggage (Image Processing)

(16-08-2015 To till now)

Mentor: Dr. Mahesh Kolekar, Indian Institute of Technology, Patna. Duration: 5th Semester

- The aim is to design an algorithm to detect a person with or without any baggage with him /her.

- This includes programming as well as working with collected real Image processing data using a detector.

Automatic Toll Gate Revenue Collector (Digital Electronic) (04-08-2014 To 24-08-14)

Mentor: Dr. Y.K.Singh , Indian Institute of Technology, Patna. Duration: 3rd Semester

- The project was about counting the number of vehicles and the total amount collected at a toll booth. The charges were different for different types of vehicles.
- Got familiarised with basic digital electronics.

Project on Whistle Detector

(28-02-2014 To 09-05-14)

Mentor: Dr. M.K.Kolekar, Indian Institute of Technology, Patna. Duration: 2nd Semester

- Completed a project on Whistle Detector

AREAS OF INTEREST:

- Digital Signal Processing
- Wireless communications and signal processing
- VLSI & Embedded Systems
- Control Engineering

TECHNICAL SKILLS:

- **Programming Languages:** C, JAVA, Verilog, HTML, Java Script, XML, PLC.
- **Software Skills:** Mat lab, Database, Circuit Maker, AutoCAD, PSPICE, Xilinx ISE Design Suite, Simvision & FPGA , Implementation (Spartan 3E), Oracle
- **Kits worked upon:** 8085, 8086
- **Operating Systems :** Windows, Linux

SCHOLASTIC ACHIEVEMENTS:

- School topper for consecutive classes: 8th, 9th and 10th.
- Secured 4th position in state level **Mathematics Olympiad** in class 12 organized by **Odisha Mathematical Society**.
- One among the 20 successful candidates from all over Odisha to get selected in **Atom50** conducted by Aakash Institute.
- Selected for 2nd level of NTSE in 10th class.

POSITION OF RESPONSIBILITY & EXTRA CURRICULAR ACHIEVEMENTS:

Position of responsibility:

- Coordinator of Training & Placement cell for the year 2015-16.

Activities:

- Active member of "SPARKONICS", the electrical engg. club of IIT Patna.
- Organised "STATIC RUSH", an event in the techno cultural fest of IIT Patna "ANWESHA".

Achievements:

- Selected in **Khokho team** for **National Level Game** for two consecutive year in **2007 & 2008** hosted by JNV Krishna(AP) & JNV Katihar (Bihar) respectively.
- Selected in **Football team** for **National Level 2009** hosted by JNV Lucknow(UP).
- Got 2nd position in “**Mock Parliament**” competition at national level in 2009-10.
- **National Science Congress 2009:** Participated in National Science Congress, India -2009 .Got interacted with many scientists.

RELEVANT COURSES:

- **Electrical Engineering:** Semiconductor Devices and Circuits, Digital Circuits and Microprocessor, Signal, System & Networks, Analog Integrated Circuits, Principles of Communication, Electrical Machines, Introduction to VLSI Design, Digital Signal Processing, Electrical Power Systems, Control Systems, Electronic Instrumentation,
- **Electrical Laboratories:** Basic Electronics Laboratory, Digital Circuits Laboratory, Analog Circuits Laboratory, Electrical Machines Laboratory, VLSI Laboratory, Communication Laboratory, Instrumentation and Control Lab
- **Computer Science:** Introduction to Computing, Data Structures and Algorithms, Object Oriented Programming.
- **Mathematics:** Calculus, Linear Algebra and Ordinary Differential Equations, Complex Analysis and Partial Differential Equation, Numerical Methods for solving Partial Differential Equation, Real Analysis.
- **Open Electives:** Introduction to Numerical Methods, Healthcare & Management Systems, Sociology.

REFERES:

Mr. Pritam Kumar Chopdar

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Dr. Mahesh Kumar H. Kolekar

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