

Siya Ram Meena

Electrical Engineering

B.Tech 3 rd Year (5th semester)

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Indian Institute of Technology, Patna

Address: Room no.: 707A Boys Hostel, IIT Patna

Bihta, Bihar, India, pin -801103

Date of Birth: 26th august 1994

OBJECTIVES

To work in a firm with a professional work driven environment where I can utilize and apply my knowledge, skills which would enable me as a fresh graduate to grow while fulfilling organizational goals.

BASIC ACADEMIC CREDENTIALS

Qualification	Board/University	Year	Percentage
B.Tech (Electrical Engineering)	Indian Institute of Technology, Patna	2013-2017	6.72/10(till 4 th sem)
Intermediate/+2	Defence Public school (RBSE)	2012	75.60%
Matriculation	New Children Academic school(RBSE)	2010	67.83%

Projects

Hydroelectricity plant:

This project is based on how hydroelectricity plant work, this project was for development of Resources for our society. a presentation were also given and all the process were described

Areas of Interest:

- Digital signal processing
- Control Engineering
- Digital Electronic Engineering
- Communication Engineering

Technical Skills:

- Programming Languages: C, C++, JAVA, Verilog, Assembly Language, PLC.
- **Software Skills:** Mat lab, Circuit Maker, AutoCAD, PSPICE, Xilinx ISE Design Suite, Simvision & FPGA, Implementation (Spartan 3E)
- Operating Systems: Windows, Linux

AWARD AND SCHOLARSHIP:

- Cleared IIT-JEE MAINS 2013 with All India Category Rank 599
- Cleared IIT-JEE Advance 2013 with All India Category Rank 465
- cleared Indian Council of agriculture research(ICAR) 2013 exam with category Rank under 50
- > Selected for Scholarship MCM at IIT Patna in 2013-
- Selected for Top class Scholarship by MHRD in 2013, 2014,2015
- > Selected for anuprati scholarship by state govt.
- ➤ Part of IIT Patna Cricket team

Extra-Curricular Achievements:

- An active member of "Bhakti-vedanta Club, IIT Patna".
- Volunteer for Security during "Anwesha 2014" (cultural fest, IIT Patna).

Relevant Courses:

• Electrical Engineering: Electrical Sciences, Semiconductor Devices and Circuits, Digital Circuits and Microprocessor, Signal, System & Networks, Analog Integrated Circuits, Principles of Communication, Electrical Machines, Introduction to VLSI Design, Digital Signal Processing, Electrical Power Systems, Control Systems, Electronic Instrumentation,
• Electrical Laboratories: Basic Electronics Laboratory, Digital Circuits Laboratory, Analog Circuits Laboratory, Electrical Machines Laboratory, VLSI Laboratory, Communication Laboratory, Instrumentation and Control Lab

- **Computer Science:** Introduction to Computing, Data Structures and Algorithms, Object Oriented Programming.
- Mathematics: Calculus, Linear Algebra and Ordinary Differential Equations, Complex Analysis and Partial Differential Equation, Numerical Methods for solving Partial Differential Equation, Real Analysis.
- Open Electives: Introductory sociology, Introduction to Numerical Methods, Introduction to linguistic

DECLARATION

Additional and appropriate references will be furnished upop PLACE: PATNA	Siya Ram Meeena
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