Divyansh Kumar

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Final Year Undergraduate Student, Roll Number - 1201EE12
Department of Electrical Engineering
Indian Institute of Technology, Patna
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EDUCATION

- **Indian Institute of Technology, Patna** — Patna, India
  Bachelor of Technology, Department of Electrical Engineering — 2012 – present
  CGPA: 7.43/10 (after 6 semesters)
- **Brij Bhushan Lal Public School** — Bareilly, India
  Class XII, Central Board of Secondary Education — 2011
  Performance: 85.00%
- **Brij Bhushan Lal Public School** — Bareilly, India
  Class X, Central Board of Secondary Education — 2009
  Performance: 85.60%

RELEVANT COURSES

Introduction to Computing, Object Oriented Programming & Data Structures
Electrical Machines, Power System Operation & Control, Power Electronics, Wireless Communication
Integrated Circuits, Communication Networks, Semiconductor Devices and Circuits, Digital Circuits
and Microprocessor, Signal, System and Networks, Analog Integrated Circuits, Principles of Communication, Introduction to VLSI Design, Embedded Systems, Digital Signal Processing, Control
Systems, Electronic Instrumentation

PRACTICAL PROFICIENCY

Proficient C, C++ & JAVA, Well versed with Programming Algorithms
Digital Circuits Laboratory, Analog Circuits Laboratory, VLSI & Embedded Systems Laboratory,
Digital Signal Processing Lab, Communication Laboratory, Instrumentation and Control Lab

PROJECTS & INTERNSHIP

Smart Parking System — May'15 - July'15
Guide: Mrs. Seema Malhotra, Masamb Electronics System — (Summer Internship)

- Designing Parking System (in embedded system) to detect arrived vehicles and the available
  slots for parking & displaying available parking slots to the user using an Android App.

RFID Based Attendance System — Jan'15 - Apr'15
Guide: Dr. Aneek Adhya, Indian Institute of Technology Patna — (Design Lab Project)

- To create an efficient student attendance system based on recognition of RFID tags.

Moving Object Detection — Aug'14 - Nov'14
Guide: Maheshkumar H. Kolekar, Indian Institute of Technology Patna — (Term Project)

- Designing algorithm for detecting a specific object based on finding point correspondences
  between the reference and the target image.

SKILLS

- Programming Languages: C, C++, JAVA, Verilog
- Proficient in MATLAB and Simulink, Mathematica and PLC programming.
- Hands on experience with Arduino Boards.

POSITIONS OF RESPONSIBILITIES

- **Secretary of Technical Affairs**, Aryabhatta Hall, IIT Patna. — July'14 - Apr'15
- **Fest Co-ordinator, Celesta 2014**, annual intra-college tech-management fest of IIT Patna.

AWARDS AND ACHIEVEMENTS

- Won second prize in **Electro Quiz**, a quiz on concepts of Basic Electrical Science organized by
  IIT Patna in November 2013.

EXTRA-CURRICULAR ACTIVITIES

- Part of Athletics team of IIT Patna.
- Avid Chess player