

Swapnil Jhawar

Department of Electrical Engineering
Indian Institute of Technology, Patna
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EDUCATION

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| Indian Institute of Technology, Patna
Bachelor of Technology in Electrical Engineering
➤ Cumulative GPA: 7.83/10 (after 4 semester) | 2013 - Present |
| Vidya Mandir Sr. Secondary School, Dadabari, Kota
Intermediate - Central Board of Secondary Education
➤ Aggregate: 87.00% | 2012 – 2013 |
| Sacred Heart Convent School, Rewa, Madhya Pradesh
Matriculation - Central Board of Secondary Education
➤ Cumulative GPA: 8.2/10.0 | 2010 – 2011 |

RESEARCH INTERESTS

- Analog and Digital Circuits
- VLSI
- Control System
- Wireless Communication

SKILLS

- **Application Software Used:** MATLAB, AUTOCAD, Microsoft Office, Xilinx ISE 13.2, NetBeans, Eclipse, Code Blocks, Arduino
- **Operating Software Used:** Windows 7/8/8.1/10/XP, Linux
- **Programming Languages:** Verilog HDL, C, JAVA, Arduino

ACDEMIC PROJECTS

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| An Advanced Sensor Network For Health Monitoring
<i>Guide: Dr. Sudan Majhi, Department of EE, IIT Patna.</i> | Feb 2015 – Present |
| ➤ Goal of this project is to design a system architecture for smart healthcare based on an advanced Sensor Network. It specially targets assisted-living residence and monitor the health condition continuously by processing the data in MATLAB. Notifies about the problems in human body. | |
| Line Follower | Feb 2014 |
| ➤ Constructed an automatic robot capable of traversing black line on white background using Arduino. | |
| Hydraulic Arm | Aug 2013 |
| ➤ Designed and created a manually robotic arm with hydraulics using as minimum as possible resources. | |

SCHOLASTIC ACHIEVEMENTS

- Secured All India Rank 4877 in **Joint Entrance Examination** for admission to Indian Institute of Technology (IIT-JEE) among 15 lakhs aspirants.
- Recipient of **Merit-cum-Means Scholarship**, awarded by the IIT on the basis of academic performance.
- Awarded First Prize in **Ultimate Social Being**, a Quiz competition held in Anwesha'15 (Annual Techno-Cultural Festival of IIT Patna).

RELEVANT COURSES

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| <ul style="list-style-type: none">➤ Core:<ul style="list-style-type: none">▪ Analog Integrated Circuits (+lab)▪ Digital Circuits (+lab)▪ Principles of Communication (+lab)▪ Signal System and Network(+lab)▪ Electrical Machines(+lab)▪ Control System**▪ Digital Signal Processing**▪ Electrical Power System**▪ VLSI Design(+lab) **▪ Electronic Instrumentation(+lab) ** | <ul style="list-style-type: none">➤ Mathematics:<ul style="list-style-type: none">▪ Probability▪ Differential Equations▪ Linear Algebra & Matrices▪ Calculus▪ Real & Complex Analysis▪ Numerical Methods➤ Others:<ul style="list-style-type: none">▪ Introduction to Computing (+lab)▪ Object Oriented Programming & Data Structures (+lab)▪ Modern Physics (+lab)▪ Economics |
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(** to be completed by Nov 2015)

EXTRA-CURRICULAR ACTIVITIES

- Represented IIT Patna Cricket team in **Inter-IIT Sports Meet** 2013 at Guwahati and 2014 at Mumbai.
- Attended a one day workshop on **Electronics System Design and Manufacturing, ESDM'15**.
- Held the position of event organizer in ANWESHA'15, Annual Techno-Cultural Festival of IIT Patna.
- Organized cricket tournaments of IIT Patna.