

Arun Kumar Singh

CONTACT INFORMATION	B2/B501, Boys Hostel, IIT Patna. Patna, India, 801103 Phone: (+91) 9430674161	E-mail: arun.iitp@gmail.com Website: in.linkedin.com/in/arunsingh93/
INTERESTS	Reconfigurable computing,VLSI for Chip design, Computer Arithmetic, Embedded Systems, Signal Processing and Wireless Communication Systems	
EDUCATION	Indian Institute of Technology Patna. India Bachelor of Technology in Electrical Engineering	July 2012 – present (CGPA 8.72/10)
RELATED COURSEWORK	VLSI Design, Embedded System, Digital Signal Processing, Microprocessors, Communication Systems, Signal Systems and Networks, Control Systems,Electrical Machines,Power System,	
RESEARCH WORK	Design and Implementation of Quadruple Floating-Point (QFP) CORDIC Supervisor: Dr. K C Ray, Department of Electrical Engineering, IIT Patna, India Published in INIS 2015: IEEE International Symposium on Nanoelectronic and Information Systems.	
SUMMER INTERNSHIP	Hardware Implementation for Cryptographic Function based on Sponge Construction Supervisor: Dr. Lam Siew Kei, Nanyang Technological University Singapore May - Aug 2015 Summer Research Internship in the Centre for High Performance Embedded Systems (CHiPES) research centre at Nanyang Technological University in Singapore	
	General Overview of working of a thermal power plant National Thermal Power Corporation (N.T.P.C),Rihandnagar India	May - July 2014
PROJECT EXPERIENCE	Design And Development of Embedded System for Image Text Extraction Dr. Kailash Chandra Ray, Department of Electrical Engineering, IIT Patna Spring 2015	
	I/O Pad design and IC fabrication of QFP-CORDIC Processor Advanced VLSI Lab, IIT Kharagpur, India.	December 2014
	Event detection in soccer videos, for automatic highlight generation Dr. Mahesh K H.Kolekar, Department of Electrical Engineering, IIT Patna	Fall 2014
	PCB designing of Binary Phase Shift Keying (BPSK) Transmitter and Receiver Communication Lab, Dr. Sumanta Gupta, Dept. of Electrical Engineering, IIT Patna	Fall 2014
TECHNICAL SKILLS	HDL: Verilog Processor: Altera Nios II Layout tools : Pyxis (Mentor Graphics), Cadence Virtuoso LE/XL Scientific Programs and Languages: MATLAB, Mathematica General Programming Skills: JAVA	
ACTIVITIES	<ul style="list-style-type: none">• Elected as the Coordinator of the Sparkonics(an Electronics club) at IIT Patna• Awarded First position for proposing an Efficient and Eco-friendly alternative to conventional Refrigeration System at Anwesha(Techno-Management Fest IITP)-2013• Conducted lectures on Arduino and Matlab,for sophomore year students under Sparkonics Club of IIT Patna.• Sports: Swimming, Badminton and Table tennis.	