GAURAV PARIHAR

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| Sex: M | |(+91) 8890619114| | Nationality: Indian |

Education

Bachelor of Technology, Electrical Engineering.	**Graduating May, 2016**
Indian Institute of Technology Patna (IIT Patna)	CGPA: 7.86/10 (Over 6 semesters)
Senior Secondary Examination, Board of Secondary Education, Rajasthan	**Graduated 2012**
Smt. J.P. Memorial Senior Secondary School Jodhpur. 86.8%	
Secondary School Examination, Board of Secondary Education, Rajasthan	**Graduated 2010**
Smt. J.P. Memorial Senior Secondary School Jodhpur. 92.83%	

Scholastic Achievements

Secured All India Rank 2065 (OBC) in IIT-JEE 2012.

Academic Projects

1. Engineering Project   **January-April, 2015**

MENTOR: **Dr. Sumanta Gupta (**Asst. Professor, Electrical Engineering IIT Patna)*.*

- Worked on the project **"Duplex Communication System".**
- Frequency Modulation technique is used for transmission .VCO (LM566) and PLL(LM565) IC's are used for modulation and demodulation respectively.
- Condenser microphones are used as audio transducers.

October, 2014

2. VLSI design mini Project

MENTOR: **Dr. Kailash Chandra Ray (**Asst. Professor, Electrical Engineering IIT Patna)*.*

- FPGA implementation of design mean filter and median filter(using Bubble sort Algorithm) in Verilog HDL. Software tool used was Xilinx ISE.

3. Summer Internship Project   **May-July, 2015**

Company: **Silicon Mentor, greater Noida**

- Worked on the project **"FPGA implementation of Convolutional Neural Network"**.
- The coding platform used was Verilog HDL(Xilinx ISE).

Relevant Coursework

- Electrical Sciences , Semiconductor Devices, Digital Circuits and Microprocessor , Signal, System and Networks, Analog Integrated Circuits, Principles of Communication, Electrical Machines, Introduction to VLSI Design, Digital Signal Processing, Control Systems, Electronic Instrumentation, Power System Analysis, Embedded Systems

Technical Skills

- **Language Skills:** Java, C
- **Design Tools:** Verilog HDL, Xilinx ISE
- **Mathematical Tools:** MATLAB
- **Assembly Language:** Microprocessor 8085

Awards & Extracurricular Activities

- Secured second position at district level in "Rajya Vigyan Pratibha Khoj Pratiyogita Pariksha".   **2011**
- Participated in robotics workshop by i3 india technologies held at IIT Patna.   **2012**
- Secured second position in Hindi Essay Writing at IIT Patna.   **2013**