RAJAT GOYAL

Fourth Year Undergraduate   Room – B-410, IIT Patna Boys Hostel,
Department of Electrical Engineering   IIT Patna, Bihta-800111
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EDUCATION

2012-present	**Bachelor of Technology, Electrical Engineering**	CGPA: 8.74/10, **(Over 6 Semesters)**	
	Indian Institute of Technology, Patna	*Department Rank: 5th*	
2011	Senior Secondary School (Class XII CBSE Board)	Aggregate: 83%	
	Kendriya Vidyalaya No. 2, Jaipur		
2009	Secondary School (Class X CBSE Board)	Aggregate: 85.6%	
	Kendriya Vidyalaya No.2, Jaipur		

WORK EXPERIENCE

- **Summer Intern at Robert Bosch Research & Technology Centre – India, Bengaluru (RBEI)**
  (Mentor: Mr. Kumar Saurabh, Architect, CR-RTC Dept.)   (May 2015 – July 2015)
  - Worked on a project "Ultrasonic Ranging System development involving sensor interfacing, hardware-board bring up and data analysis and visualization" on a Raspberry Pi platform interfaced with camera and ultrasonic sensors.
  - Performed several on-field experiments in real-time environment to collect the data using Python and further analyzed the collected data in Rstudio using R.

ACADEMIC PROJECTS

- **B.Tech Project: Design Of 8-bit RISC Processor For FPGA**
  (Mentor: Dr. K.C.Ray, Dept. of EE, IIT Patna)   (September 2015 – Present)
  - Initiated work on the designing of simple 8-bit RISC processor for FPGA implementation and presently working on the design of memory architecture and its further implementation using VerilogHDL.
- **Design Lab Project: A Real-Time Embedded Platform For Text Recognition**
  (Mentor: Dr. K.C.Ray, Dept. of EE, IIT Patna)   (January 2015 – April 2015)
  - Developed a text recognition system on Altium Innovation Station platform capable of capturing and extracting the text from a real-time captured image using traversing based segmentation algorithm.
- **TEAM BAJA IIT PATNA**   (August 2014-February 2015)
  (Mentor: Dr. Akhilendra Singh and Dr. Mayank Tiwari, Dept. of ME, IIT Patna)
  - Implemented the electrical system of an all-terrain sporting vehicle system comprising of battery, a pair of kill switches, lights and alarms for the final round of 2015 BAJA SAEINDIA national competition.

SKILL-SET AND EXTRA-CURRICULAR ACTIVITIES

Software Packages	**:**	Matlab & Simulink, Xilinx Design Suite, Pyxis schematics
Programming Languages	**:**	C, C++, Intel 8085 assembly language, Verilog HDL
Language	**:**	English, Hindi (Native Proficiency)

Positions of Responsibility:
- Coordinator of Hospitality Team for ANWESHA'15 (Annual techno-cultural fest of IIT Patna).
- Led Table tennis team from IIT Patna as Team Captain comprising of seven members in 50th Inter-IIT sports meet held at IIT Bombay (Dec,2014).

Coding & Music:
- Actively participate in online coding challenges and secured first position in NJACK Coding Challenge held in September'12 at IIT Patna.
- Synth Player and founder member of the College Band named "**Eighth Note**".