

DLD Assignment 7

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1 Boolean Equation

$$c = \overline{A} B \overline{C} \overline{D} \quad (1)$$

2 Logic Gate Circuit

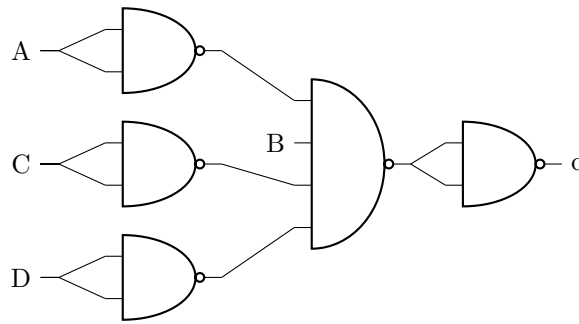


Figure 1: Logic Gate for 2.5.1 (c)