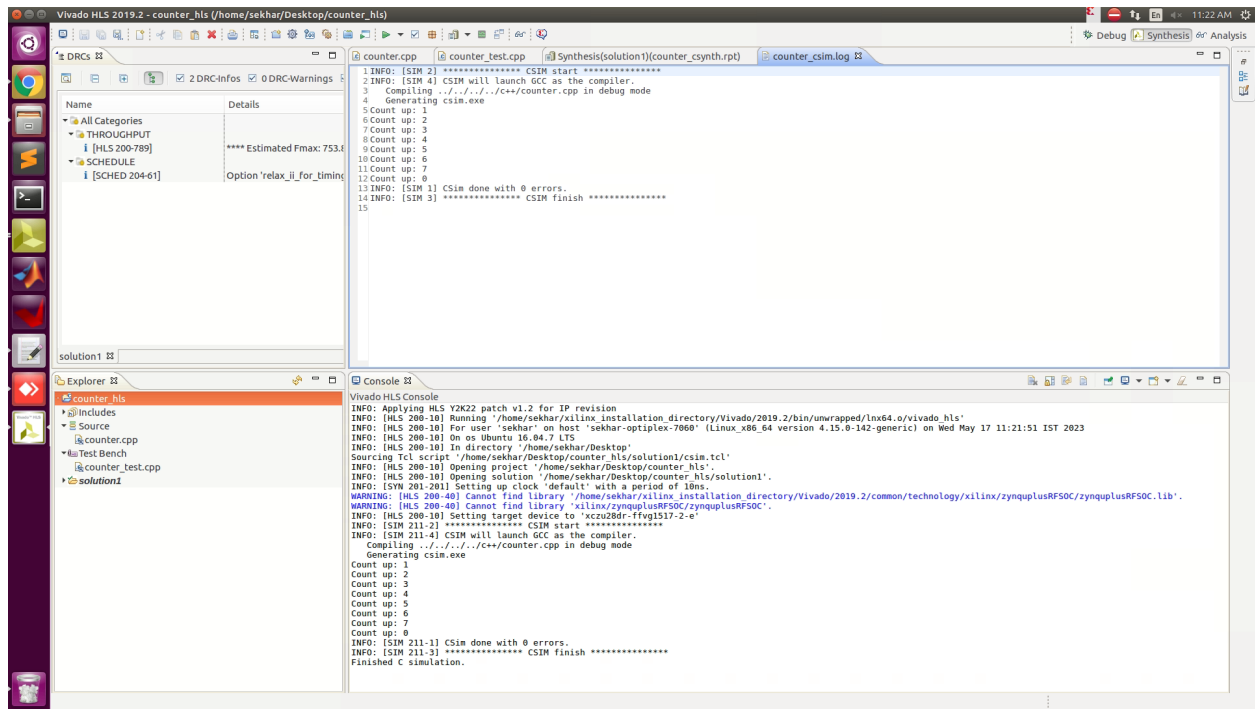


Design a Counter circuit

Simulation Result :



Synthesis Result :

