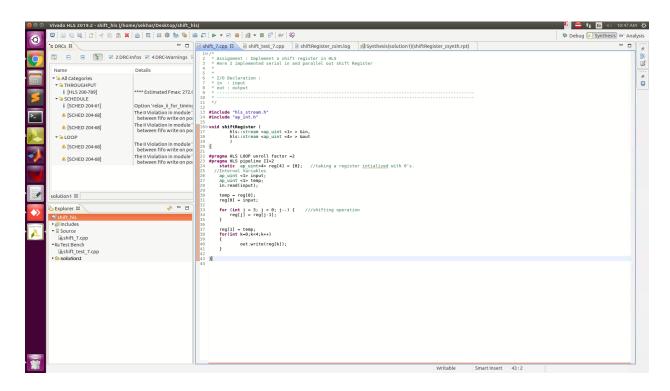
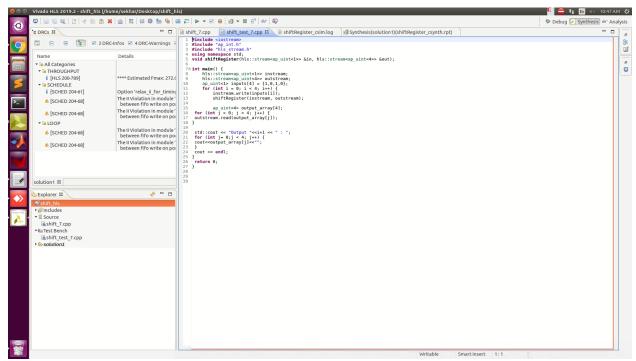
Assignment-7

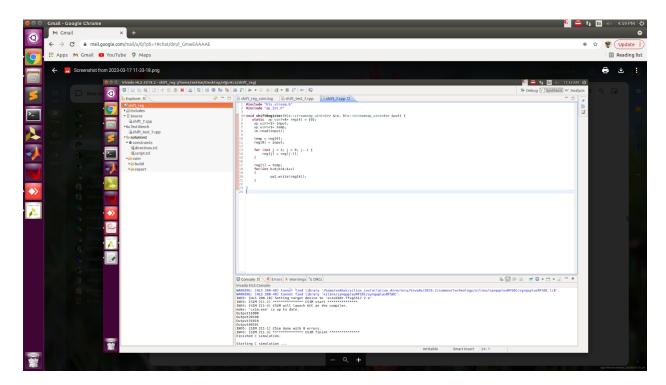
Implement a 10 bit shift register

Simulation:





Result:



Synthesis Result:

