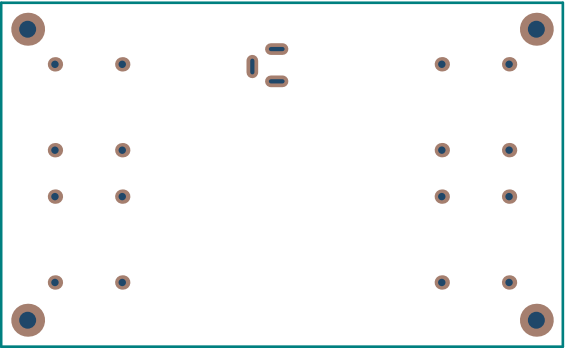
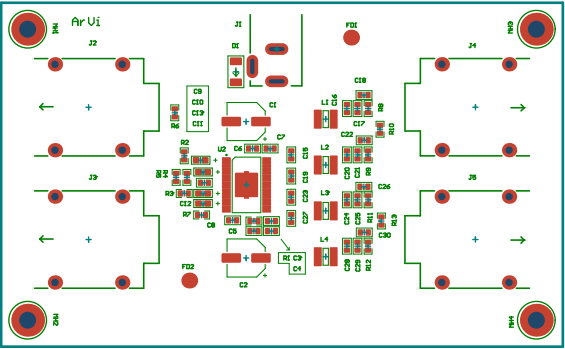
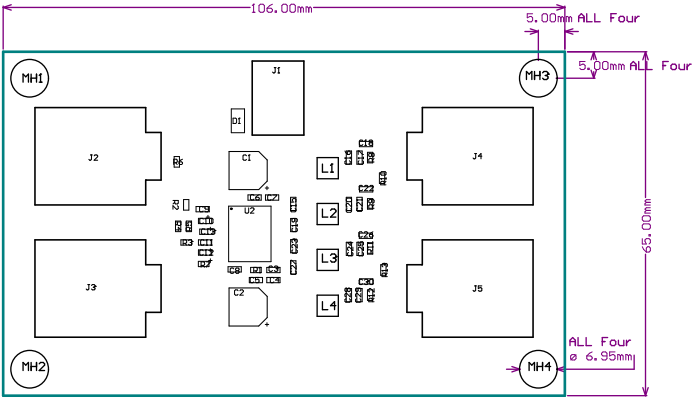
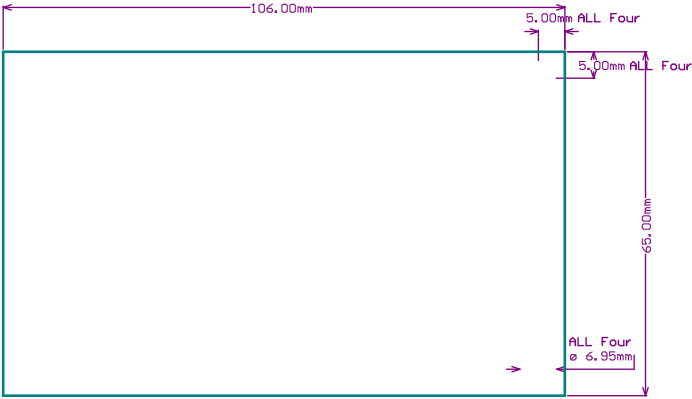


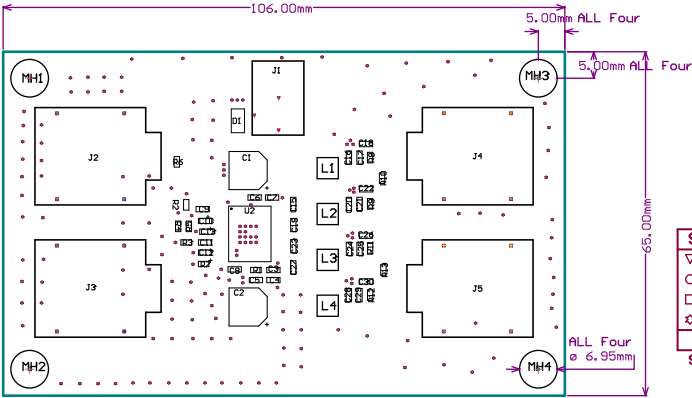
Layer	Name	Material	Thickness	Constant	Board Layer Stack
	Top Overlay				
	Top Solder	SM-001	1.00mil	4	
	Top Surface Finish	PbSn	0.79mil		
1	Top Layer	CF-004	1.38mil		
	Dielectric 1	Core-043	59.00mil	4.3	
2	Bottom Layer	CF-004	1.38mil		
	Bottom Surface Finish	PbSn	0.79mil		
	Bottom Solder	SM-001	1.00mil	4	
	Bottom Overlay				











Symbol	Count	Hole Size	Plated	Hole Type	Drill Layer Pair	Via/Pad	Pad Shape	Template	Description	Hole Tolerance (+)	Hole Tolerance (-)
▽	3	31.50mil (0.80mm)	PTH	Slot	Top Layer - Bottom Layer	Pad	Rounded	r420_200h80_300r100m0mx0			
○	4	125.98mil (3.20mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c600h320m0mx0_2670034071			
□	16	55.12mil (1.40mm)	PTH	Round	Top Layer - Bottom Layer	Pad	Rounded	c250h140m0mx0			
☆	172	10.00mil (0.25mm)	PTH	Round	Top Layer - Bottom Layer	Via	Rounded	v51h25m0mx0			
195 Total											

Slot definitions : Routed Path Length = Calculated from tool start centre position to tool end centre position.
Hole Length = Routed Path Length + Tool Size = Slot length as defined in the PCB layout

