



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

HYDERABAD - 500 085, ANDHRA PRADESH, INDIA

CONSOLIDATED MARKS MEMO / CREDIT SHEET



BACHELOR OF TECHNOLOGY- ELECTRONICS & COMMUNICATION ENGINEERING

CMM. No.: **C 0438628**

Serial No.: 21101035655

Name: **BIRRU VIJAY KUMAR**

Name of the College: **8W-GNIET, KHANAPUR**

Month & Year of Final Exam: **April, 2014**

Hall Ticket No.: **108W1A0457**

Year of Admission: **2010-2011**

Class Awarded: **FIRST CLASS**

S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS	S.No.	SUBJECT TITLE	INT MARKS	EXT MARKS	TOTAL	CREDITS
	Maximum Marks in Theory	25	75	100			Maximum Marks in Lab	25	50	75	

I YEAR

1	ENGLISH	18	58	76	4	2	MATHEMATICS - I	17	45	62	6
3	MATHEMATICAL METHODS	21	58	79	6	4	ENGINEERING PHYSICS	18	57	75	4
5	ENGINEERING CHEMISTRY	16	26	42*	4	6	COMPUTER PROGRAMMING & DATA STRUCTURES	17	57	74	6
7	ENGINEERING DRAWING	18	26	44*	4	8	COMPUTER PROGRAMMING LAB	12	39	51	4
9	ENGG. PHYSICS & ENGG. CHEMISTRY LAB	18	34	52	4	10	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB	18	47	65	4
11	IT WORKSHOP & ENGINEERING WORKSHOP	20	40	60	4						

I SEMESTER

II YEAR

II SEMESTER

1	MATHEMATICS-III	18	37	55	3	1	PRINCIPLES OF ELECTRICAL ENGINEERING	20	57	77	3
2	PROBABILITY THEORY & STOCHASTIC PROCESSES	21	46	67	3	2	ELECTRONIC CIRCUIT ANALYSIS	20	33	53	4
3	ENVIRONMENTAL STUDIES	20	53	73	3	3	PULSE & DIGITAL CIRCUITS	17	45	62	4
4	ELECTRIC CIRCUITS	21	26	47	4	4	SWITCHING THEORY & LOGIC DESIGN	15	36	51	4
5	ELECTRONIC DEVICES & CIRCUITS	19	50	69	4	5	ELECTROMAGNETIC THEORY & TRANSMISSION LINES	20	27	47	4
6	SIGNALS & SYSTEMS	21	29	50	4	6	ELECTRICAL ENGINEERING LAB	25	47	72	2
7	ELECTRONIC DEVICES & CIRCUITS LAB	19	42	61	2	7	ELECTRONIC CIRCUIT ANALYSIS LAB	22	44	66	2
8	BASIC SIMULATION LAB	21	46	67	2	8	PULSE & DIGITAL CIRCUITS LAB	22	46	68	2

I SEMESTER

III YEAR

II SEMESTER

1	CONTROL SYSTEMS	16	28	44	4	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	21	54	75	4
2	COMPUTER ORGANIZATION	22	26	48	4	2	OPERATING SYSTEMS	20	48	68	4
3	ANTENNAS & WAVE PROPAGATION	20	37	57	3	3	DIGITAL COMMUNICATIONS	18	46	64	3
4	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	16	51	67	4	4	MICROPROCESSORS & MICROCONTROLLERS	22	44	66	4
5	ANALOG COMMUNICATIONS	17	48	65	3	5	DIGITAL SIGNAL PROCESSING	18	35	53	4
6	IC APPLICATIONS	15	35	50	3	6	MICROPROCESSORS & MICROCONTROLLERS (LAB)	22	38	60	2
7	ANALOG COMMUNICATIONS (LAB)	14	29	43	2	7	DIGITAL SIGNAL PROCESSING (LAB)	20	42	62	2
8	IC APPLICATIONS (LAB)	16	39	55	2	8	ADVANCED ENGLISH COMMUNICATION SKILLS (LAB)	22	39	61	2

I SEMESTER

IV YEAR

II SEMESTER

1	MANAGEMENT SCIENCE	20	42	62	3	1	CELLULAR & MOBILE COMMUNICATIONS	18	52	70	3
2	VLSI DESIGN	22	35	57	4	2	RADAR SYSTEMS	24	38	62	3
3	MICROWAVE ENGINEERING	23	34	57	3	3	DIGITAL DESIGN THROUGH VERILOG HDL	22	49	71	3
4	COMPUTER NETWORKS	22	35	57	4	4	INDUSTRY ORIENTED MINI PROJECT	-	45	45	2
5	DIGITAL IMAGE PROCESSING	19	49	68	3	5	SEMINAR	44	-	44	2
6	EMBEDDED SYSTEMS	24	26	50	4	6	MAJOR PROJECT	42	141	183	10
7	E-CAD AND VLSI LAB	22	44	66	2	7	COMPREHENSIVE VIVA	-	92	92	2
8	MICROWAVE ENGG. AND DIGITAL COMMNS. LAB	22	46	68	2						

(# Project Internal=50, External=150)

Number of Credits registered for : 200 Aggregate Marks Secured for best: 192

Aggregate Marks Secured : 3569 OUT OF 5200 (68.63%)

Date of Issue : **June 13, 2014**

(see overleaf for Rules concerned to award of class)

A indicates 'ABSENT'



(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS