

VELAGAPUDI RAMAKRISHNA SIDDHARTHA ENGINEERING COLLEGE**VIJAYAWADA - 520 007, A.P., INDIA.**

(Approved by AICTE, Accredited by NAAC with 'A' Grade and ISO 9001: 2008 Certified)

(An Autonomous College Affiliated to JNTUK, Kakinada 533 003, India)

STATEMENT OF GRADES

Memo No. 01974

B.TECH. DEGREE EXAMINATIONS MAY 2018**BRANCH : ELECTRICAL AND ELECTRONICS ENGINEERING****SEMESTER : II****REGULATION : VR17**

REGD.NO.	178W1A0257	AADHAAR NO.	508744764603
NAME OF THE CANDIDATE	VEMANA VIJAY KUMAR		
FATHER'S NAME	VEMANA RAMESH		
MOTHER'S NAME	VEMANA VIJAYA NIRMALA		
COURSE CODE	TITLE OF THE COURSE	CREDITS	GRADE
17MA1201	LAPLACE TRANSFORMS AND INTEGRAL CALCULUS	4.0	A
17PH1202	ENGINEERING PHYSICS	3.0	A ⁺
17CS1203	PROGRAMMING IN C	3.0	A ⁺
17EE1204	NETWORK ANALYSIS - I	3.0	A
17HS1205	TECHNICAL ENGLISH AND COMMUNICATION SKILLS	3.0	A ⁺
* 17MC1206A	TECHNOLOGY AND SOCIETY	---	A ⁺
17PH1251	ENGINEERING PHYSICS LAB	1.5	Ex
17CS1252	COMPUTER PROGRAMMING LAB	1.5	Ex
17ME1253	BASIC WORKSHOP	1.5	A ⁺
PERCENTAGE OF ATTENDANCE : 91.48%			

* Mandatory not considered for GPA Calculation

SGPA: 8.80**CGPA: 9.04**

Prepared by:

Checked by:



CONTROLLER OF EXAMINATIONS
Date: 12/07/2018