

Frequency Generator using OpAmp

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Function Generator using OpAmp

This project showcases DIY Function generator with satisfactory range and accuracy

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Abstract

Function generator are useful tools in academia and industries. Mostly they are available in market. In this project we are trying to understand and study simple frequency generators with use of OpAmp. We use generic OpAmp IC LM741, which is single package and easy to understand with benefit of extensive academic experience.

1 Introduction

Function generator is circuit which generates periodic function with predictable frequencies with respect to time. Here, we will study only mono frequency generator but it can also generate superposed functions. Signals from Function generator comes in many forms but mostly it is either sinusoidal or square wave. We will generate sinusoidal, square and triangle wave as output.

We used basic circuits with few modification as our need. With use of IC LM741 we used OpAmp in our circuit.

For Sinusoidal wave we used Wein Bridge circuit, which is easy to understand and implement. Also, Wein bridge circuit is quite less noise compare to its competition RC phase shift Oscillator, which have more component than Wein bridge and more complicated to understand. For Square wave we used standard astable multivibrator circuit, with little modification. Lastly, Triangle wave can be made from just attaching Integrator to our square wave output with some regulation.

Now, each circuit (this wave form generator) has different block, basically we divided whole circuit in there block. Main work for us is to combine all of this. We wandered across CMOS families, BJTs but finally we settled into physical switch which is coupled for power transmission and also for output change.

2 Blocks

As told in introduction each circuit is in their blocks. First block for sine wave which is nothing but Wien bridge circuit, second is square wave which is astable multivibrator, third for triangular wave which integrator attached to second block (square wave block).

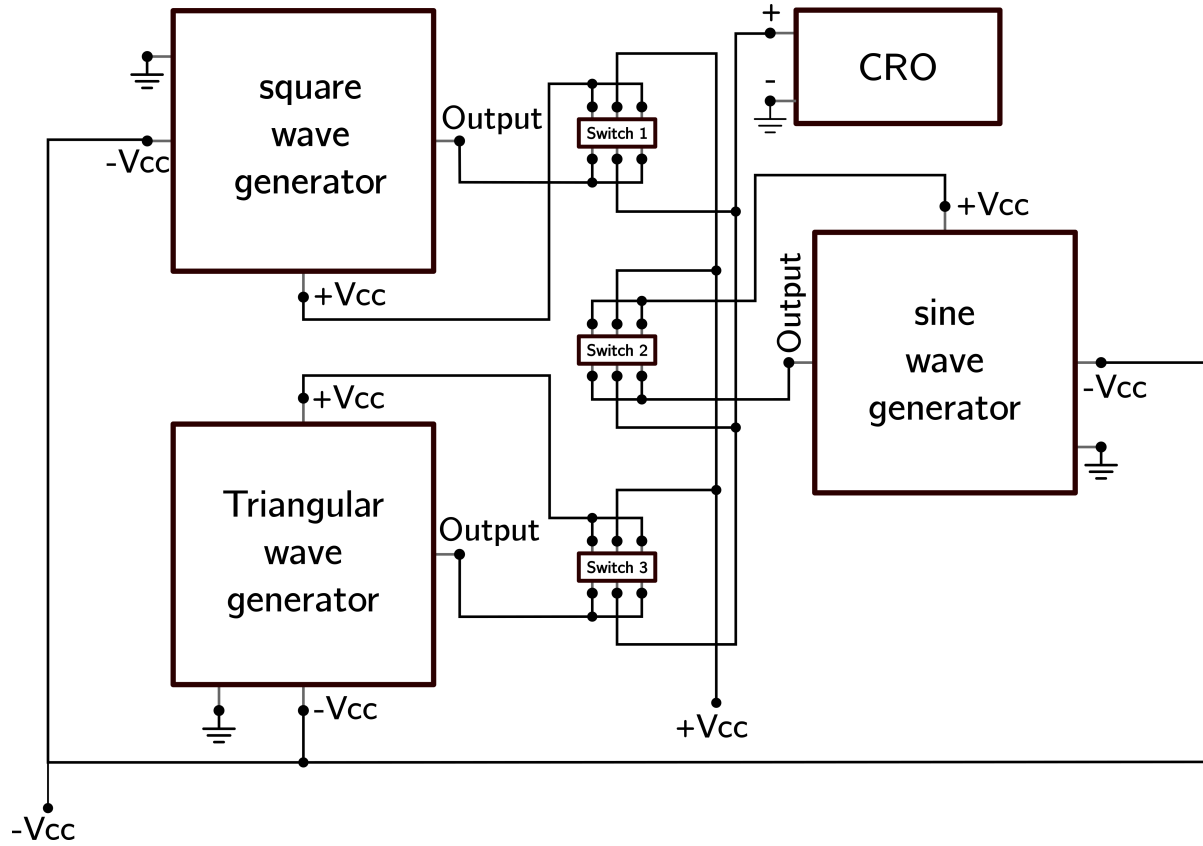


Figure 1: Block diagram of our function generator

2.1 Block 1: Sine wave generator

In first block, we have basic circuit of Wien bridge. You can see in figure 1. In center we have OpAmp (IC LM741). This is amplifier with RC component attached with input and output. Here, at one end there is RC parallel component and at other end series RC component.

Here, frequency is given by,

$$f = \frac{1}{2\pi RC} \quad (1)$$

For sustaining oscillation gain must be 3 and for non inverting amplifier gain,

$$A = 1 + \frac{R_F}{R_1} = 3 \quad (2)$$

So, we get relation $R_F = R_1$

Here, you can see our block circuit, at the end we attached two Zener diode for regulation to the output. As you can see OpAmp in IC LM741 package. Power supply given from 4 and 7 to 12V and -12V. We chose $R_1 = 12k\Omega$. By relation of R_1 and R_F , we got $R_F = 24k\Omega$.

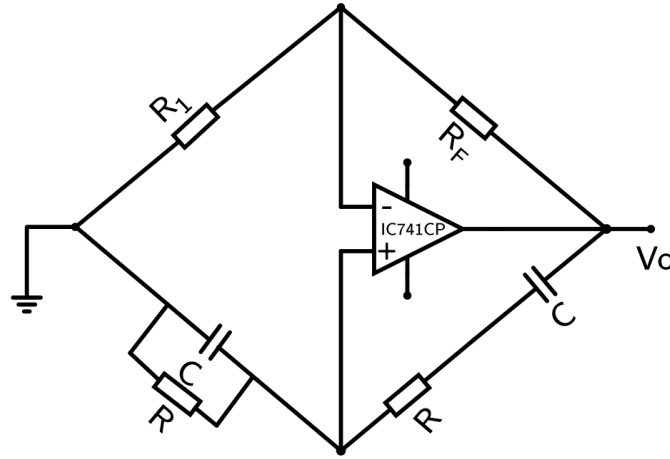


Figure 2: Wein bridge circuit

For frequency range we used Potential with max range of $100k\Omega$. So, lowest and maximum frequency whould be (with constant capacitance at $50nF$),

$$f_{min} = \frac{1}{2\pi \times 100k \times 10n} \approx 159hz$$

$$f_{max} = \frac{1}{2\pi \times 100 \times 10n} \approx 159khz$$

So, frequency range would be $159hz$ to $159khz$

2.2 Block 2: Square wave generator

As square wave generator we have basic astable multivibrator. This circuit works on scenario where output will have to stable state and it will swing between them, hence the name. When circuit is $+V_{sat}$, we will have high signal output and when circuit is $-V_{sat}$, we will have low signal output. So, we will have square wave as desired. The circuit for astable multivibrator is shown below.

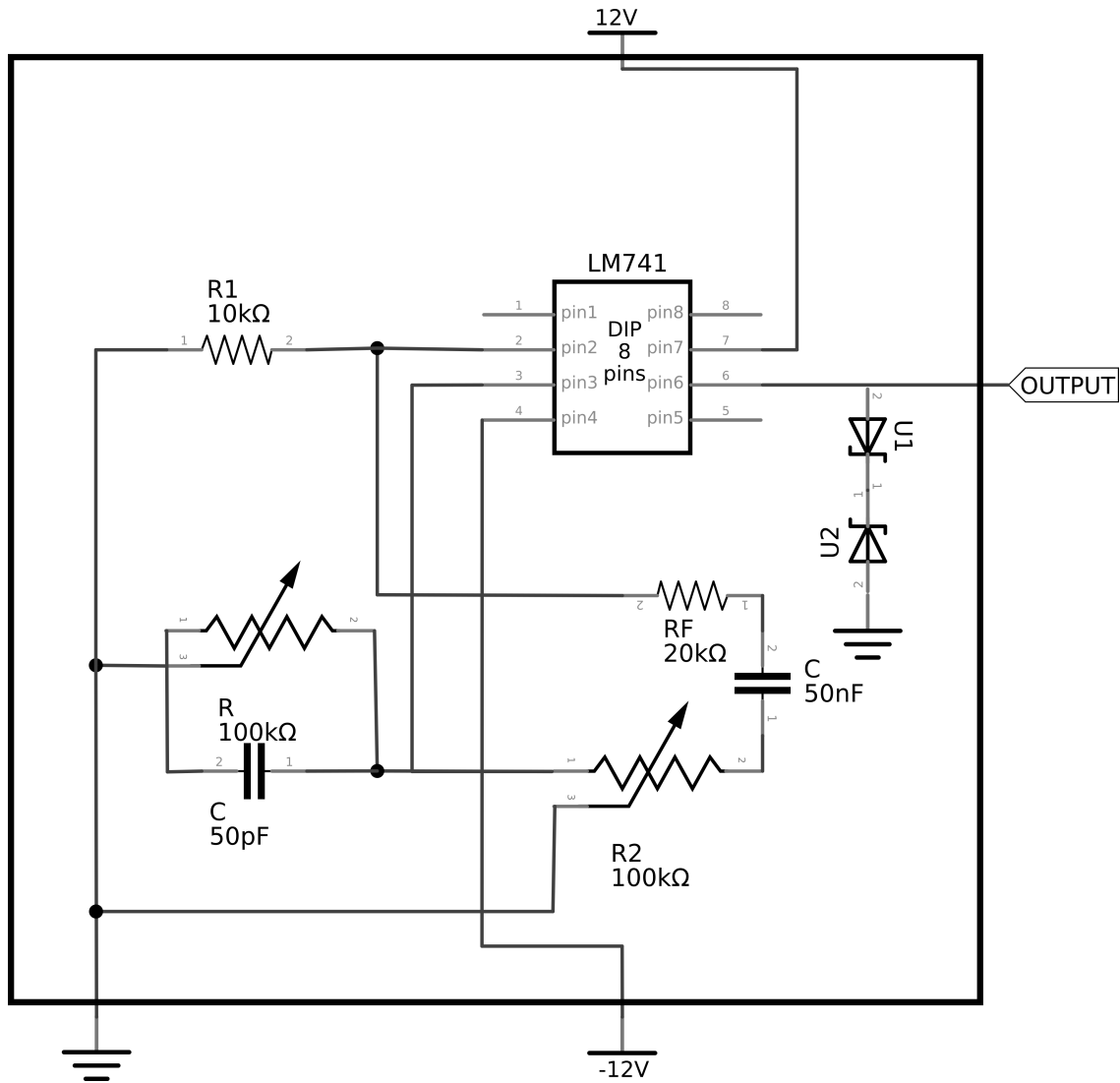


Figure 3: Our block 1, which consist of IC741CP

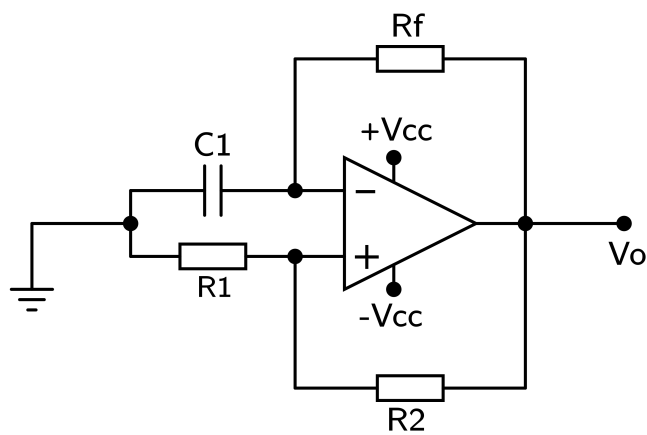


Figure 4: astable multivibrator circuit

Here, frequency would be,

$$f = \frac{1}{2RC \ln\left(\frac{2R_1 + R_2}{R_2}\right)} \quad (3)$$

If, we take $R_2 = 1.16R_1$ then,

$$f = \frac{1}{2RC} \quad (4)$$

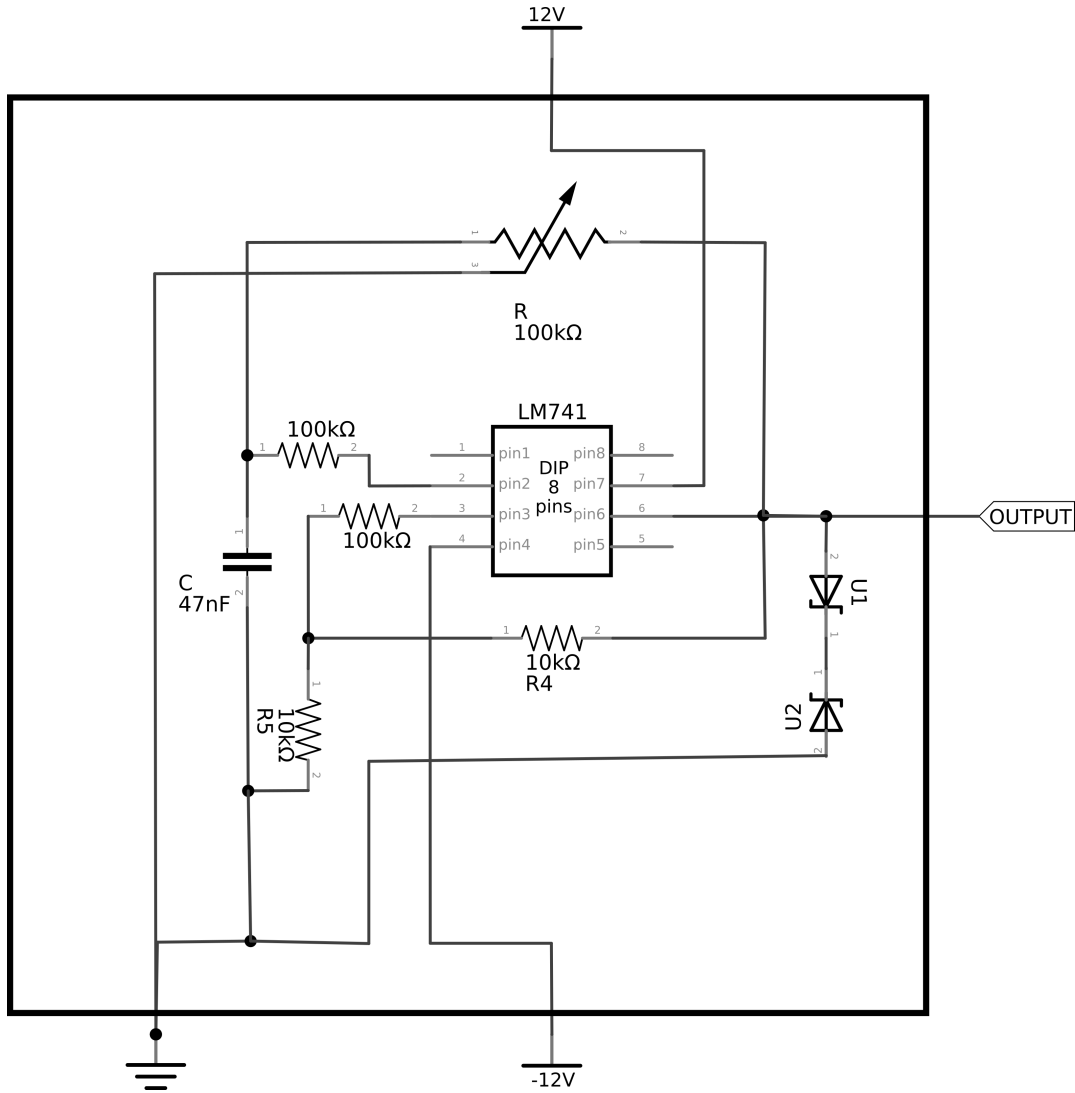


Figure 5: second block: square wave generator

Here, we took $R_1 = 10k\Omega$ and $R_2 = 11.6k\Omega$ such that $\frac{R_2}{R_1} = 1.16$. Also, you can see that we employed $100k\Omega$ in input terminals for accurate and reliable signal.

Frequency range would be of (for constant capacitance at $50nF$),

$$f_{min} = \frac{1}{2 \times 100k \times 50n} \approx 100hz$$

$$f_{max} = \frac{1}{2 \times 100 \times 50n} \approx 100kHz$$

2.3 Block 3: Triangluar Wave generator

We basically extend block 2 with integrator circuit. Which would give triangular wave as intended. Here, this integrator circuit differs from basic circuit that $100k\Omega$ as feedback resistor is joined. Which would give better stability and accurate output. Circuit diagram is shown below,

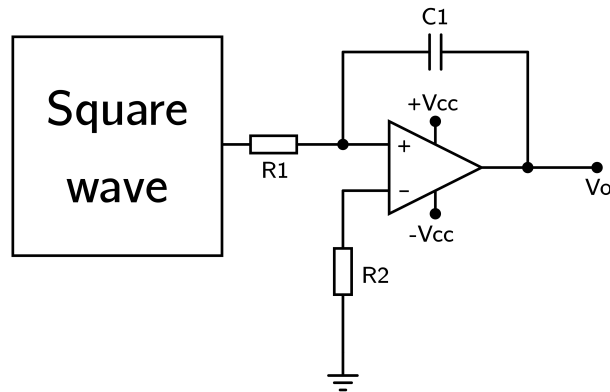


Figure 6: integrator circuit with square wave as input

Here, R_4 have to be $10R_3$. Frequency is give by same relation as block 2.

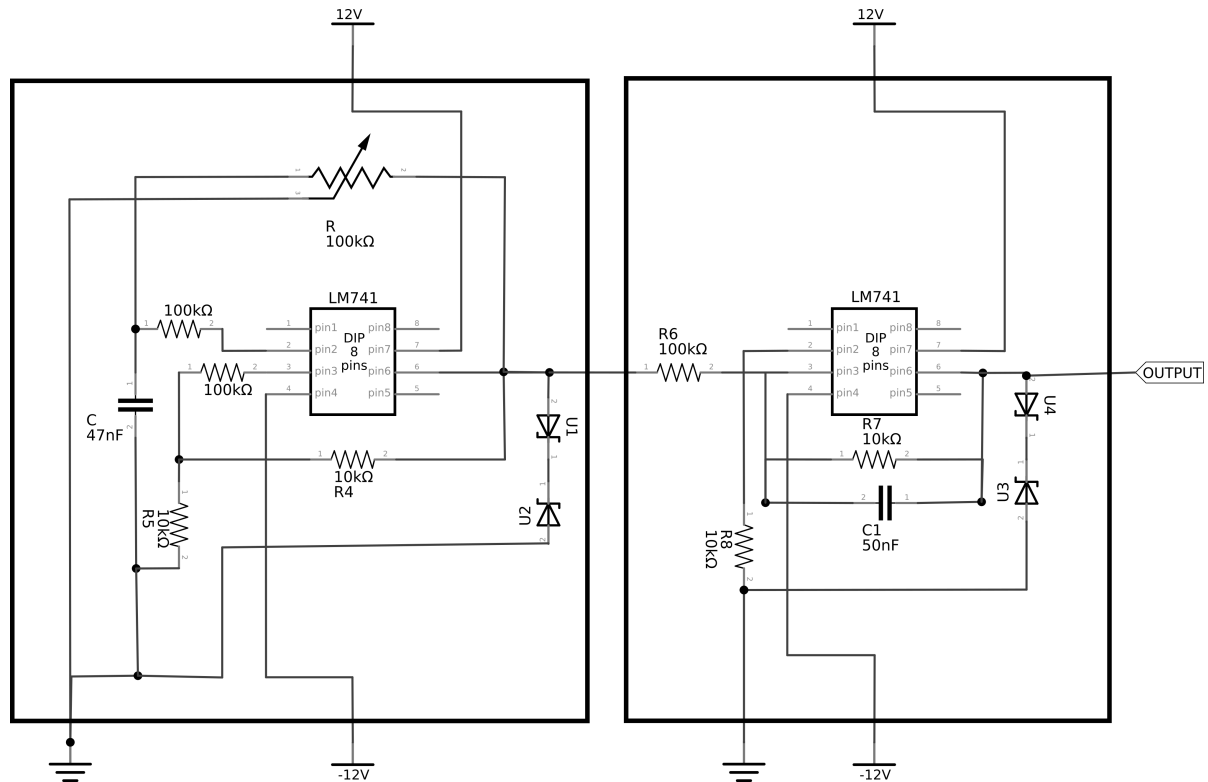


Figure 7: block 3: triangular wave generator

3 connection and switching

For connection of all this block we have used push pull button with two poles. One for power controlling and other for output controlling. Basic diagram of this switch is drawn in figure below.

When switch is **ON** (means pushed) it will connect 1 terminals with common and complete the circuit. When switch is **OFF** (pulled condition), the circuit will open and we will not get connection.

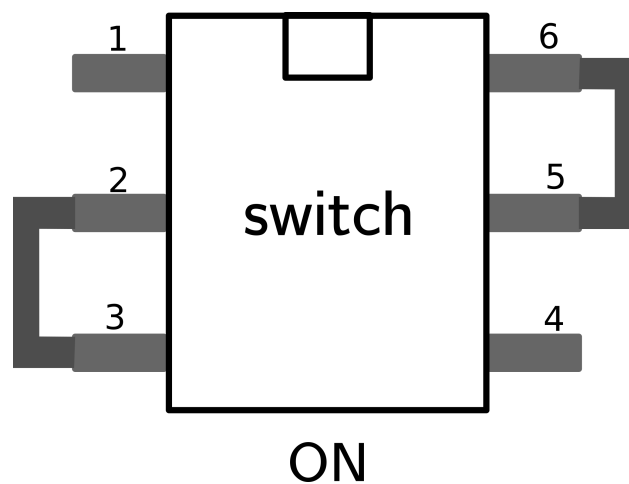


Figure 8: switch on state for two pole push button switch

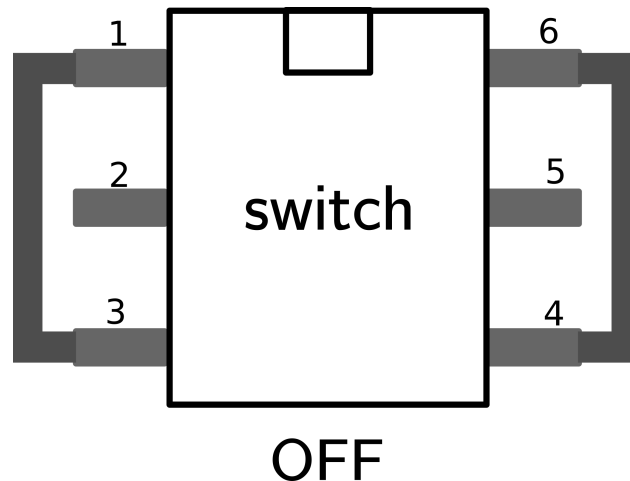


Figure 9: switch off state for two pole push button switch

The +Vcc in common (upper common) is completely independent of Output terminal common (lower common). Which means switch can completely operate two tasks, which is when on it power the block and take output and give to CRO. You can see this is on block diagram in figure 1 .

4 Output

The output of whole circuit which is single output after connecting switches, will goes to CRO (cathode ray oscilloscope), which will measure gain and show signal form.

Theoretically, it should give exact signal but errors from ICs, connections, components are reasonable. We are tried our best to minimize it with simulation in Pspice simulations. But real life and simulations are distanced things. We are expected to see some divergence.

5 Pspice simulations

We did Pspice simulation In This website by Paul Falsted. Here are simulations result from different blocks. This outputs are for Potentiometer valued at $3.3k\Omega$. We gain peek to peek voltage value at $2.8917V$ for sine wave and $2.11V$ and 2.2 in square wave and triangular wave respectively. This figures are from matplotlib, since we could not get from falsted. We got accurate p-p voltages.

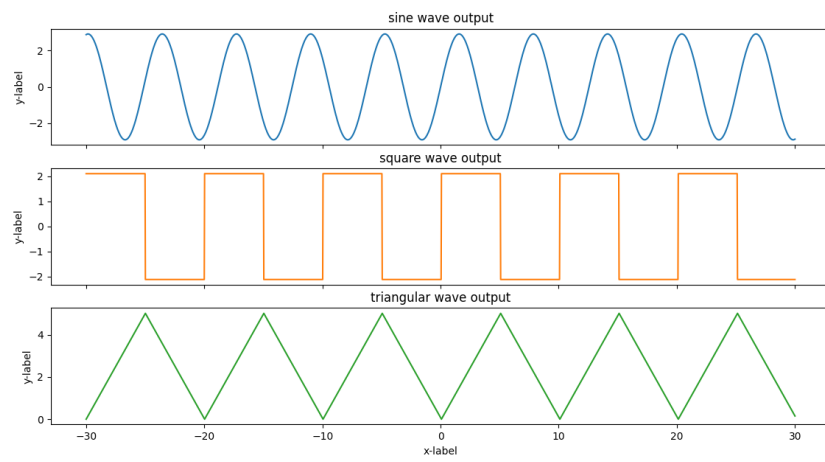


Figure 10: Outputs

References