

VIJAY KUMAR PATNEEDI

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Resume Web page: <https://vijaypatneedi.github.io/>

EDUCATION

Bachelors, Electrical & Electronics Engineering, **NIT Warangal** CGPA: **8.00**
(2013 – 2017)

Aditya Junior College, Board of Intermediate, A.P., (2010 – 2012) Percentage: **96.6**

Pragati High School, Board of Secondary Education Percentage: **93.5**
A.P., (2009 – 2010)

PROFESSIONAL CAREER

Data Science Intern, iNeuron.ai Jan 2020-June 2020

Senior Design Engineer, Larsen & Toubro July 2017 – Dec 2019

Student Trainee, Samsung R&D Bangalore June 2016 - July 2016

SKILLS:

- Machine learning – Numpy, Pandas, Matplotlib, Scikit learn, Seaborn.
- Deep learning – Numpy, Tensorflow, PyTorch, Keras.
- IDE- Jupyter, PyCharm, VSCode.
- Microsoft Excel, Word, PowerPoint and other office suite.

PRESENT WORK EXPERIENCE:

DATA SCIENCE INTERN, iNeuron.ai, BENGALURU.

- With keen interest on mathematics and problem solving, I've transitioned into data science domain from my previous core-engineering domain.
- Worked on Machine learning models, developed understanding on CNNs, RNNs, and other variants of deep learning architectures including reinforcement learning techniques.
- Looking actively to work on projects to enhance my understanding.

PERSONAL PROJECTS:

- Time Series: Predicting stock prices using RNN-LSTM for univariate / multivariate data.
- Machine learning: Quora question pairs similarity, sentiment analysis.
- Deep learning: Object-detection using CNN architectures, Yolo-v3, tensor flow.
- Human Activity Recognition: Use Machine learning to recognize various activities by taking activity recognition of different individuals. Algorithms: Logistic Regression, KNN, Random forest classifier.

PREVIOUS RESPONSIBILITIES:

SENIOR DESIGN ENGINEER, LARSEN & TOUBRO, DELHI

- Developing technical designs, specifications of electrical traction systems for Indian Railways to ensure that installation and operations conform to standards and requirements.
- Understanding and implementing the newly designed 2x25kV system for DFCC segment in Indian Railways.
- Auto CAD, Auto LISP programming for fast and error proof designs.

STUDENT TRAINEE, SAMSUNG R&D BENGALURU, BENGALURU, KARNATAKA

- Test and verify hardware and support peripherals to ensure that they meet specifications and requirements, by recording and analyzing test data in System Verilog language.

ACHIEVEMENTS:

- **Math enthusiast** and been an active Instructor for students ranging from Class 9-Class 12 during the years 2015-2017.
- Active member and '**C**' Certificate holder in National Cadet Corps (**NCC**) NIT Warangal 2013-17.
- Attained All India Rank **2004** in Open Category in JEE mains'13 and 7k rank in JEE Advanced out of 14 lakh students.
- Achieved state rank **905** in EAMCET among 5 lakh students.
- Stood District **1st** in Interstate Mathematics competition held by **Ramanujan academy** in class X.
- Champion in **Badminton** Tournament for L&T Sports Week – 2019, Runner up in Chess.