

S9300-32D

HW Change List from Alpha to Beta



2019.09.06

HW Change List from Alpha to Beta (1/1)

Item	Issue	Description	Root Cause	Type	Solution
1	P1V05 no output	The P1V05 power does not normally output	Design mistake	Layout modification	1. Fix P1V05 BIAS pins tied to P5V_SB power rails.
2	Fan speed detect fail	The fan4/5 can't detect fan speed	Design mistake	Layout modification	1. Modify fan tach circuit.
3	New Function design to add independent P1V8_A_EN signal	New function design	New function design	Layout modification	1. Add independent P1V8_A_EN signal.
4	P0V8_TRVD can't monitor by BMC	BMC can't monitor P0V8_TRVD voltage	There is no signal connection between BMC and P0V8_TRVD platform reset pins	Layout modification	1. Add power signal connect to BMC.
5	New Function design to add Efuse I2C interface	New function design	New function design	Layout modification	1. Add I2C interface for Efuse.
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