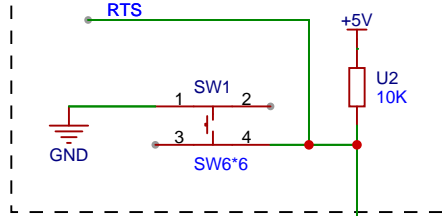
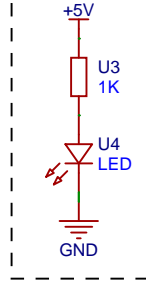


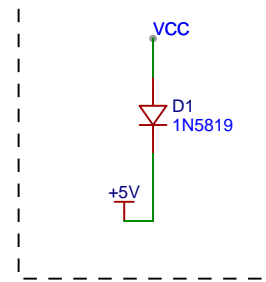
RESET LOGIC



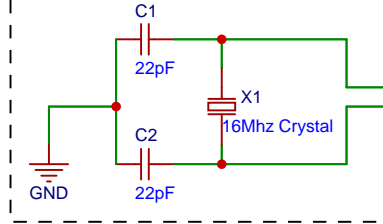
STANDBY LED



PROTECTION (POLARITY)



TIMING



U1
 (PCINT14/RESET#)PC6
 (PCINT16/RXD)PD0
 (PCINT17/TXD)PD1
 (PCINT18/INT0)PD2
 (PCINT19/OC2B/INT1)PD3
 (PCINT20/XCK/T0)PD4
 VCC
 GND
 (PCINT6/XTAL1/TOSC1)PB6
 (PCINT7/XTAL2/TOSC2)PB7
 (PCINT21/OC0B/T1)PD5
 (PCINT22/OC0A/AIN0)PD6
 (PCINT23/AIN1)PD7
 (PCINT0/CLKO/ICP1)PB0
 ATMEGA328P-PN

PC5(ADC5/SCL/PCINT13)
 PC4(ADC4/SDA/PCINT12)
 PC3(ADC3/PCINT11)
 PC2(ADC2/PCINT10)
 PC1(ADC1/PCINT9)
 PC0(ADC0/PCINT8)
 GND
 AREF
 AVCC
 PB5(SCK/PCINT5)
 PB4(MISO/PCINT4)
 PB3(MOSI/OC2A/PCINT3)
 PB2(SS#/OC1B/PCINT2)
 PB1(OC1A/PCINT1)

U6
 ANALOG_BREAKOUT
 1
 2
 3
 4
 5
 6

U5
 DIGITAL_BREAKOUT
 1
 2
 3
 4
 5
 6
 7
 8
 9
 10
 11
 12
 13
 14

