

Operating Systems

Virtual Memory

Virtualizing Memory

- ▣ The physical memory is *an array of bytes*.
- ▣ A program keeps all of its data structures in memory.
 - ◆ **Read memory** (load):
 - Specify an address to be able to access the data
 - ◆ **Write memory** (store):
 - Specify the data to be written to the given address

Virtualizing Memory (Cont.)

■ A program that Accesses Memory (mem.c)

```
1      #include <unistd.h>
2      #include <stdio.h>
3      #include <stdlib.h>
4      #include "common.h"
5
6      int
7      main(int argc, char *argv[])
8      {
9          int *p = malloc(sizeof(int)); // a1: allocate some
                                         memory
11         printf("(%d) address of p: %p\n",
12             getpid(), (unsigned) p); // a2: print out the
                                         address of the memory
13         *p = 0; // a3: put zero into the first slot of the memory
14         while (1) {
15             *p = *p + 1;
16             printf("(%d) p: %d\n", getpid(), *p); // a4
17         }
18         return 0;
19     }
20 }
```

Virtualizing Memory (Cont.)

- The output of the program `mem.c`

```
prompt> ./mem
(2134) memory address of p: 00200000
(2134) p: 1
(2134) p: 2
(2134) p: 3
(2134) p: 4
(2134) p: 5
^C
```

- ◆ The newly allocated memory is at address `00200000`.
- ◆ It updates the value and prints out the result.

Virtualizing Memory (Cont.)

▣ Running `mem.c` multiple times

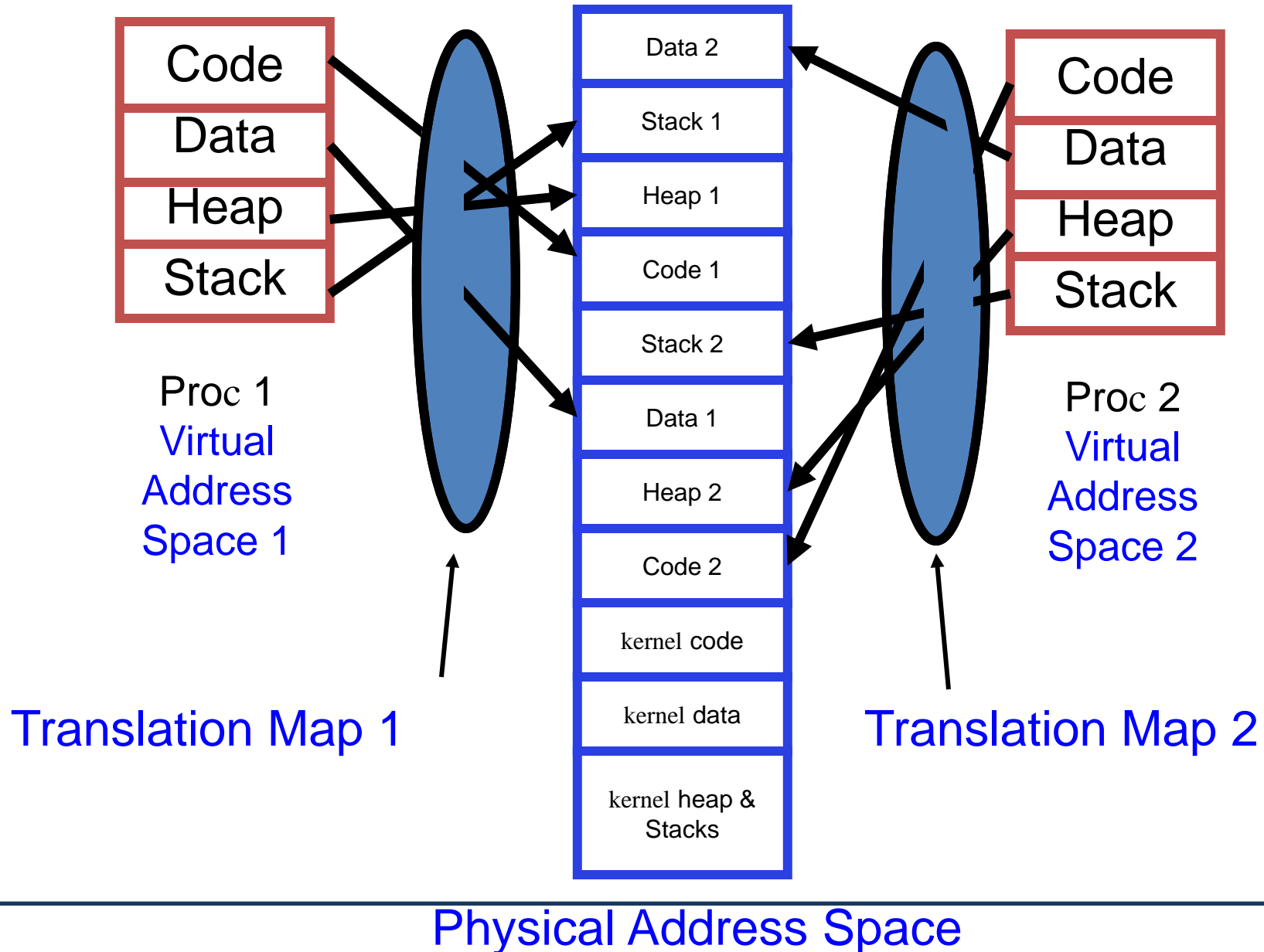
```
prompt> ./mem & ./mem &  
[1] 24113  
[2] 24114  
(24113) memory address of p: 00200000  
(24114) memory address of p: 00200000  
(24113) p: 1  
(24114) p: 1  
(24114) p: 2  
(24113) p: 2  
(24113) p: 3  
(24114) p: 3  
...
```

- ◆ It is as if each running program has its **own private memory**.
 - Each running program has allocated memory at the same address.
 - Each seems to be updating the value at `00200000` independently.

Virtualizing Memory (Cont.)

- ▣ Each process accesses its own private **virtual address space**.
 - ◆ The OS maps **address space** onto the **physical memory**.
 - ◆ A memory reference within one running program does not affect the address space of other processes.
 - ◆ Physical memory is a shared resource, managed by the OS.

Providing the Illusion of Separate Address Spaces



Memory Virtualization

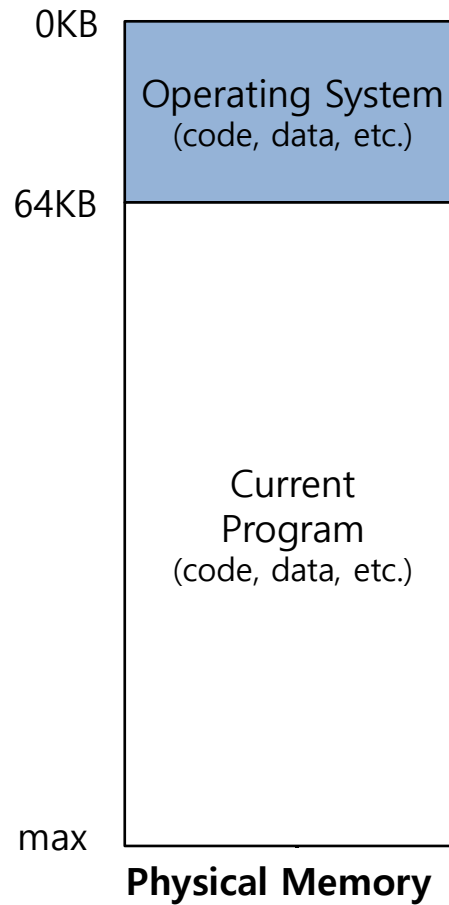
- ▣ What is **memory virtualization**?
 - ◆ OS virtualizes its physical memory.
 - ◆ OS provides an **illusion memory space** per each process.
 - ◆ It seems to be seen like **each process uses the whole memory**.

Benefit of Memory Virtualization: A Beautiful Illusion

- ▣ **Transparency**: ease of use in programming
- ▣ Memory **efficiency** in terms of **times** and **space**
- ▣ The guarantee of isolation for processes as well as OS
 - ◆ Protection from **errant accesses** of other processes

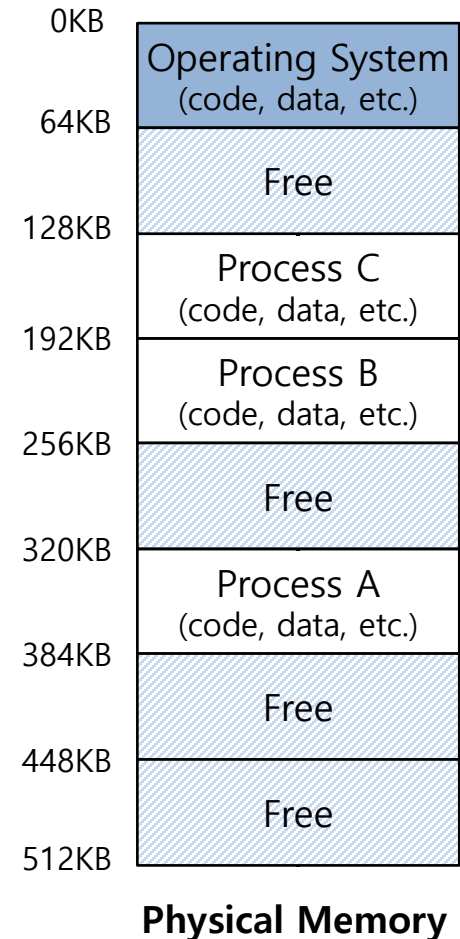
OS in The Early System

- ▣ Load only one process in memory.
 - ◆ Poor utilization and efficiency



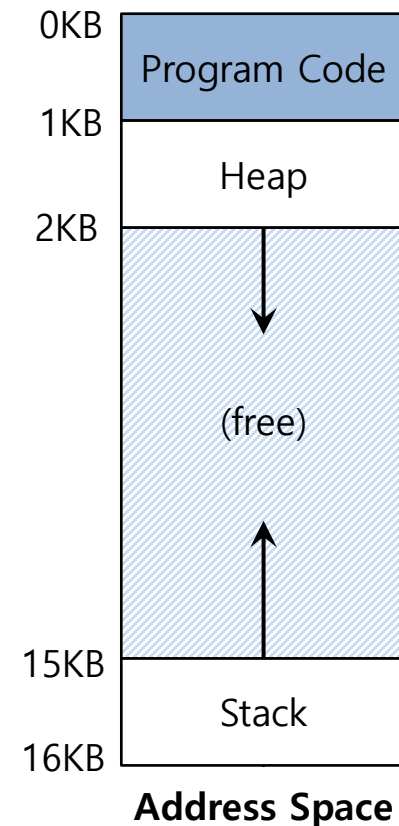
Multiprogramming and Time Sharing

- ❑ **Load multiple processes** in memory.
 - ◆ Execute one for a short while.
 - ◆ Switch processes between them in memory.
 - ◆ Increase utilization and efficiency.
- ❑ Cause an important **protection issue**.
 - ◆ Errant memory accesses from other processes



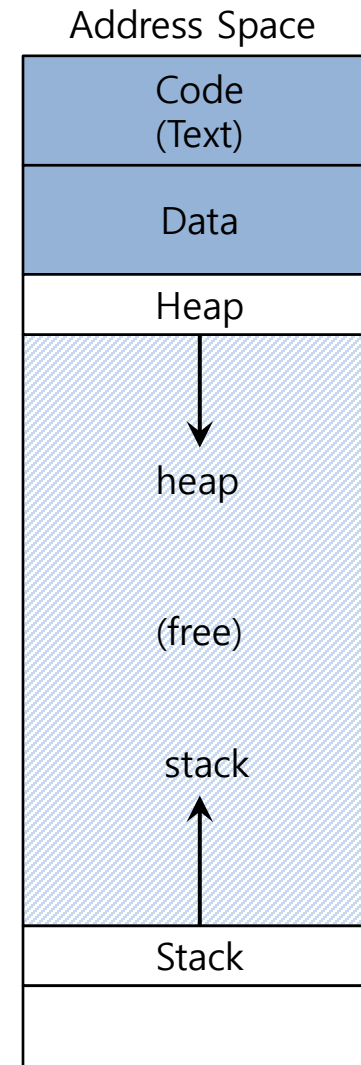
Address Space

- ❑ OS creates an **abstraction** of physical memory.
 - ◆ The address space contains all about a running process.
 - ◆ It is the running program's view of memory in the system
 - ◆ That is consist of program code, heap, stack and etc.
 - ◆ The program really isn't in memory at physical addresses 0 through 16KB.
 - ◆ Rather it is loaded at some arbitrary physical address(es).



Virtual Address(Cont.)

- ▣ Code
 - ◆ Where instructions live
- ▣ Data
 - ◆ Store global and static variables
- ▣ Heap
 - ◆ Dynamically allocate memory.
 - `malloc` in C language
 - `new` in object-oriented language
- ▣ Stack
 - ◆ Store return addresses or values.
 - ◆ Contain local variables arguments to routines.



Virtual Address

- ▣ **Every address** in a running program is virtual.
 - ◆ OS translates the virtual address to physical address

```
#include <stdio.h>
#include <stdlib.h>

int main(int argc, char *argv[]){

    printf("location of code   : %p\n", (void *) main);
    printf("location of heap   : %p\n", (void *) malloc(1));
    int x = 3;
    printf("location of stack  : %p\n", (void *) &x);

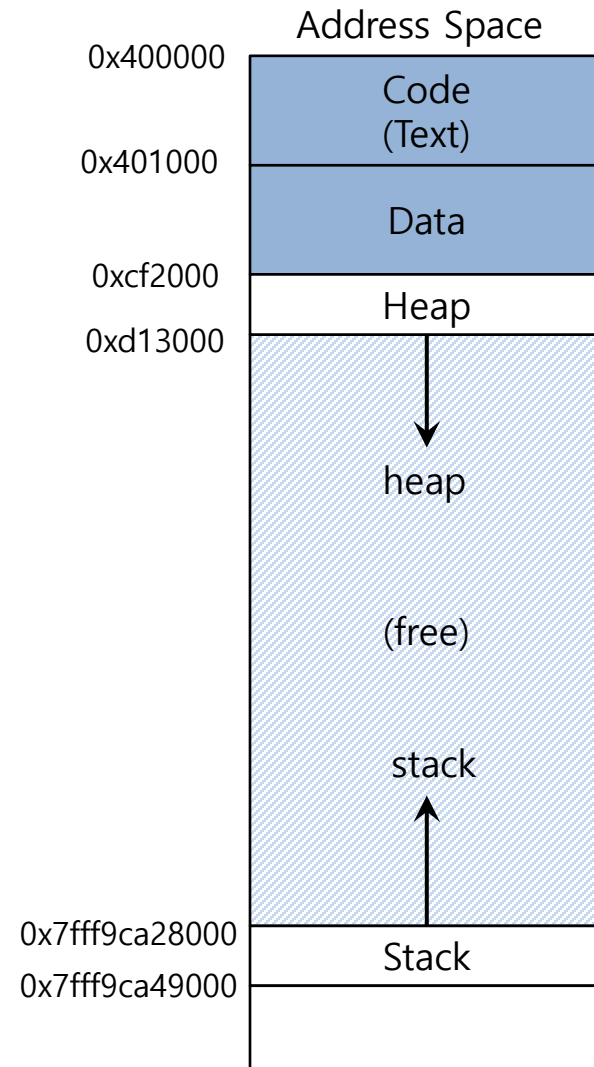
    return x;
}
```

A simple program that prints out addresses

Virtual Address(Cont.)

▣ The output in 64-bit Linux machine

```
location of code   : 0x40057d  
location of heap   : 0xcf2010  
location of stack  : 0x7fff9ca45fcc
```



Address Translation

- ▣ In memory virtualizing, efficiency and control are attained by hardware support.
 - ◆ e.g., registers, TLB(Translation Look-aside Buffer)s, page-table

Address Translation

- ▣ Hardware (Memory Management Unit (MMU)): transforms a **virtual address** to a **physical address**.
 - ◆ The desired information is actually stored in a physical address.
- ▣ The OS must get involved at key points to set up the hardware.
 - ◆ The OS must manage memory to intervene.

Example: Address Translation

▣ C - Language code

```
void func()  
    int x;  
    ...  
    x = x + 3; // this is the line of code we are interested in
```

- ◆ **Load** a value from memory
- ◆ **Increment** it by three
- ◆ **Store** the value back into memory

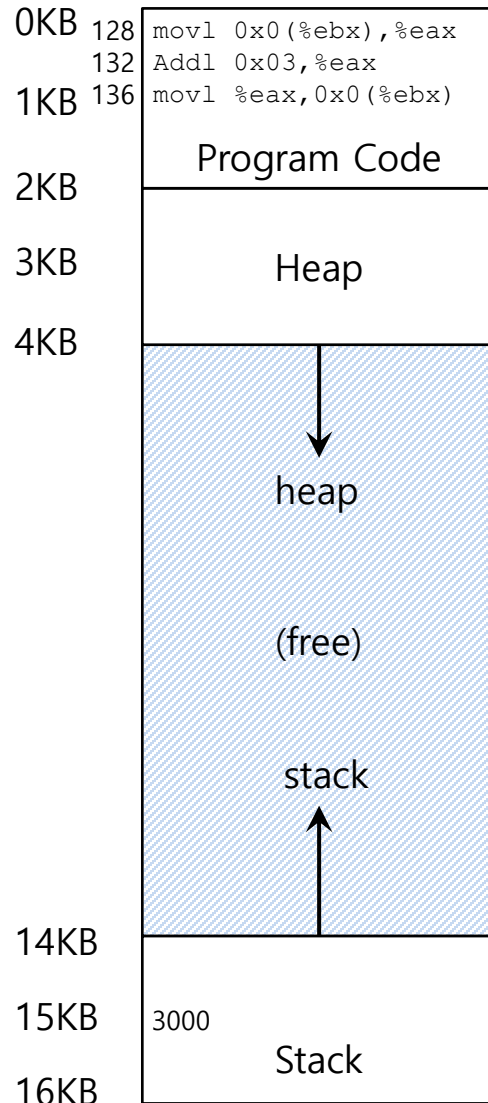
Example: Address Translation(Cont.)

▣ Assembly

```
128 : movl 0x0(%ebx), %eax      ; load 0+ebx into eax
132 : addl $0x03, %eax         ; add 3 to eax register
136 : movl %eax, 0x0(%ebx)      ; store eax back to mem
```

- ◆ **Load** the value at that address into `eax` register.
- ◆ **Add** 3 to `eax` register.
- ◆ **Store** the value in `eax` back into memory.

Example: Address Translation(Cont.)

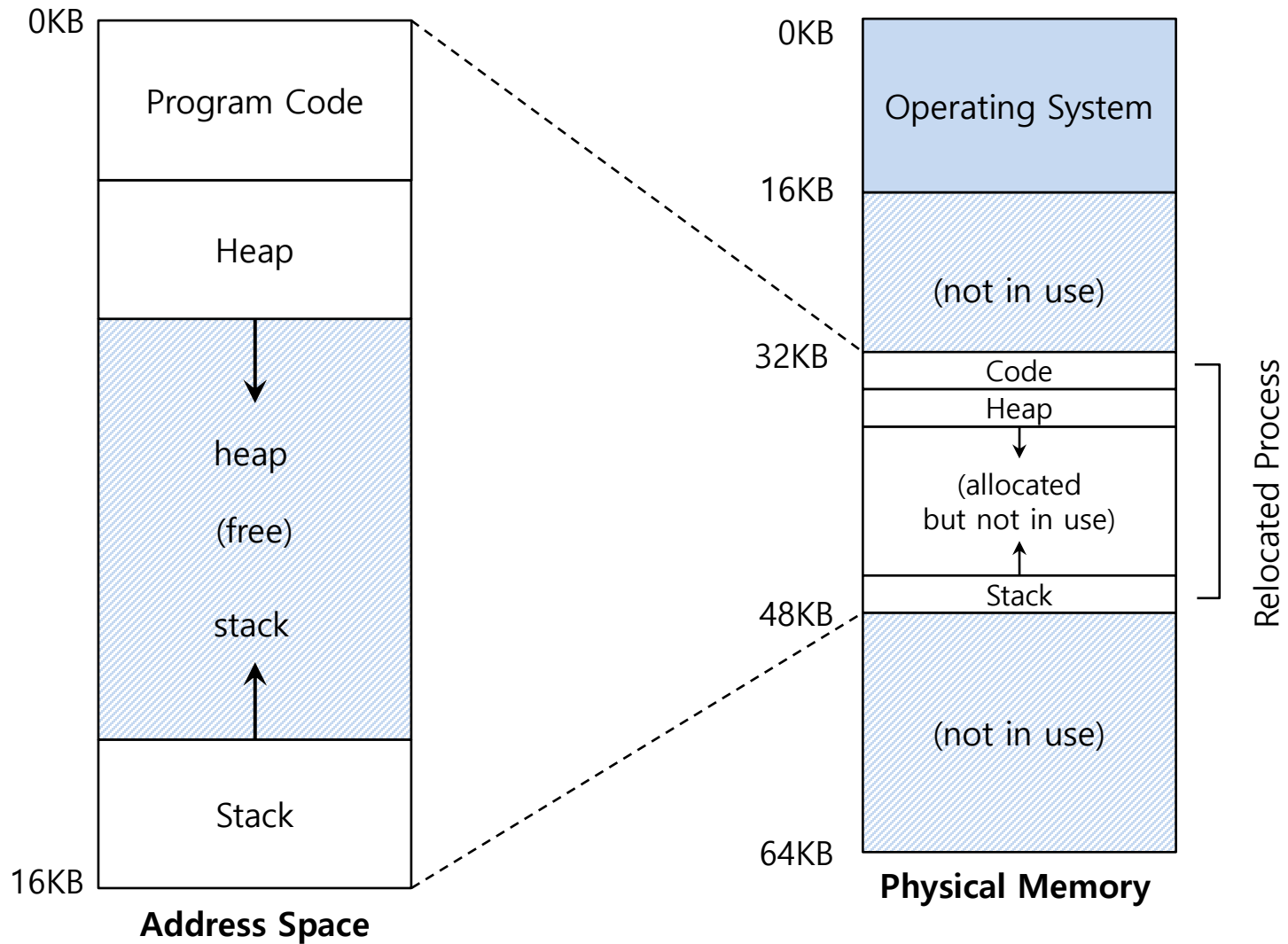


- Fetch instruction at address 128
- Execute this instruction (load from address 15KB)
- Fetch instruction at address 132
- Execute this instruction (no memory reference)
- Fetch the instruction at address 136
- Execute this instruction (store to address 15 KB)

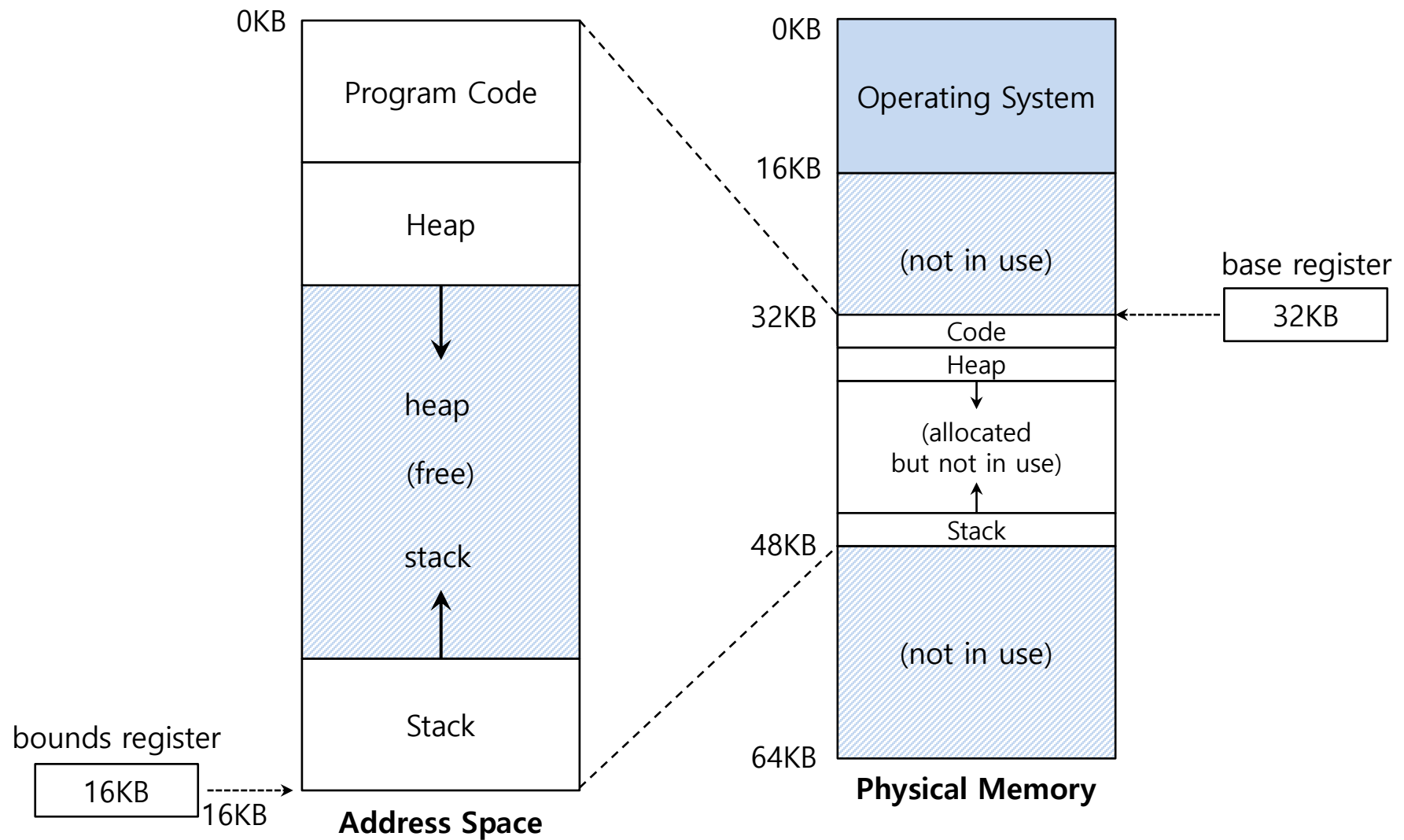
Relocation Address Space

- ▣ The OS wants to place the process **somewhere else** in physical memory, not at address 0.
 - ◆ The address space start at address 0.

A Single Relocated Process



Base and Bounds Register: A simple implementation



Dynamic(Hardware base) Relocation

- When a program starts running, the OS decides **where** in physical memory a process should be **loaded**.
- we'll need two hardware registers within each CPU
 - ◆ Set the **base** register a value.

$$\text{physical address} = \text{virtual address} + \text{base}$$

- ◆ Every virtual address must **not be greater than bound** and **negative**.

$$0 \leq \text{virtual address} < \text{bounds}$$

Relocation and Address Translation

128 : movl 0x0(%ebx), %eax

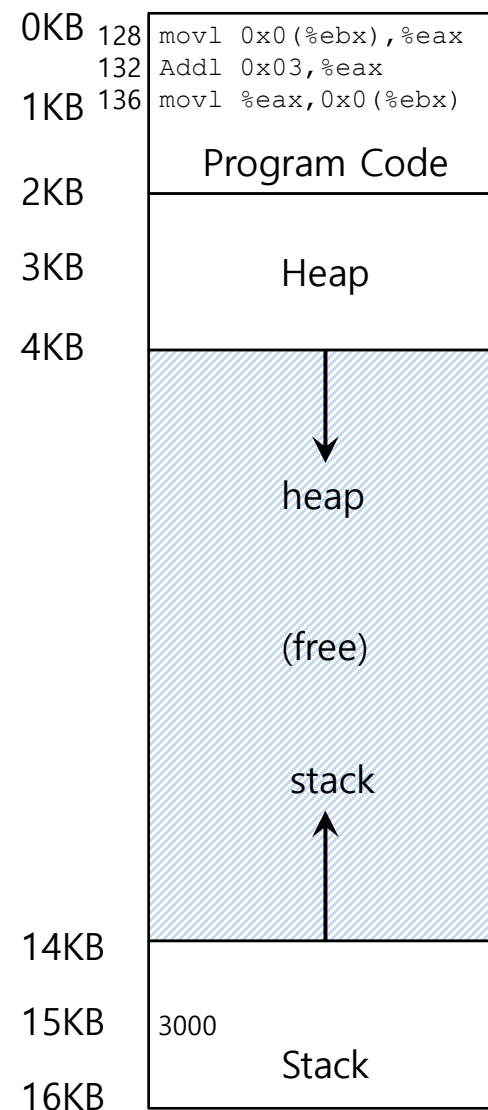
- ◆ **Fetch** instruction at address 128

$$32896 = 128 + 32KB(base)$$

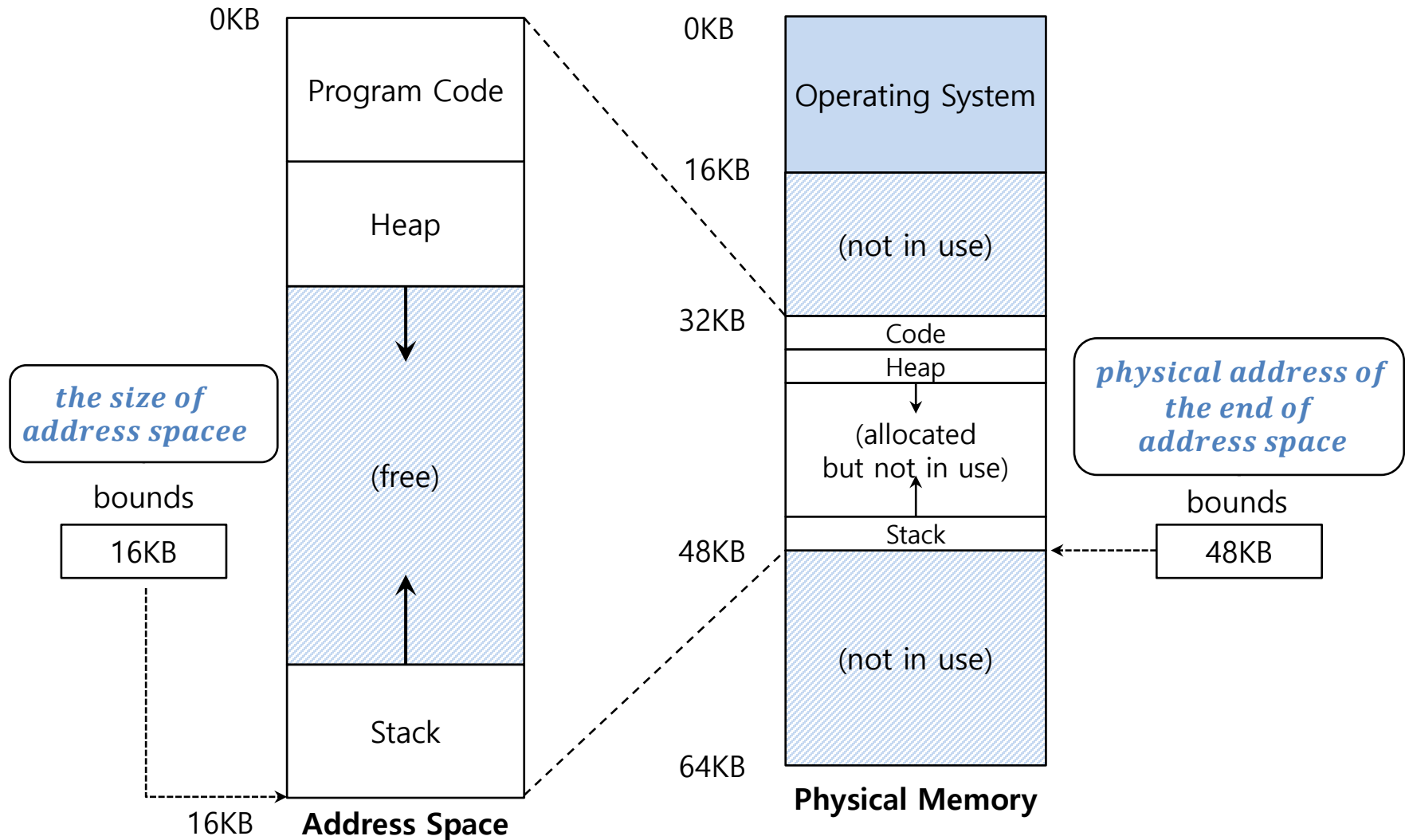
- ◆ **Execute** this instruction

- Load from address 15KB

$$47KB = 15KB + 32KB(base)$$



Two ways of Bounds Register

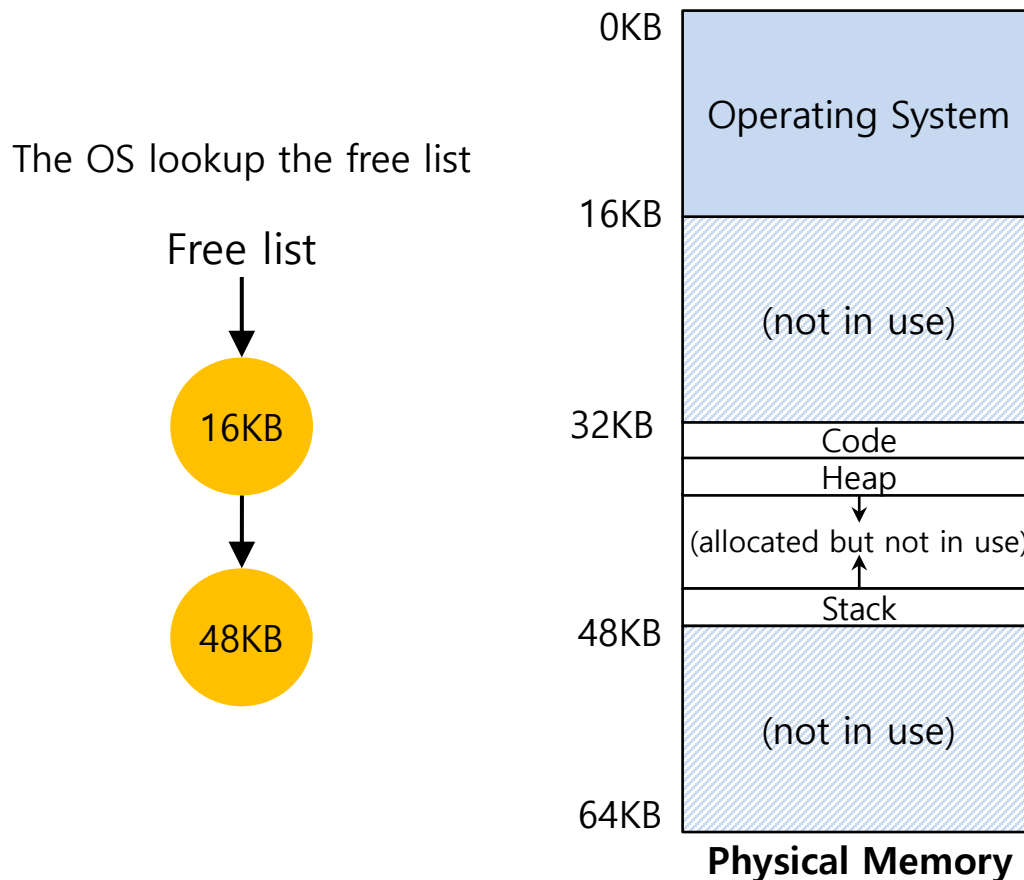


OS Issues for Memory Virtualizing

- ▣ The OS must **take action** to implement **base-and-bounds** approach.
- ▣ Three critical junctures:
 - ◆ When a process **starts running**:
 - Finding space for address space in physical memory
 - ◆ When a process is **terminated**:
 - Reclaiming the memory for use
 - ◆ When context **switch occurs**:
 - Saving and storing the base-and-bounds pair

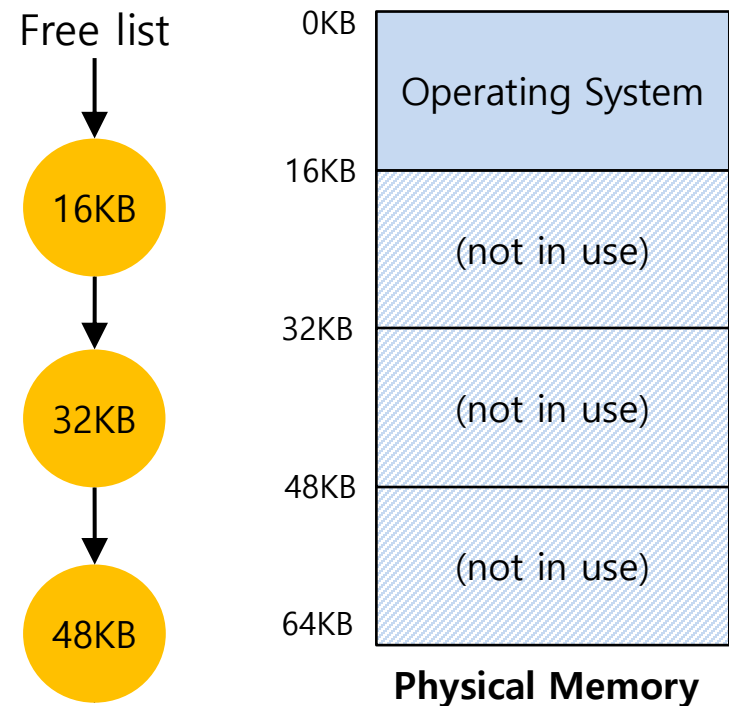
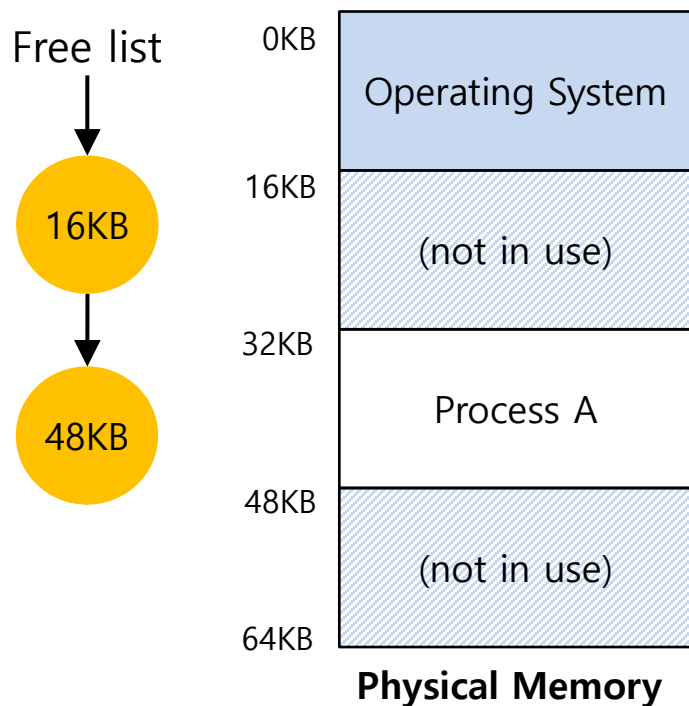
OS Issues: When a Process Starts Running

- ▣ The OS must **find a room** for a new address space.
 - ◆ free list : A list of the range of the physical memory which are not in use.



OS Issues: When a Process Is Terminated

- ▣ The OS must **put the memory back** on the free list.



OS Issues: When Context Switch Occurs

- The OS must **save and restore** the base-and-bounds pair.
 - ◆ In **process structure** or **process control block(PCB)**

