

L10  
PQINN  
CH4

1hr  
lecture

# CIRCUIT SATISFIABILITY ①

example of ~~an~~ an NP-Hard pb.

we do not ~~know~~ know of an algorithm to solve it in polynomial time  $O(n^c)$   
((JACK EDMONDS))  $n$  = size of the input

assumption: there are no loops in the circuit

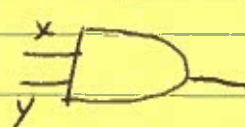
INPUT: set of  $m$  boolean (T/F) values  
 $x_1, \dots, x_n$

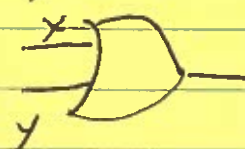
OUTPUT: a single boolean value


for specific input values, we can calculate the output of the circuit in polynomial (LINEAR) time using DFS (depth first search), since we

can compute the output of a  $k$ -input gate in  $O(k)$  time

## 3 types of gates:

AND GATE   $x \wedge y$

OR GATE   $x \vee y$

NOT GATE   $\bar{x}$



(2)

## CircuitSat pb:

given a boolean circuit,  
is there an input that  
makes the circuit output T  
(or the circuit always output F)

naïve solution: try all  $2^m$  possible  
inputs exhaustively  
→ exponential time

no other <sup>exact</sup> algorithm is available.  
local search approaches are available

Cook-Levin THM CircuitSat is  
NP-complete

case study: specific circuit with  
16 inputs labeled a-p  
each input takes 2 values 0, 1  
→  $2^{16} = 65,536$  possible inputs

## summary of the program design:

we will determine whether the  
circuit is satisfiable by considering  
all 65,536 possible input  
combinations

the combinations will be  
allocated to the p proc in a  
cyclic fashion, that output T  
and print the ones  
every proc will examine its combination

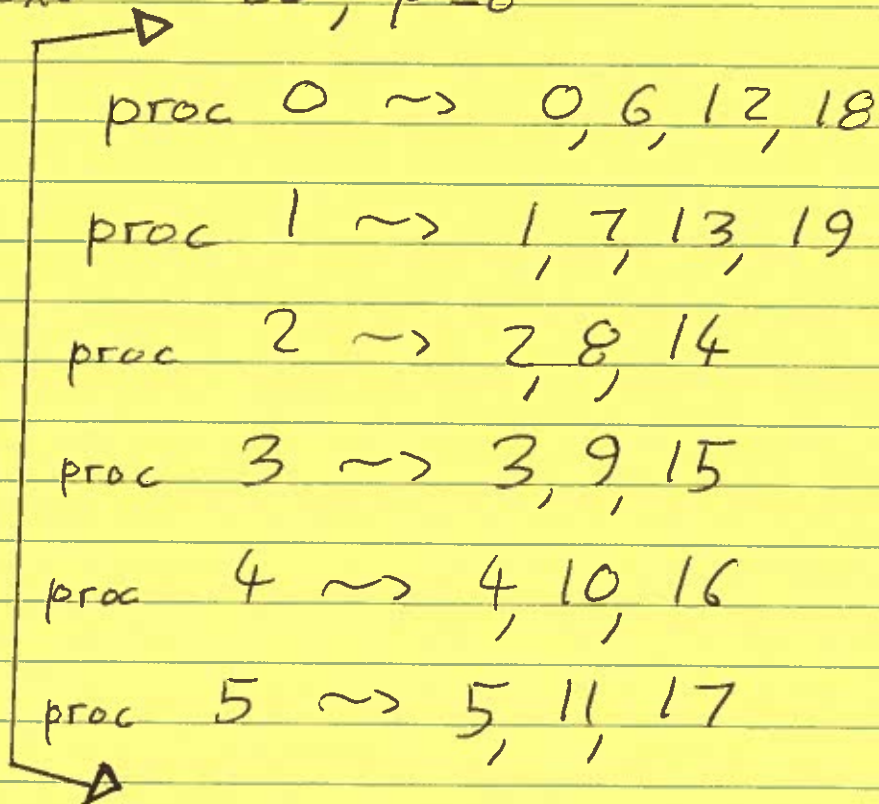
3

"cyclic fashion"

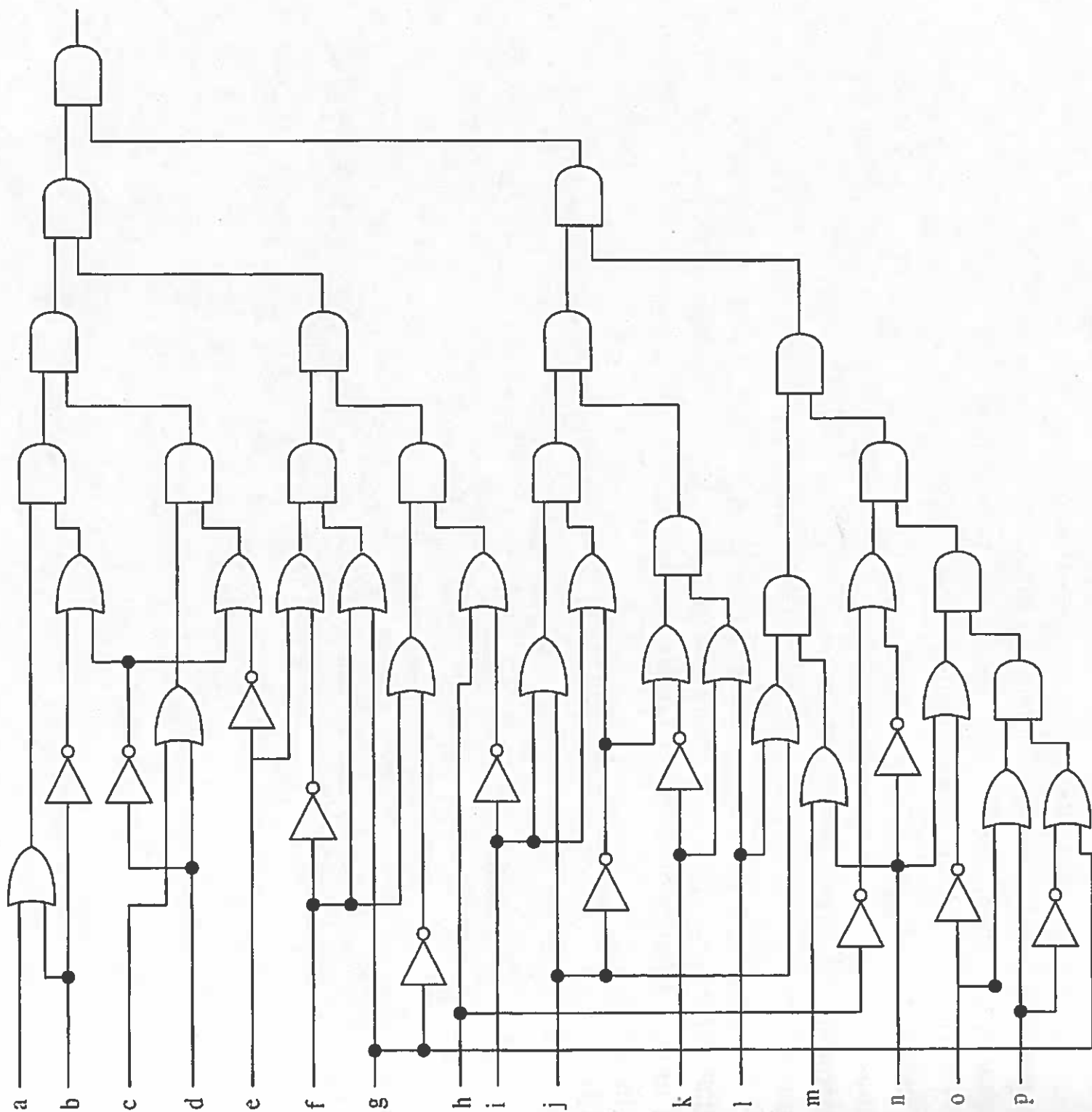
$n$  tasks,  $0, \dots, n-1$  are assigned to  
 $p$  procs,  $0, \dots, p-1 \rightsquigarrow$

task  $k$  is assigned to proc  $k \bmod p$

ex.  $n=20, p=6$



practical application of  
Circuit Set: design and  
verification of  
logical devices





sat1.c

```
/*
 * Circuit Satisfiability, Version 1
 *
 * This MPI program determines whether a circuit is
 * satisfiable, that is, whether there is a combination of
 * inputs that causes the output of the circuit to be 1.
 * The particular circuit being tested is "wired" into the
 * logic of function 'check_circuit'. All combinations of
 * inputs that satisfy the circuit are printed.
 *
 * Programmed by Michael J. Quinn
 *
 * Last modification: 3 September 2002
 */
```

```
#include "mpi.h"
#include <stdio.h>
```

```
int main (int argc, char *argv[]) {
    int i;
    int id;          /* Process rank */
    int p;           /* Number of processes */
    void check_circuit (int, int);

    MPI_Init (&argc, &argv);
    MPI_Comm_rank (MPI_COMM_WORLD, &id);
    MPI_Comm_size (MPI_COMM_WORLD, &p);

    for (i = id; i < 65536; i += p)
        check_circuit (id, i);

    printf ("Process %d is done\n", id);
    fflush (stdout);
    MPI_Finalize();
    return 0;
}
```

```
/* Return 1 if 'i'th bit of 'n' is 1; 0 otherwise */
```

LIVE | SNET |  
DEMO |  
compile  
submit  
execute

```

#define EXTRACT_BIT(n,i) ((n&(1<<i))>0)

void check_circuit (int id, int z) {
    int v[16];    /* Each element is a bit of z */
    int i;

    for (i = 0; i < 16; i++) v[i] = EXTRACT_BIT(z,i);
    if ((v[0] || v[1]) && (!v[1] || !v[3]) && (v[2] || v[3])
        && (!v[3] || !v[4]) && (v[4] || !v[5])
        && (v[5] || !v[6]) && (v[5] || v[6])
        && (v[6] || !v[15]) && (v[7] || !v[8])
        && (!v[7] || !v[13]) && (v[8] || v[9])
        && (v[8] || !v[9]) && (!v[9] || !v[10])
        && (v[9] || v[11]) && (v[10] || v[11])
        && (v[12] || v[13]) && (v[13] || !v[14])
        && (v[14] || v[15])) {
        printf ("%d) %d%d%d%d%d%d%d%d%d%d%d%d%d%d\n", id,
            v[0],v[1],v[2],v[3],v[4],v[5],v[6],v[7],v[8],v[9],
            v[10],v[11],v[12],v[13],v[14],v[15]);
        fflush (stdout);
    }
}

```

sat2.c

```
/*
 * Circuit Satisfiability, Version 2
 *
 * This enhanced version of the program prints the
 * total number of solutions.
 */

#include "mpi.h"
#include <stdio.h>

int main (int argc, char *argv[]) {
    int count;          /* Solutions found by this proc */
    int global_count;   /* Total number of solutions */
    int i;
    int id;             /* Process rank */
    int p;              /* Number of processes */
    int check_circuit (int, int);

    MPI_Init (&argc, &argv);
    MPI_Comm_rank (MPI_COMM_WORLD, &id);
    MPI_Comm_size (MPI_COMM_WORLD, &p);

    count = 0;
    for (i = id; i < 65536; i += p)
        count += check_circuit (id, i);

    MPI_Reduce (&count, &global_count, 1, MPI_INT, MPI_SUM, 0,
        MPI_COMM_WORLD);
    printf ("Process %d is done\n", id);
    fflush (stdout);
    MPI_Finalize();
    if (!id) printf ("There are %d different solutions\n",
        global_count);
    return 0;
}

/* Return 1 if 'i'th bit of 'n' is 1; 0 otherwise */
#define EXTRACT_BIT(n,i) ((n&(1<<i))>0)
```

LIVE  
DEMO  
snr

compile
submit
execute

```

int check_circuit (int id, int z) {
    int v[16];    /* Each element is a bit of z */
    int i;

    for (i = 0; i < 16; i++) v[i] = EXTRACT_BIT(z,i);
    if ((v[0] || v[1]) && (!v[1] || !v[3]) && (v[2] || v[3])
        && (!v[3] || !v[4]) && (v[4] || !v[5])
        && (v[5] || !v[6]) && (v[5] || v[6])
        && (v[6] || !v[15]) && (v[7] || !v[8])
        && (!v[7] || !v[13]) && (v[8] || v[9])
        && (v[8] || !v[9]) && (!v[9] || !v[10])
        && (v[9] || v[11]) && (v[10] || v[11])
        && (v[12] || v[13]) && (v[13] || !v[14])
        && (v[14] || v[15])) {
        printf ("%d) %d%d%d%d%d%d%d%d%d%d%d%d%d%d%d\n", id,
            v[0],v[1],v[2],v[3],v[4],v[5],v[6],v[7],v[8],v[9],
            v[10],v[11],v[12],v[13],v[14],v[15]);
        fflush (stdout);
        return 1;
    } else return 0;
}

```



(4)

program "sat2.c"  $\rightarrow$  count the total #sol<sup>s</sup>

collective communication:

a group of proc<sup>s</sup> works together to distribute or gather a set of one or more values

REDUCTION is a collective communication operation

$\rightarrow$  modify check\_circuit so that it returns  $\begin{cases} 0 & \text{combination does not satisfy} \\ 1 & \text{combination does satisfy} \end{cases}$

$\rightarrow$  modify loop to accumulate the #valid sol.<sup>s</sup> for each proc.

after a proc has completed its work it will participate in the REDUCTION operation.

MPI\_Reduce performs one or more reduction operations on values submitted by all the proc<sup>s</sup> in a comm/ter.

~~int MPI\_Reduce~~

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int MPI\_Reduce ( 7 params

```
void *operand, /* addr of 1st reduction elm */
void *result, /* " " " " reduction result */
int count, /* reductions to perform */
MPI_Datatype type, /* type of elms */
MPI_Op operator, /* reduction operator */
int root, /* process getting result */
MPI_Comm comm) /* comm for */
```

list of other built-in REDUCTION ops  $\rightarrow$  RTM MPI\_PROD etc