













a(35) \_\_n0464(1:0) n0435(1:0) a(32) a(32),\_n0439(0) n0439(1:0) \_\_n0468(1:0) a(33) a(63:0) \_\_\_\_n0472(1:0) XOR3 \_\_n0476(1:0) n0443(1:0) a(13), n0476(0) \_\_n0480(1:0) a(28) \_n0484(1:0) a(0)...\_n0504(1)a(25) a(25),\_n0452(0) n0452(1:0) a(9),\_n0484(0) \_n0488(1:0) a(26) a(23) \_n0492(1:0) n0456(1:0) a(24),\_n0456(1) a(5),\_n0492(0) \_n0496(1:0) a(21)> n0460(1:0) \_n0500(1:0) a(4),\_n0496(1) a(19) n0504(1) \_n0504(1:0) a(20) a(20), n0464(1) a(17) a(18) a(18),\_n0468(1) a(15) a(16),\_n0472(1) a(14),\_n0476(1) a(12)\_n0480(1) a(10),\_n0484(1) a(7),\_n0488(0) a(8),\_n0488(1) a(6),\_n0492(1) a(3),\_n0496(0) n0504(0),\_n0305 a(35),\_n0435(1) a(33),\_n0439(1) \_\_n0448(1) \_\_n0190,\_n0448(0) a(28), n0448(1) a(26), n0452(1) a(23),\_n0456(0) a(22),\_n0460(1)

## a(16) n0472(1:0) MUX outalu(0) a(13) n0476(1:0) a(14) b(0) a(11) n0480(1:0) a(0)...\_n0504(1) a(12) n0065... n0374(19) - NV a(9) n0484(1:0) b(63) - NV a(10) n0007(63:0) - NV a(7)> n0488(1:0) a(8) a(8), n0488(1) a(5) a(5)\_n0492(0) n0492(1:0) a(6) a(6), n0492(1) a(3) -GND a(3), n0496(0) n0496(1:0) a(4) a(4),\_n0496(1) a(63:0) n0504(1) n0500(1:0) n0504(0), n0305

-a(16), n0472(1)
-a(13), n0476(0)
-a(14), n0476(1)
-a(11), n0480(0)
-a(12), n0484(0)
-a(10), n0484(1)
-a(10), n0484(1)
-a(17), n0488(0)

b(63:0) sel(4)...sel(0) n0374(31) n0011(63:0) \_n0010(63:0) n0009(63:0) n0008(63:0) \_n0379(63)...\_n0374(18) n0378(63)...\_n0374(17) b(1) a(1) a(63) n0006(63:0) n0005(63:0) n0004(63:0) n0003(63:0) n0002(63:0)