

Charting the Future of Persistent Memory: Insights from Optane PMEM

Sajad Karim

Background: Non-volatile memory (NVM), or persistent memory, is a promising and emerging storage technology that has not only disrupted the typical long-established memory hierarchy but also invalidated the proclaimed programming paradigm used in traditional database management systems and file systems. [NVM1, NVM2, NVM3, NVM4, IDS1, IDS2, DB1 , DB2 , DB3 , DB4 , DB5]

- It bridges the gap between primary and secondary storage
- Shares the characteristics of both categories.
- Nonvolatile, offers latency close to DRAM, and entails higher capacity than DRAM.
- Byte-addressable

Goal: The primary goal of this exercise is to conduct a comprehensive survey

- Survey of the applications and performance implications of byte-addressable persistent memory (PMEM), with a focus on emerging technologies such as Intel Optane DC Persistent Memory Module.
- Work should aim to systematically review the current state of research, classify application scenarios, and evaluate the design trade-offs associated with different operational modes (e.g., Memory Mode, AppDirect Mode, and mixed configurations).
- Work should synthesize experimental results, benchmark studies, and design techniques from various domains; including in-memory databases, file systems, and high-performance computing
- Provide an in-depth analysis of the impact of DC PMEM on these applications/domains, while also identifying gaps and future research directions.

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Steps that should be considered:

- Literature Review:
 - Define the scope and formulate specific research questions.
 - Collect relevant literature from digital libraries, conferences, and technical reports (e.g., IEEE Xplore, ACM Digital Library, Google Scholar) using defined search keywords (such as “Intel Optane PMem”, “persistent memory”, “byte-addressable NVM”, “NVDIMM”, “persistent memory applications”, etc.).
 - Specify a time range if needed to capture the most recent developments.
- Data Extraction and Classification:
 - Extract key information (e.g., application domain, operational mode, performance metrics) from selected studies.
 - Develop a taxonomy categorizing the applications and methodologies that leverage PMEM.
- Comparative Analysis:
 - Synthesize trends and common findings across the literature.
 - Use tables, graphs, and taxonomies to compare performance outcomes and design trade-offs.
- Gap Analysis and Future Directions:
 - Identify open research challenges and areas where further investigation is needed.
- Paper Write-up:
 - Document the methodology, analysis, findings, and recommendations in a structured thesis format.

References

- [NVM1] A. Faraclas, et al., Modeling of set and reset operations of phasechange memory cells, IEEE electron device letters (2011).
- [NVM2] A. K. Mishra, et al., Architecting on-chip interconnects for stacked 3D STT-RAM caches in CMPs, ISCA, IEEE, 2011.
- [NVM3] B. Gervasi, Will carbon nanotube memory replace DRAM?, IEEE Micro (2019).
- [NVM4] D. B. Strukov, G. S. Snider, D. R. Stewart, R. S. Williams, The missing memristor found, nature (2008).
- [IDS1] J. Yang, et al., NV-Tree: Reducing Consistency Cost for NVMbased Single Level Systems, USENIX FAST, 2015.
- [IDS2] I. Oukid, et al., FPTree: A hybrid SCM-DRAM persistent and concurrent B-tree for SCM, MOD, SIGMOD, 2016.
- [DB1] Ru Fang, Hui-I Hsiao, Bin He, C Mohan, and Yun Wang. 2011. High performance database logging using storage class memory. In 2011 IEEE 27th International Conference on Data Engineering. IEEE, 1221–1231.
- [DB2] Tianzheng Wang and Ryan Johnson. 2014. Scalable logging through emerging non-volatile memory. Proceedings of the VLDB Endowment, 7, 10, 865–876.
- [DB3] Shen Gao, Jianliang Xu, Bingsheng He, Byron Choi, and Haibo Hu. 2011. PCMLogging: Reducing transaction logging overhead with PCM. In Proceedings of the 20th ACM international conference on Information and knowledge management, 2401–2404.
- [DB4] Gihwan Oh, Sangchul Kim, Sang-Won Lee, and Bongki Moon. 2015. SQLite optimization with phase change memory for mobile applications. Proceedings of the VLDB Endowment, 8, 12, 1454–1465.
- [DB5] Jian Huang, Karsten Schwan, and Moinuddin K Qureshi. 2014. NVRAM-aware logging in transaction systems. Proceedings of the VLDB Endowment, 8, 4, 389–400.