Vikas Pedapudi

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EDUCATION

National Institute of Technology, Warangal

Bachelor of Technology in Electronics and Communication Engineering

Jul. 2016 – Aug. 2020

Narayana Junior College, Marikavalasa

Intermediate in MPC (Maths, Physics, Chemistry) - BIEAP

St. Mary's High School, Yellamanchili

Class X - Secondary School Certificate (SSC) - BSEAP

Hyderabad, IN

CGPA: 7.59/10

Score: 980/1000

GPA: 9.8/10

Netcracker Technology

July 2022 - Present

Jun. 2014 - Apr. 2016

Jun. 2013 - Apr. 2014

 $Software\ Engineer$

Experience

- Developing next-generation cloud-based billing application (Cloud NRM) as part of core R&D team using C++, Kubernetes, Docker, AWS, WSL, and Postman.
- Implemented around 65 features on billing and finance sub-systems supported bug fixes for clients including AIS, Altice Portugal, Andorra Telecom, BT, Fastweb, NTT, Nuuday, Telenet, and VMO2.
- Developed a ProtoBuf-XML (PB2XML) marshaller as an independent utility library in NRM.
- Improved Vertex tax library integration for billing, resulting in a 3.5x faster execution time and a 250% performance improvement by processing 5.1 million events in 41 minutes.
- Optimized database by 84.93% with proper schema handling for test suites in billing and rating applications.
- Utilized DevOps practices including CI/CD pipelines, containerization, and infrastructure-as-code.

Junior Software Engineer

October 2020 - June 2022

- Upgraded NRM to RHEL 8.2, GCC 8.3.x, and C++17.
- Performed Just In Time Rollups (JITR) of new features, bug fixes, and fixed regression test suites.
- Integrated NRM with Tax SaaS provider Vertex O Series for British Telecom DigiCo.
- Implemented SEPA support for Andorra Telecom in Cloud NRM.

Projects

Detection and Classification of Eye Diseases | Python, Keras, TensorFlow

Jul. 2019 - Apr. 2020

 Developed a multi-class classifier using Convolutional Neural Networks to classify the eye diseases from the Retinal Fundus Images in 4 classes namely Cataracts, Diabetic Retinopathy, Glaucoma, and Normal. Achieved a classification accuracy of 84%

Implementation of AES on FPGA | VHDL

Mar - Apr 2018

 $\bullet \ \ \text{Developed VHDL code for the 128-bit AES encryption algorithm and implemented it on Nexys 4 DDR Artix-7 FPGA Board}$

8086 Based Microprocessor Project | Assembly Language

Feb 2019

• Interfaced both Hex-keypad and 7-segment display together with the 8086 microprocessor using 8255 PPI, so that the key pressed in the hex-keypad gets serially displayed in the SSD

Obstacle Avoiding Vehicle using Arduino | Arduino Programming (C/C++)

Mar - Apr 2019

 Designed a vehicle with an ultrasonic sensor mounted on a servo motor to detect obstacles and find alternative paths. Interfaced components with Arduino UNO

TECHNICAL SKILLS

Primary Skill: C/C++, Oracle SQL, PostgreSQL, Shell scripting

Platforms: Windows, Linux/Unix

Others: CUDA, Python, Kubernetes, Git, WSL, Docker, CLion, Postman, Jenkins, Jira

ACADEMIC ACHIEVEMENTS

- Received a consolation prize at the Netracker Hackathon in Hyderabad for developing the 'BillData as ProtoBuf' idea.
- Recognized for the work in Cloud NRM and awarded 'You've Made a Difference' award at Netcracker Technology.

2023 2021

- Recognized for the work in NRM RHEL 8.2 upgrade project and awarded 'Team Award Champions' at Netcracker Technology.
- Participated in the A.P. State Level 20th National Children's Science Congress presenting a project on "Briquetting of Agro Waste" under the focal theme: ENERGY EXPLORE, HARNESS & CONSERVE.

• Secured 3rd Prize in the **District Educational Science Exhibition** for the exhibit under the sub-theme "Mathematical Modelling" with the main theme of "Science and Society" 2012-13

• Secured 1st Prize in Quiz Competition held in **District Inspire Exhibition**.

2011-12

Recognized as the 'Best Student' by St. Mary's Management and awarded a gold medal and a memento on the occasion of Silver Jubilee
Celebrations of the school.

Extracurricular Activities

• Serving as Joint Treasurer for the NIT Warangal Alumni Association, Hyderabad Chapter (NITWAAH).

2022-24

Sub-Coordinator of Event Conduction and Coordination (ECC) - Technozion, NIT Warangal.

2018

• Software Developer at Web & Software Development Cell (WSDC), NIT Warangal.

2016-17

• Served as the House Leader, St. Mary's High School.

2013-14