JB

JAMES BROWN
SENIOR FIRMWARE DEVELOPER & HARDWARE DEVELOPER & TEAM LEADER

SUMMARY

IT Professional with over 20 years of experience specializing in IT department management for several companies. I can implement good IT products at local and global levels. My greatest strength for project development is a mix of speed, quality and cost.

SKILLS

Project Scheduling – over 90% of projects led were finished in due time.
Strategic Planning
Communication Skills
Team Management

CONTACT

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EXPERIENCE

PROGRAM DEVELOPER • STAR GROUP • 2008.5 – 2011.4
Responsible for developing Customer Relationship Management
Implemented Sales Force Automation for publishing company
Developed several business management program
Implemented secure file container based on driver development
Implemented anti-virus vaccine program

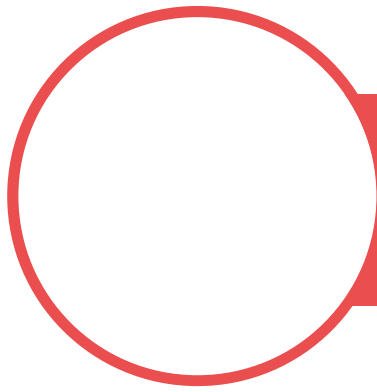### FIRMWARE DEVELOPER & HARDWARE DEVELOPER • STAR GROUP • 2011.5 – 2015.7
Developed wireless communication system using Bluetooth module, nRF24 module and so on
Implemented karaoke PCB design and developed firmware
Developed ARM-based solution and microcontroller-based solution
Developed digital signal processing product
Implemented communication system using Xilinx and Cypress FPGA.

PROJECT MANAGER • NIC • 2015.8 – 2020.9
Responsible for developing software & hardware project
As a project manager, implemented advanced secure USB
As a project manager, implemented network insulator device and print monitoring system
As a project manager, implemented 2 web-based projects

TEAM LEADER • RASEN • 2020.10 – CURRENT
Responsible for 10 team members (6 program developers, 2 PCB designers and 2 firmware developers)
Responsible for teaching new team member
Implemented 2 solutions using hardware and software tech both

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EDUCATION

BACHELOR OF COMPUTER SCIENCE • 2004.11 – 2008.4 • NATIONAL INSTITUTE OF INFORMATION TECHNOLOGY

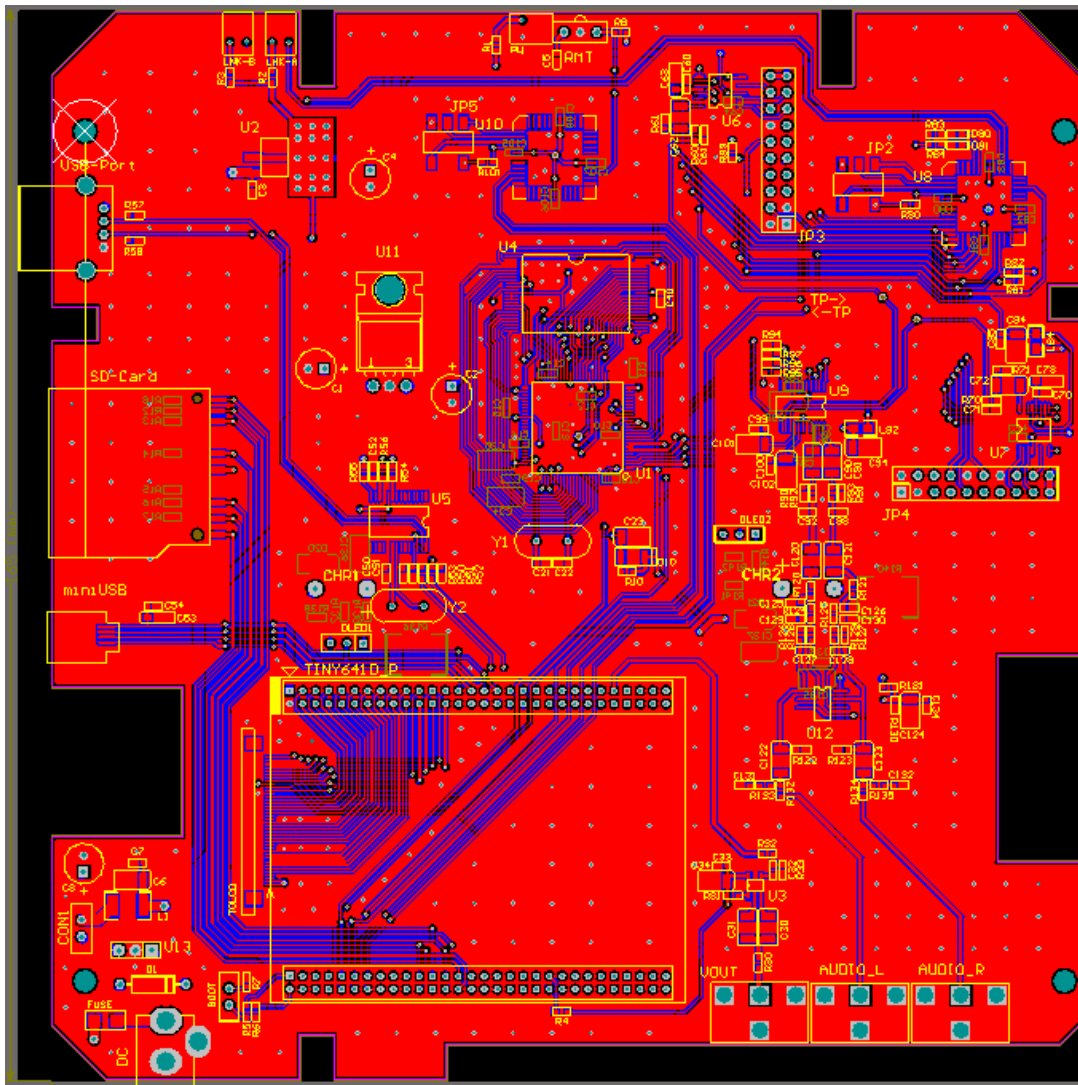**MASTER OF COMPUTER SCIENCE • 2010.4 – 2012.12 • KARNATA UNIVERSITY**
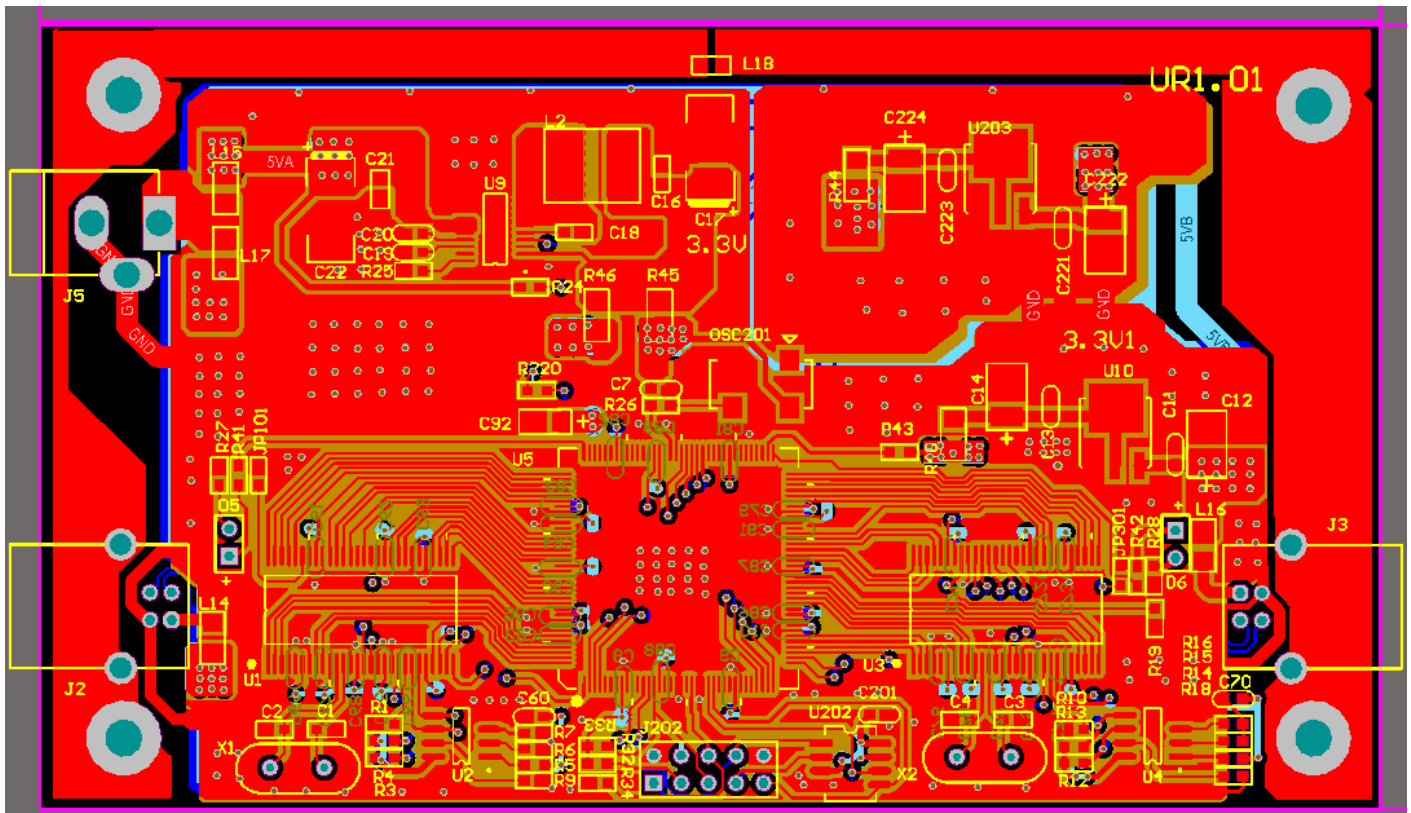
CERTIFICATION

MCSD from Microsoft, 2007.9
Java SE Programmer from Sun, 2008.1

SNAPSHOT

1. KARAOKE PCB DESIGN

2. COMMUNICATION SYSTEM USING XILINX FPGA
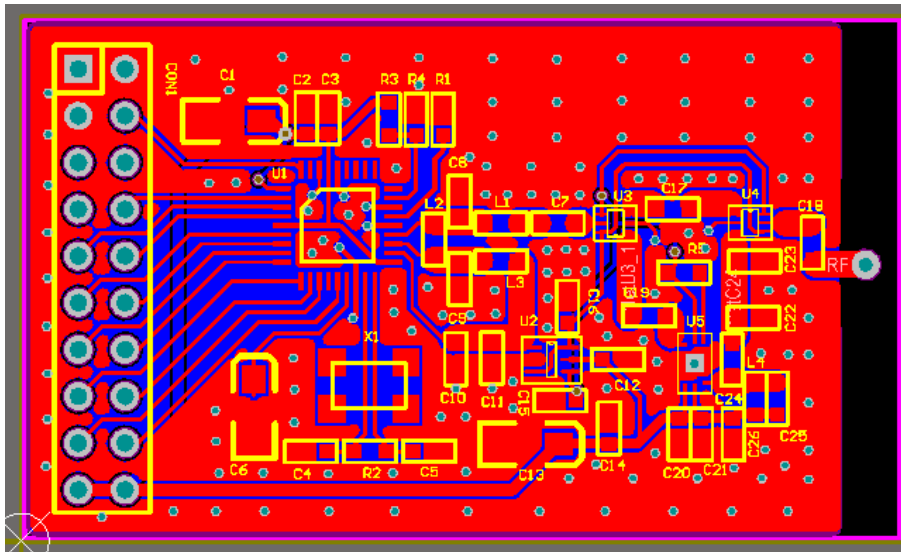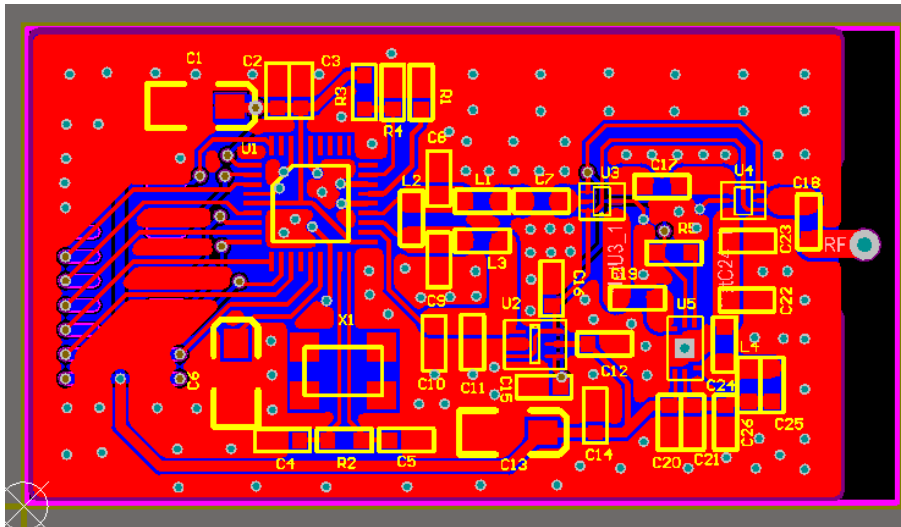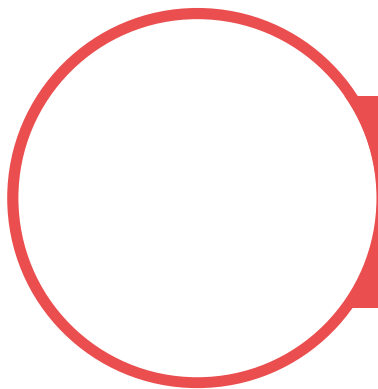
3. ARM BASED FIRMWARE

```c
1    //Uart.c
2    #include "uiGlobal.h"
3    #include <stdint.h>
4    #include <stdarg.h>
5
6    UINT8 TxArray[300];
7    UINT8 RxArray[300];
8    extern uint32_t SystemFrequency;
9
10   void Uart_Initialize(int Baudrate)
11   {
12       STRUCT_UART_FORMAT sFrame;
13       STRUCT_DATA_BUF RxBuf;
14       STRUCT_DATA_BUF TxBuf;
15
16       sFrame.BaudRate = Baudrate;       // baud rate
17       sFrame.CharBit = CHAR_8BIT;       // character bit
18       sFrame.StopBit = ONE_STOP;        // stop bit
19       sFrame.Parity = NONE;             // parity
20       sFrame.TxMode = UART_TX_POLLING;  //send check
21
22       TxBuf.pBuf=TxArray;
23       TxBuf.Size=sizeof(TxArray);
24       RxBuf.pBuf=RxArray;
25       RxBuf.Size=sizeof(RxArray);
26
27       UART_Init(UART0, UART0_MAP_A16_A17, sFrame, &RxBuf, &TxBuf);
28       NVIC_EnableIRQ(UART0_IRQn);
29   }
30   /*******************************************************************/
31   /*******************************************************************/
32   void Uart_InitBaudrate_Switch(int Baudrate)
33   {
34       volatile struct USART_REGS * pUart;
35
36       pUart = UART_GetRegs(UART0);
37       pUart->SPBREG = SystemFrequency / (16 * Baudrate);
38   }
39   /*******************************************************************/
40   /*******************************************************************/
41   UINT8 Uart_IsTXFIFOEmpty(void)
42   {
```

4. WIRELESS COMMUNICATION MODULE

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