

Off

Disassembly

```

0x000002C2 F1080804 ADD     r8,r8,#0x04
75:      AND R6, R3, R6 ;has PE0 val
0x000002C6 EA030606 AND     r8,r8,#0x04
76:      AND R7, R3, R7 ;has PE1 val
0x000002CA EA030707 AND     r8,r8,#0x04
77:      AND R8, R3, R8 ;has PE2 val
78:
79:      ;need to shift the
80:      LSR R7, R7, #1
81:      LSR R8, R8, #2

```

Port E Hardware

TM4C123 16 MHz

SW1 PE0
SW2 PE1
SW3 PE2

LED

Port E Registers

DATA: 0x06 PUR: 0x00 LOCK: 0x01
DIR: 0x08 PDR: 0x00 CR: 0xFF
DEN: 0x0F RCGCGPIO: 0x00000010 Clock enabled

Grading Controls

Number from EdX: Grade Score: 0
Copy this to EdX:

On

Disassembly

```

0x000002CE EA030808 AND     r8,r8,r8
80:      LSR R7, R7, #1
0x000002D2 EA4F0757 LSR     r8,r8,r8
81:      LSR R8, R8, #2
0x000002D6 EA4F0898 LSR     r8,r8,r8
82:      LSR R9, R9, #3
83:      AND R8, R3, R8 ;has PE0 val
84:      AND R7, R3, R7 ;has PE1 val
85:      AND R6, R3, R6 ;has PE2 val
86:      LSL R6, R6, #3 ;shift left by 3

```

Port E Hardware

TM4C123 16 MHz

SW1 PE0
SW2 PE1
SW3 PE2

LED

Port E Registers

DATA: 0x0A PUR: 0x00 LOCK: 0x01
DIR: 0x08 PDR: 0x00 CR: 0xFF
DEN: 0x0F RCGCGPIO: 0x00000010 Clock enabled

Grading Controls

Number from EdX: Grade Score: 0
Copy this to EdX: