

AVVARI MANISAI

Gender: Male

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B. Tech – ECE

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Anantha Lakshmi Institute of Technology and Sciences

Anantapur, Andhra Pradesh

Educational Qualification:

Degree/ Examination	Year of Passing	School/Institute	Board/University	Percentage/ CGPA
B. Tech	2022	Anantha Lakshmi Institute of Technology and Sciences, Anantapur	Jawaharlal Nehru Technological University, Anantapur	67
Class XII	2018	Sri Chaitanya Junior College, Anantapur	Board of Intermediate Education, Andhra Pradesh	63
Class X	2016	ZP High School, Bukkarayasmudram	Board of Secondary Education, Andhra Pradesh	8.3

Technical Skills:

- Programming Languages : C, python
- Engineering software : XILINX (ISE design 14.7),
- Other Software : MS Office

Project Work:

UG project:

- **Area efficient VLSI architecture for reversible radix2 FFT algorithm using folding technique and reversible gate:**
July 2021-MAY 2022
This project is about Area Efficient VLSI Architecture for reversible Radix-2 FFT Algorithm using Folding Technique & Reversible gate. Based on the radix-2 algorithm, it presents the superiority of low area consumption and longer throughput.

Areas of Interest:

- C language
- Python

Positions of Responsibility:

- Actively volunteered for "college-day" cultural fest celebrated on July-2019
- Actively participated in Dance competition during "college-day" cultural fest on July-2019

Strengths

- Able to cope up with failures and learn from them.
- Respect towards work.

Personal Details:

- Date of Birth : 19/02/2000
- Gender : Male
- Nationality : Indian
- Fathers name : A. Ramesh
- Mothers name : A. Uma Devi
- Languages Known : Telugu, English, Hindi
- Hobbies : Playing Sports, Social Activities
- Permanent address : Padmasali street, Bukkarayasamudram, Anantapur

Declaration:

I Hereby declare that all the information stated above is true to best of my knowledge and belief. I take the responsibility of any mistakes in data if occur in future.

Date :

Place :

Signature :

(A. Manisai)