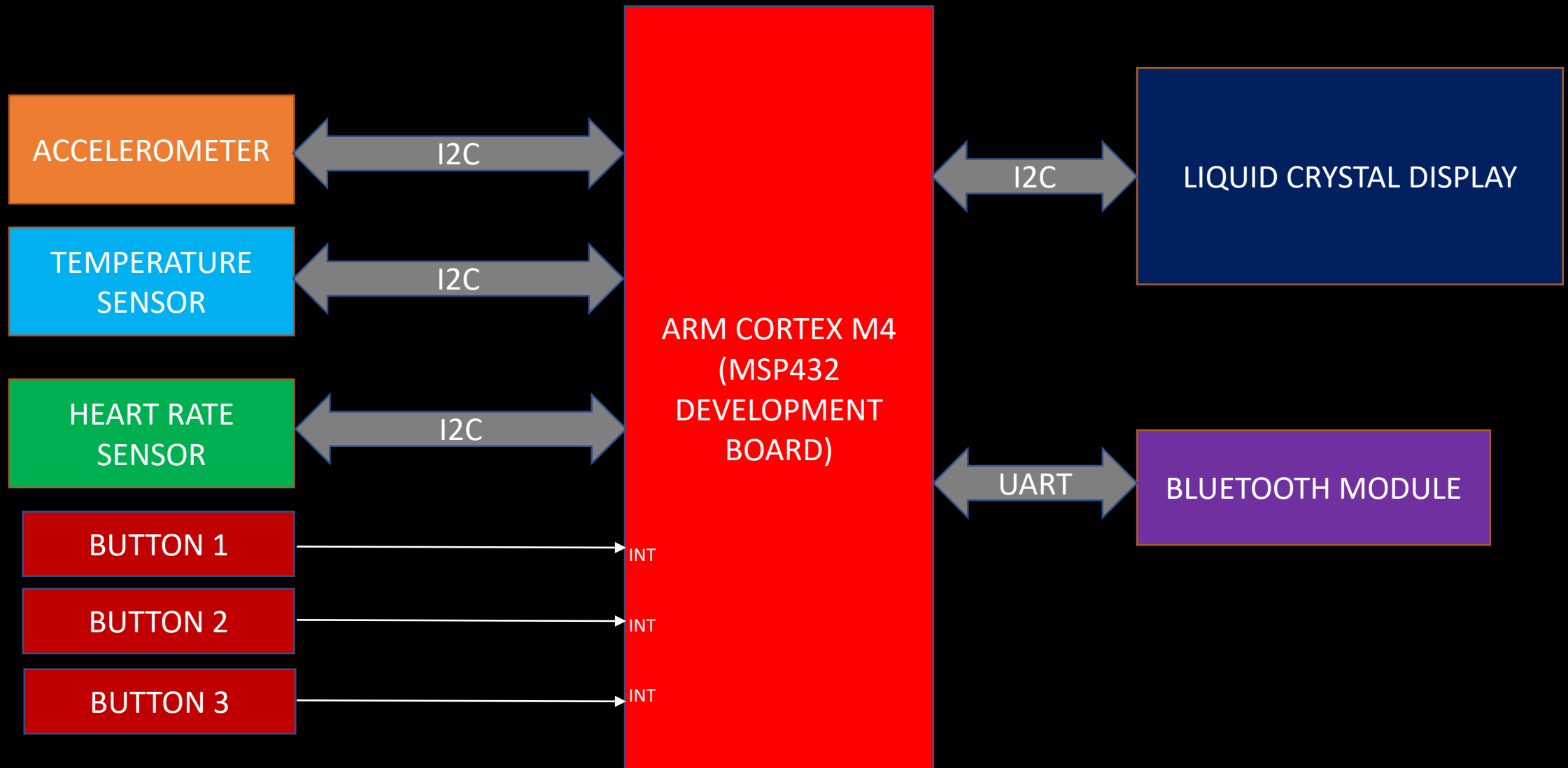


FITNESS TRACKER

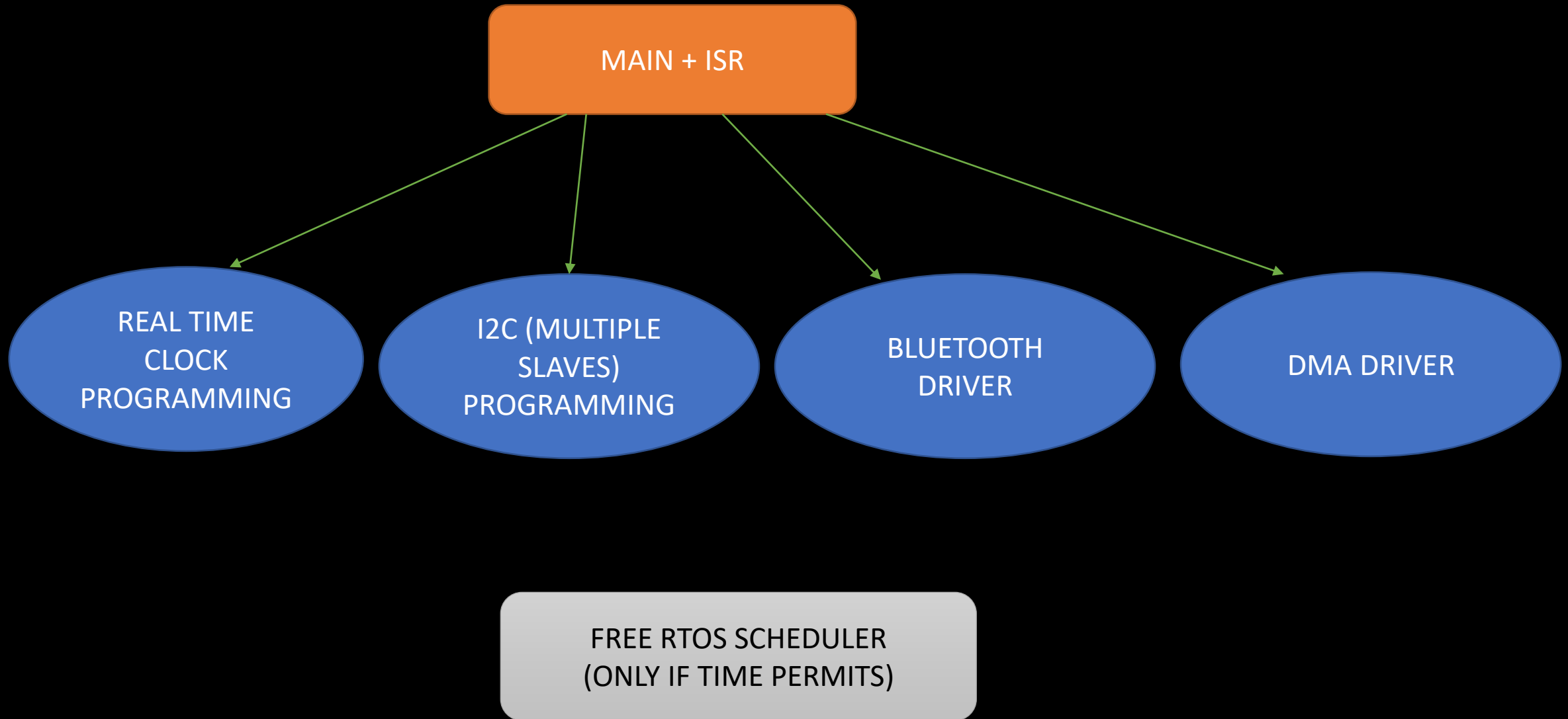
STUDENT NAME: VIKRANT WAJE
EMBEDDED SYSTEM DESIGN

HARDWARE BLOCK DIAGRAM



PROJECT NAME: FITNESS TRACKER
STUDENT NAME: VIKRANT WAJE

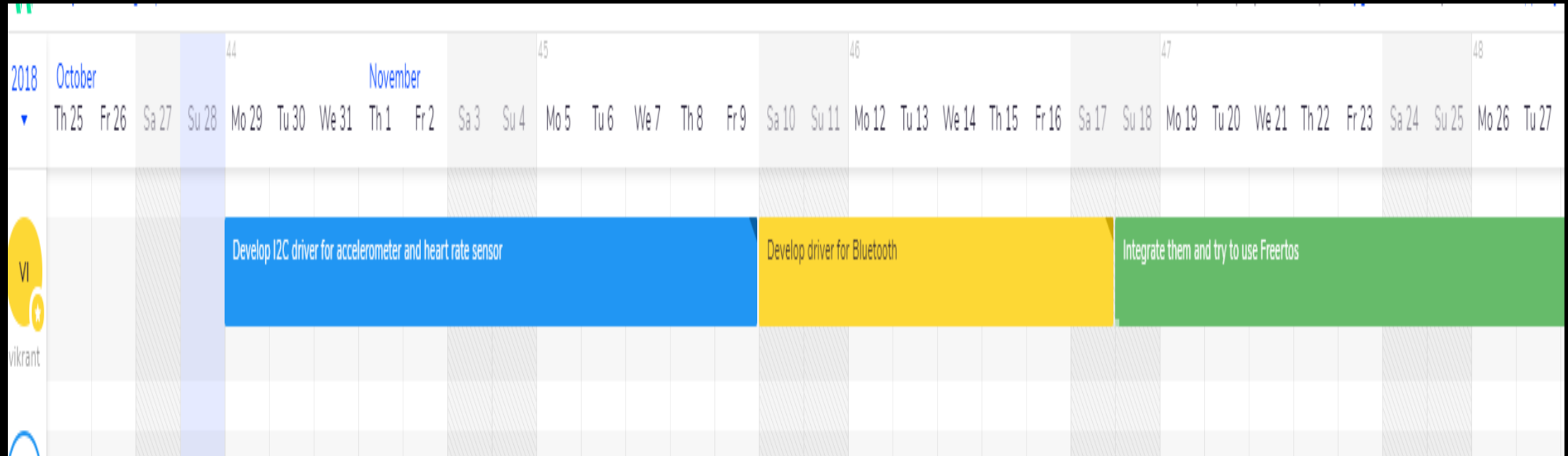
SOFTWARE BLOCK DIAGRAM



COMPONENT SELECTION

- ACCELEROMETER : LSM6DS3 (PURCHASED)
- HEART RATE SENSOR: MAX30105 (PURCHASED)
- TEMPERATURE SENSOR: HAVENT FINALISED
- BLUETOOTH MODULE: HC-05 (NOT YET PURCHASED)
- LCD DISPLAY: ADM12864H (HAD IT ALREADY)
- PROCESSOR: CORTEX M4 – MSP432 (HAD IT ALREADY)
- BUTTONS: TACTILE BUTTONS(NOT YET PURCHASED)

GANTT CHART



POTENTIAL RISK AND MITIGATION

1) Multiple slaves on I2c bus may cause bus contention

- Idea is to solve it using some kind of lock

2) Free RTOS may take much more time to be implemented

- Free RTOS is already a stretch goal. May or may not be implemented depending upon time left. Atleast I will have basic system working with help of Main + ISR



REFERENCES

- <https://app.teamweek.com/#timeline/365552/groups/325965?zoom=month>
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