



Literature

Literature:

The hardware track studies topics selected from:

[Parallel Computer Organization and Design](#), by
Michel Dubois, Murali Annavaram and Per Stenstrom,
ISBN 978-521-88675-8. Cambridge University Press, 2012.

Some of the compiler transformations taught in the software track can be found in:

[Optimizing Compilers for Modern Architectures](#)

Randy Allen and Ken Kennedy, Morgan Kaufmann, 2001.

This book's focus is low-level loop parallelization and related optimisations.

[Compilers, Principles, Techniques, and Tools](#). (the "Dragon Book")

A. V. Aho, M. S. Lam, R. Sethi, and J. D. Ullman,
Addison Wesley, 2006 (2nd edition).

Classical and famous standard book with a long history.

Papers related to various other techniques presented in the course, and pointers to CUDA material will be added later, for example in the Additional Material folder.

Software: