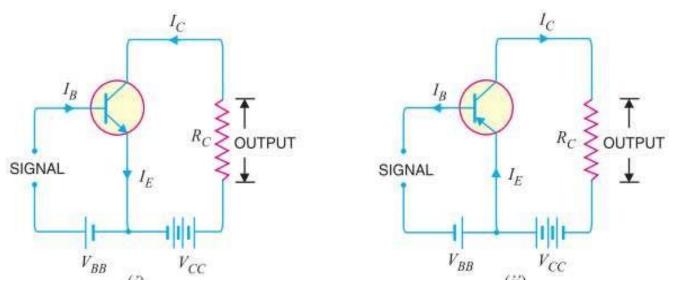
## COMMON EMITTER CONNECTION

• The common-emitter terminology is derived from the fact that the emitter is common to both the input and output sides of the configuration.



• First Figure shows common emitter npn configuration and second figure shows common emitter pnp configuration.

## COMMON EMITTER CONNECTION

- Base Current amplification factor ( $\beta$ ):
- In common emitter connection input current is base current and output current is collector current.
- The ratio of change in collector current to the change in base current is known as base current amplification factor,  $\beta$ .

 $\beta = \frac{\Delta I_C}{\Delta I_B}$ 

• Normally only 5% of emitter current flows to base, so amplification factor is greater than 20. Usually this range varies from 20 to 500.

# RELATION BETWEEN $\beta$ AND $\alpha$

$$\beta = \frac{\Delta I_C}{\Delta I_B}$$

$$\alpha = \frac{\Delta I_C}{\Delta I_E}$$

$$\beta = \frac{\Delta I_C}{\Delta I_E}$$

$$I_E = I_B + I_C$$

$$\Delta I_E = \Delta I_B + \Delta I_C$$

$$\Delta I_B = \Delta I_E - \Delta I_C$$

$$\beta = \frac{\Delta I_C}{1 - \alpha}$$

$$\beta = \frac{\Delta I_C}{1 - \alpha}$$

$$\alpha = \frac{\Delta I_C}{\Delta I_E} \qquad \beta = \frac{\Delta I_C / \Delta I_E}{\frac{\Delta I_E}{\Delta I_E} - \frac{\Delta I_C}{\Delta I_E}} = \frac{\alpha}{1 - \alpha}$$

$$I_E = I_B + I_C \qquad \beta = \frac{\alpha}{1 - \alpha}$$

$$\Delta I_E = \Delta I_B + \Delta I_C \qquad \beta = \frac{\alpha}{1 - \alpha}$$

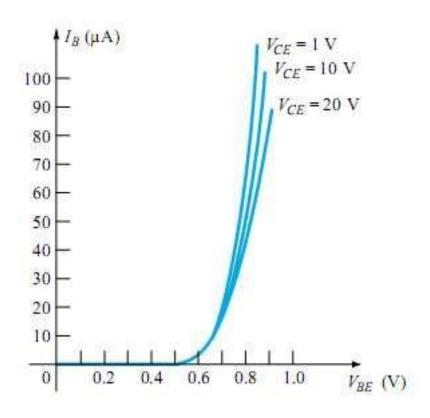
#### EXPRESSION FOR COLLECTOR CURRENT

$$I_{C} = \alpha I_{E} + I_{CBO}$$
 $I_{E} = I_{B} + I_{C} = I_{B} + (\alpha I_{E} + I_{CBO})$ 
 $I_{E} (1 - \alpha) = I_{B} + I_{CBO}$ 

$$I_{E} = \frac{I_{B}}{1 - \alpha} + \frac{I_{CBO}}{1 - \alpha}$$
 $I_{C} ; I_{E} = *(\beta + 1) I_{B} + (\beta + 1) I_{CBO}$ 

#### CHARACTERISTICS OF COMMON EMITTER CONFIGURATION

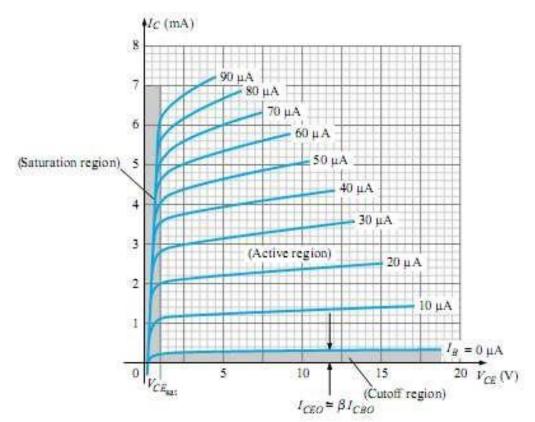
#### • <u>Input Characteristics</u>: → V<sub>BE</sub> vs I<sub>B</sub> characteristics is



- → V<sub>BE</sub> vs I<sub>B</sub> characteristics is called input characteristics.
- → I<sub>B</sub> increases rapidly with V<sub>BE</sub>. It means input resistance is very small.
- $\rightarrow$  I<sub>E</sub> almost independent of V<sub>CE</sub>.
- $\rightarrow$ I<sub>B</sub> is of the range of micro amps.

#### CHARACTERISTICS OF COMMON EMITTER CONFIGURATION

• Output Characteristics:



- V<sub>CE</sub> vs I<sub>c</sub>
   characteristics is
   called output
   characteristics.
- Ic varies linearly with Vce, only when Vce is very small.
  - As, Vce increases, Ic becomes constant.

### EXPRESSION FOR COLLECTOR CURRENT

$$I_{C} = \alpha I_{E} + I_{CBO}$$

$$I_{E} = I_{B} + I_{C} = I_{B} + (\alpha I_{E} + I_{CBO})$$

$$I_{E} (1 - \alpha) = I_{B} + I_{CBO}$$

$$I_{E} = \frac{I_{B}}{1 - \alpha} + \frac{I_{CBO}}{1 - \alpha}$$

$$I_{C} ; I_{E} = *(\beta + 1) I_{B} + (\beta + 1) I_{CBO}$$

$$\beta = \frac{\alpha}{1 - \alpha} \quad \therefore \quad \beta + 1 = \frac{\alpha}{1 - \alpha} + 1 = \frac{1}{1 - \alpha}$$

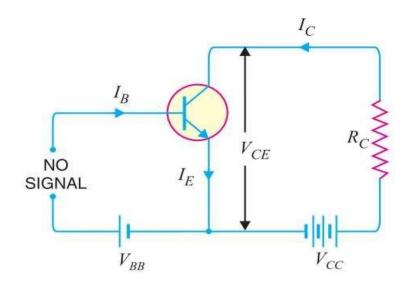
#### COMPARISON OF TRANSISTOR CONNECTION

S. No.	Characteristic	Common base	Common emitter	Common collector
1.	Input resistance	Low (about 100 Ω)	Low (about 750 Ω)	Very high (about 750 kΩ)
2.	Output resistance	Very high (about 450 kΩ)	High (about 45 kΩ)	Low (about 50 Ω)
3.	Voltage gain	about 150	about 500	less than 1
4.	Applications	For high frequency applications	For audio frequency applications	For impedance matching
5.	Current gain	No (less than 1)	High (β)	Appreciable

### TRANSISTOR LOAD LINE ANALYSIS

- In transistor circuit analysis it is necessary to determine collector current for various V<sub>CE</sub> voltage.
- One method is we can determine the collector current at any desired V<sub>CE</sub> voltage, from the output characteristics.
- More conveniently we can use load line analysis to determine operating point.

### TRANSISTOR LOAD LINE ANALYSIS



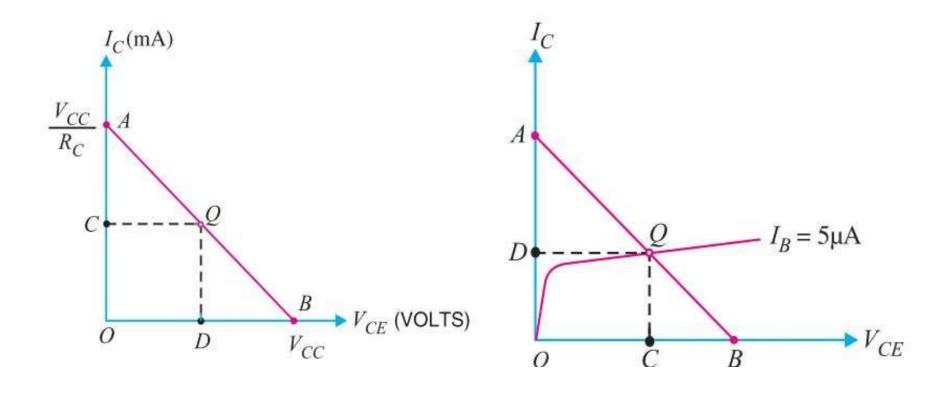
- Consider common emitter npn transistor ckt shown in figure.
- There is no input signal.
- Apply KVL in the output ckt-

$$V_{CE} = V_{CC} - I_C R_C$$

(i) When the collector current  $I_C = 0$ , then collector-emitter voltage is maximum and is equal to  $V_{CC}$  i.e.

$$\begin{aligned} \text{Max. } V_{CE} &= V_{CC} - I_C \, R_C \\ &= V_{CC} \qquad (\because I_C = 0) \\ \text{When collector-emitter voltage } V_{CE} &= 0, \qquad V_{CE} &= V_{CC} - I_C R_C \\ 0 &= V_{CC} - I_C R_C \end{aligned}$$
 
$$\begin{aligned} \text{Max. } I_C &= V_{CC} / R_C \end{aligned}$$

# TRANSISTOR LOAD LINE ANALYSIS



### OPERATING POINT

The zero signal values of  $I_C$  and  $V_{CE}$  are known as the operating point.

- It is called operating point because variation of Ic takes place about this point.
- It is also called quiescent point or Q-point.

