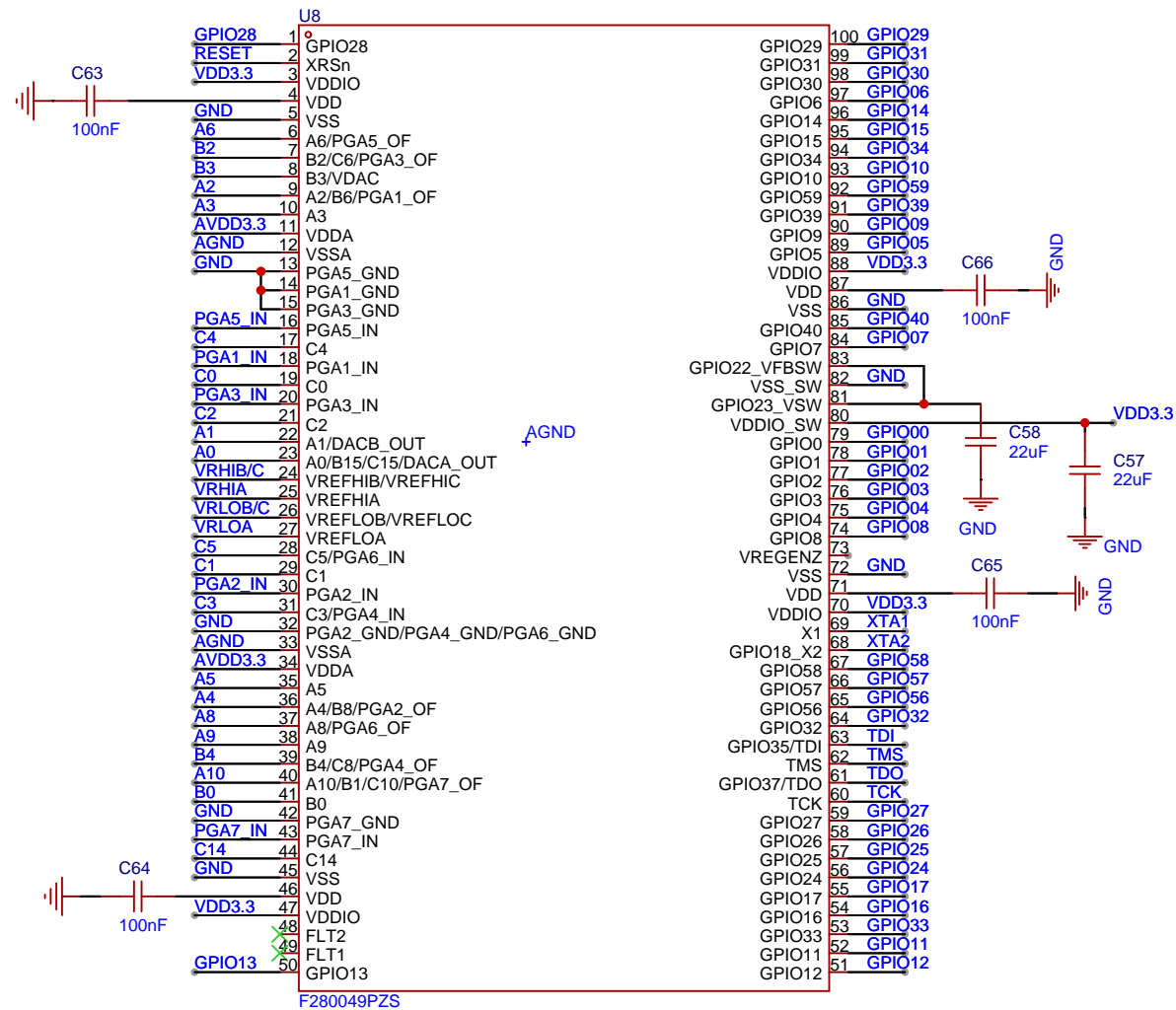


## 使用内部DCDC提供1.2v内核电源

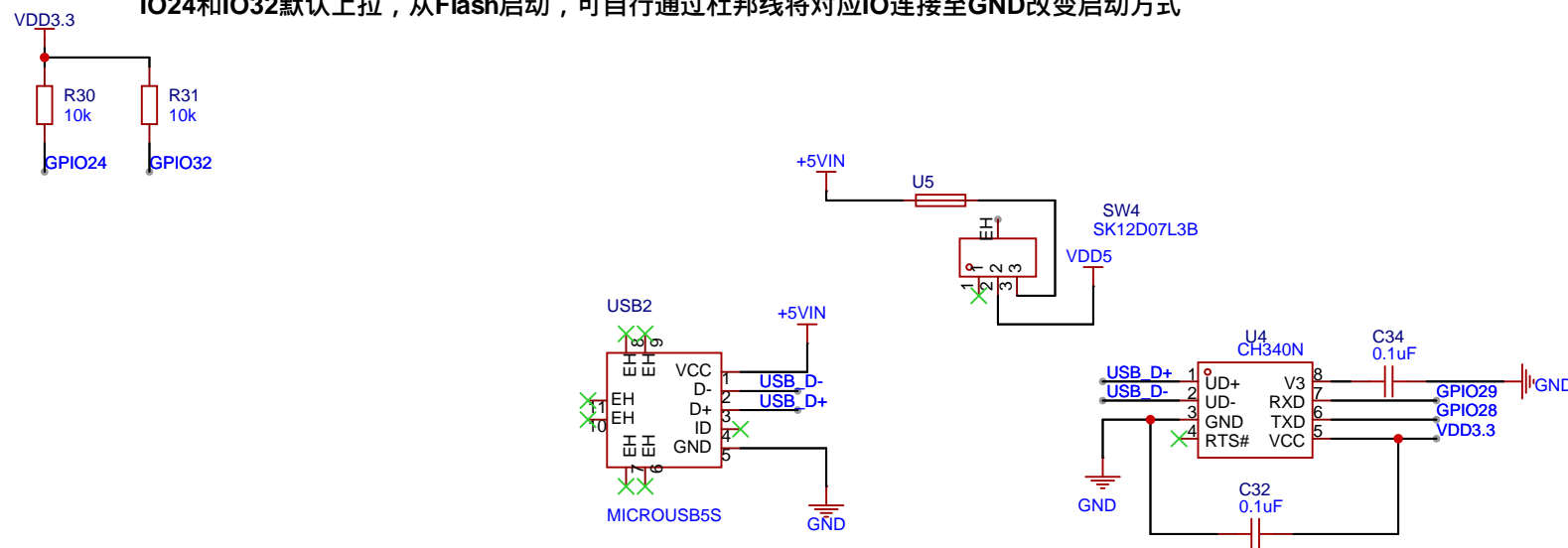


**F280049PZS**

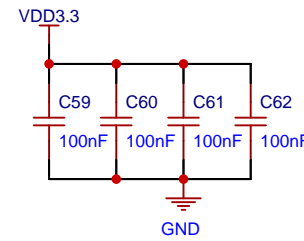
### Table 8-13. Device Default Boot Modes

BOOT MODE	GPIO24 (DEFAULT BOOT MODE SELECT PIN 1)	GPIO32 (DEFAULT BOOT MODE SELECT PIN 0)
Parallel IO	0	0
SCI/Wait boot	0	1
CAN	1	0
Flash	1	1

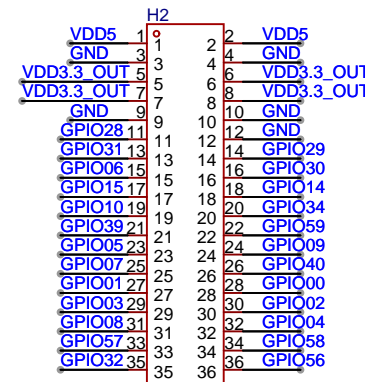
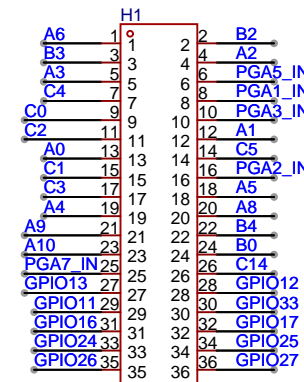
**IO24和IO32默认上拉，从Flash启动，可自行通过杜邦线将对应IO连接至GND改变启动方式**



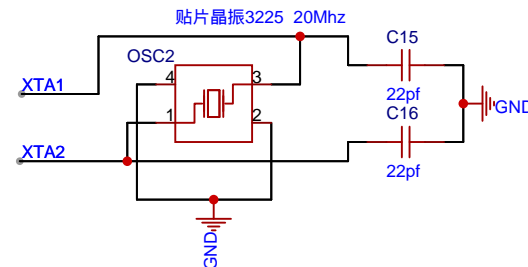
## microUSB转串口(SCIA)



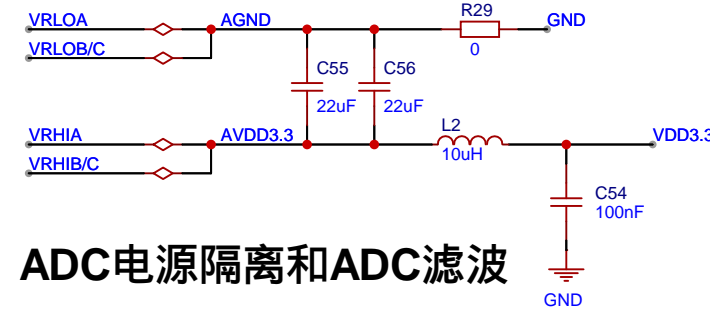
## 滤波电容



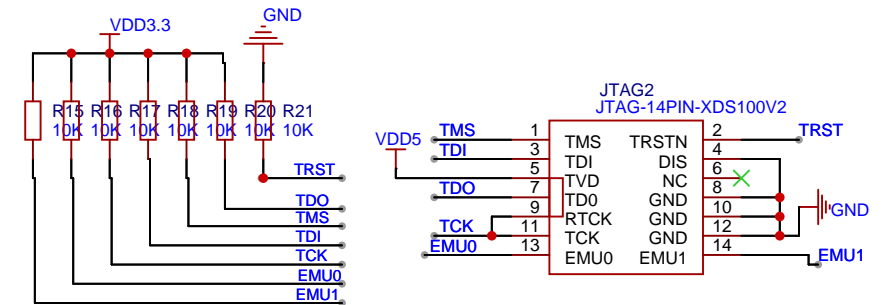
## 引出排针



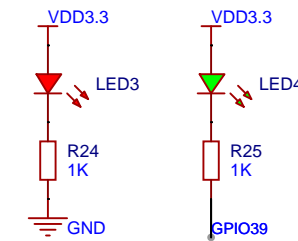
## 20Mhz贴片晶振



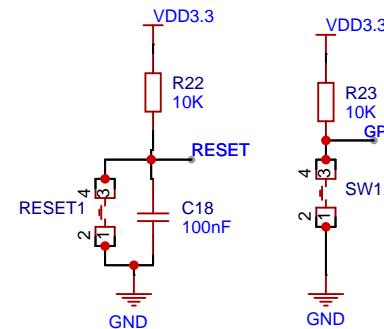
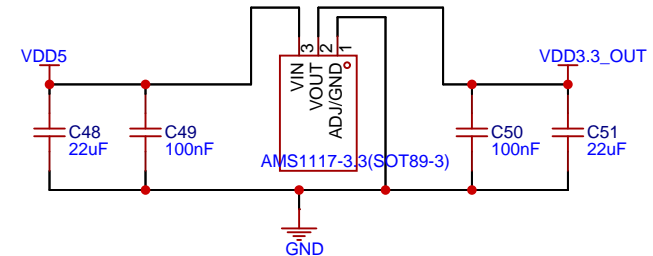
## ADC电源隔离和ADC滤波



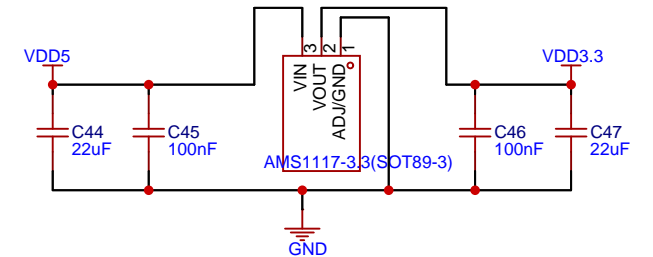
## JTAG调试



## 电源指示灯及用户灯



## 复位及按键



### 5V转3.3V,两路,板内芯片和对外供电分开

原理图	DSP280049			更新日期	2025-01-21
				创建日期	2025-01-11
图页	TMS320F28035			物料编码	
绘制		最小系统板开发板			
审阅					
		版本	尺寸	页	1 共 1
		V1.0	A4	嘉立创EDA	