

Objective	Obtain full-time position as an engineer in order to pursue my passion for learning and technology.	
Education	University of California, Los Angeles Los Angeles, CA <ul style="list-style-type: none">Major: Computer Science and Engineering B.S.Current Cumulative GPA: 3.5UPE (CS honors club, top 1/3 of major) – tutoring chair + host of undergrad review sessions	June 2017
Experience	Ozcan Research Group UCLA <ul style="list-style-type: none">Projects including QT applications, Matlab image registration, Android camera stackLab goal: introduce new imaging and sensing architectures capable of compensating in the digital domain for the lack of complexity of optical components	10/2016 - 06/2017
Relevant Coursework <ul style="list-style-type: none"><i>Circuit Analysis</i>: building protoboard circuits, Laplace transforms, power analysis<i>Digital Design</i>: MIPS single & multi-cycle, x86, caches, assembly, memory architecture<i>Operating Systems</i>: concurrency, virtual memory, security, scheduling, file systems<i>Computer Networking</i>: layered network architecture, routing protocols, TCP/IP emphasis<i>Algorithm Design</i>: divide & conquer, greedy, dynamic programming, NP-completeness		
UCLA Invention 2016 Los Angeles, CA <ul style="list-style-type: none">Integrated Uber API to create a medical Android app (1 day, ~1000 LOC)Use of Bluetooth and location servicesLaid out business plan to make profitable product proposal to investors (placed 3rd)		
USC vs. UCLA Open Hack 2015 Los Angeles, CA <ul style="list-style-type: none">Created socially connected Android application called Scenic (1 day, ~2000 LOC)Integrated Google Maps API to leverage custom 3D interfaceUse of GitHub to manage code merging		
Projects <ul style="list-style-type: none"><i>Simon Says</i>: board game re-created with FPGA board & Verilog (digital design)<i>Neural Spike Detector</i>: absolute-value detector using CMOS and pass-transistor logic<ol style="list-style-type: none">Layout optimized for regularity and minimal trace length using Cadence<i>SimpleDB</i>: implemented key features of a database system (buffer pool, catalog, files)<i>WeensyOS</i>: implemented crucial kernel and memory management modules of a simple OS<i>QMusic</i>: party playlist web app using Spotify API, Go, and Javascript (10 weeks, ~3000 LOC)<i>ImagCalc</i>: FPGA project enabling camera module to interpret 7-segment digits<ol style="list-style-type: none">Numbers and operators are scanned; final expression is converted to bits and evaluated		
Work Kinross South <ul style="list-style-type: none">UCLA Library student worker at book cataloging center		
Skills	Programming Languages <ul style="list-style-type: none">C/C++, Java, OCaml, LISP, HTML + CSS General / Other <ul style="list-style-type: none">Experience with Cadence (schematic & layout design, simulation & analysis), QT frameworkSoftware construction: UML diagrams/design, version control, project managementLinux familiarity: CLI tools, GDB debugging, BASH	03/2016 – 06/2017