

Modelsim User Guide

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ModelSim-Altera Software Simulation User Guide January 2013 Altera Corporation 1. In the Tool name list, select ModelSim-Altera. Ensure that Run gate-level simulation automatically after compilation box is turned off. 2. Under EDA Netlist Writer settings, in the Format for output netlist list, select Verilog HDL.

ModelSim-Altera Software Simulation User Guide - intel.com

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ModelSim SE User's Manual

(Figure 1-3) ModelSim User's Manual.vhd set. since they are not order dependent. 34 ModelSim User's Manual.Introduction Basic Steps for Simulation Figure 1-3. you need to load the design with the vsim command using the names of any top-level modules (many designs contain only one top-level module).4a ." then invoke the simulator.

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Environment Variables (see ModelSim cmd "printenv") LM_LICENSE_FILE Required Pathname oflicense.datfile or port@host DOPATH Optional Search path for ".do" files EDITOR Optional Specifies editor for "edit" cmd MODELSIM Optional Pathname of modelsim.inifile MODELSIM_TCL Optional List of modelsim.tclfiles

ModelSim 6.0 Quick Guide - Carnegie Mellon University

This User Guide describes the steps required prior to using ModelSim SE/PE or QuestaSim in the Libero SoC environment to run simulation on your design targeted for Microsemi SmartFusion2, IGLOO2, RTG4, and PolarFire devices.

ModelSim SE/PE and QuestaSim in Libero SoC User Guide

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ModelSim's award-winning Single Kernel Simulator (SKS) technology enables transparent mixing of VHDL and Verilog in one design. Its architecture allows platform-independent compile with the outstanding performance of native compiled code. The graphical user interface is powerful, consistent, and intuitive.

ModelSim ASIC and FPGA Design - Mentor Graphics

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ModelSim Reference Manual - cc.gatech.edu

6 ModelSim Installation and Licensing Guide, v6.6b Installation and Licensing Supported Platforms SystemC Supported Platforms SystemC runs on a subset of supported platforms, as shown in Table 1-2. Refer to SystemC Simulation in the User's Manual for more details. For the Windows platform, you must install the compilers manually. The ...

ModelSim SE Installation and Licensing Guide - Xilinx

ModelSim 6.3 SUPPORT Quick Guide Quick Guide www.support.model.com ModelSim 6.3 Key Commands add memory opens the specified memory in the MDI frame of the Main window add testbrowser adds .ucdb files to the Test Management Browser add watch adds signals or variables to the Watch window add wave

ModelSim Quick Guide - UCSD

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The ModelSim*-Intel® FPGA edition software is a version of the ModelSim* software targeted for Intel® FPGAs devices. The software supports Intel gate-level libraries and includes behavioral simulation, HDL test benches, and Tcl scripting.

Intel® FPGA Simulation - ModelSim*-Intel® FPGA

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