

Multicore Navigator User Guide

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KeyStone Architecture Multicore Navigator User's Guide Literature Number: SPRUGR9H November 2010-Revised April 2015

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KeyStone Architecture Multicore Navigator (Rev. G)

KeyStone Training Multicore Navigator Overview Overview Agenda • What is Navigator? – Definition – Architecture – Queue Manager Sub-System (QMSS) – Packet DMA (PKTDMA) – Descriptors and Queuing • What can Navigator do? ... Multicore Navigator User Guide

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Multicore Navigator for Dummies - Texas Instruments

It can however be used in a chain so that the output queue can trigger the input channel of a
peripheral's pktDMA. The Infrastructure pktDMA has 32 channels, so it can process 32 transfers
simultaneously. You can refer to the Multicore Navigator User Guide on TI.com (search for
SPRUGR9E) for more details. -dave

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Multicore Navigator is used to exchange control packets between the application and the SA
firmware. Note: Due to export control restrictions the SA driver is a separate download from the
rest of the MCSDK.

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Chip solutions contain multiple processing cores. The

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- Multicore Navigator is a collection of hardware components that facilitate data movement and multi-core control.
- The major components within the Navigator domain are: -A hardware Queue Manager (QM). -Specialized packet DMAs, called PKTDMA. -Data structures to describe packets, called descriptors.

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