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Microelectronic Circuit Design 4th Edition Errata - Updated 4/4/14 P11.126 Change the open-loop gain to 94 dB and unity-gain frequency to 2.5 MHz P13.18 Should refer to Fig. P13.3 Prob. 13.34 Assume  $V_{SS} = 0$ . Fig. P13.13: Transistor should be a depletion-mode transistor

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