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applied negative feedback can improve its performance (gain stability, linearity, frequency response, step response) and reduces sensitivity to parameter variations due to manufacturing or environment.

## Negative-feedback amplifier - Wikipedia

This page contains the complete set of materials for my FPGA & Verilog design course which I taught in Isfahan University of Technology, 2010.

# FPGA & Verilog Design - Mohammad S. Sadri - Googoolia

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## IBM System/360 - Wikipedia

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