

## *Modelsim User Guide*

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(Figure 1-3) ModelSim User's Manual.vhd set. since they are not order dependent. 34 ModelSim User's Manual.Introduction Basic Steps for Simulation Figure 1-3. you need to load the design with the vsim command using the names of any top-level modules (many designs contain only one top-level module).4a ." then invoke the simulator.

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Environment Variables (see ModelSim cmd "printenv") LM\_LICENSE\_FILE Required Pathname oflicense.datfile or port@host DOPATH Optional Search path for ".do" files EDITOR Optional Specifies editor for "edit" cmd MODELSIM Optional Pathname of modelsim.inifile MODELSIM\_TCL Optional List of modelsim.tclfiles

**ModelSim 6.0 Quick Guide - Carnegie Mellon University**

This User Guide describes the steps required prior to using ModelSim SE/PE or QuestaSim in the Libero SoC environment to run simulation on your design targeted for Microsemi SmartFusion2, IGLOO2, RTG4, and PolarFire devices.

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ModelSim's award-winning Single Kernel Simulator (SKS) technology enables transparent mixing of VHDL and Verilog in one design. Its architecture allows platform-independent compile with the outstanding performance of native compiled code. The graphical user interface is powerful, consistent, and intuitive.

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**ModelSim SE Installation and Licensing Guide - Xilinx**

ModelSim 6.3 SUPPORT Quick Guide Quick Guide [www.support.model.com](http://www.support.model.com) ModelSim 6.3 Key Commands add memory opens the specified memory in the MDI frame of the Main window add testbrowser adds .ucdb files to the Test Management Browser add watch adds signals or variables to the Watch window add wave

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The ModelSim\*-Intel® FPGA edition software is a version of the ModelSim\* software targeted for Intel® FPGAs devices. The software supports Intel gate-level libraries and includes behavioral simulation, HDL test benches, and Tcl scripting.

**Intel® FPGA Simulation - ModelSim\*-Intel® FPGA**

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