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KeyStone Architecture Multicore Navigator User's Guide Literature Number: SPRUGR9G November 2010–Revised October 2014

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KeyStone Architecture Multicore Navigator - Texas Instruments

What is Multicore Navigator? • Multicore Navigator is a hardware mechanism that facilitates data movement and multicore co-working • Supports multiple users (players) –Each core in a multicore system –High bit-rate peripherals including SRIO, AIF2, Network Coprocessor (NETCP), and PCIe • Users can think of the Navigator as a mailbox ...

Using Multicore Navigator - Texas Instruments

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It can however be used in a chain so that the output queue can trigger the input channel of a peripheral's pktDMA. The Infrastructure pktDMA has 32 channels, so it can process 32 transfers simultaneously. You can refer to the Multicore Navigator User Guide on Tl.com (search for SPRUGR9E) for more details. -dave

Multicore Navigator Tips 'n Tricks, pt. 2 - The Process ...

BIOS MCSDK 2.0 User Guide. From Texas Instruments Wiki. Jump to: navigation, search. ... The Multicore Navigator is used to exchange control packets between the application and the SA firmware. Note: Due to export control restrictions the SA driver is a separate download from the rest of the MCSDK.

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Multicore Programming Guide Multicore Programming and Applications/DSP Systems Abstract As application complexity continues to grow, we have reached a limit on increasing performance by merely scaling clock speed. To meet the ever-increasing processing demand, modern System-On-Chip solutions contain multiple processing cores. The

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KeyStone Training Multicore Navigator Overview Overview Agenda • What is Navigator? - Definition - Architecture - Queue Manager Sub-System (QMSS) - Packet DMA (PKTDMA) - Descriptors and Queuing • What can Navigator do? ... Multicore Navigator User Guide

KeyStone Training Multicore Navigator Overview

• Multicore Navigator is a collection of hardware components that facilitate data movement and multi-core control. • The major components within the Navigator domain are: -A hardware Queue Manager (QM). -Specialized packet DMAs, called PKTDMA. -Data structures to describe packets, called descriptors.

KeyStone Training Multicore Navigator Overview

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