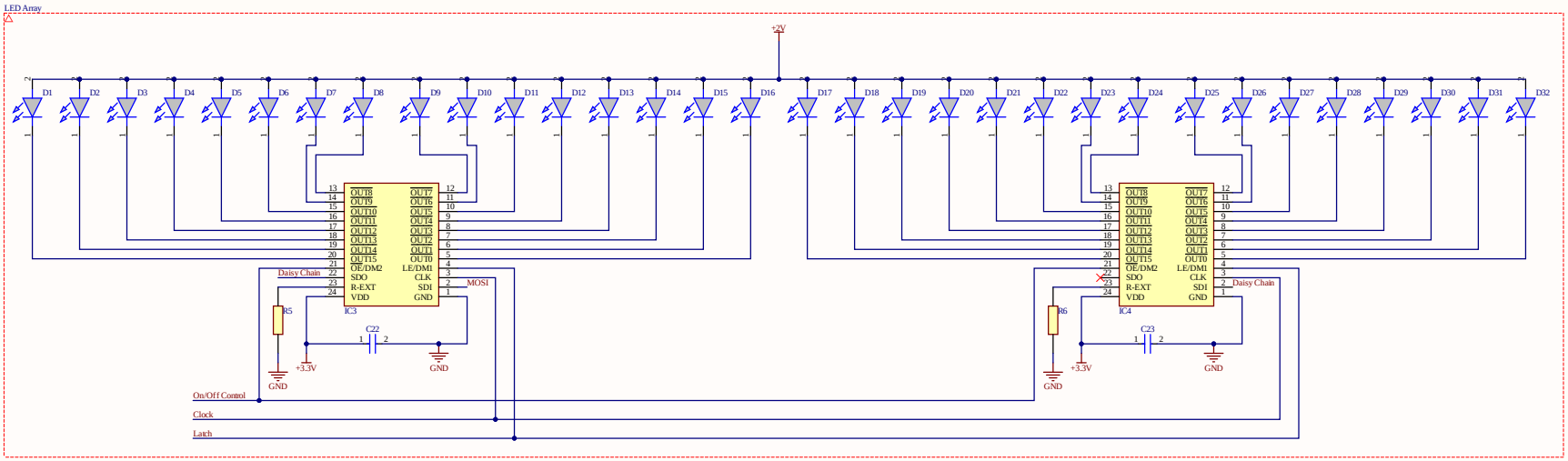

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Date:	5/01/2024	Sheet of	
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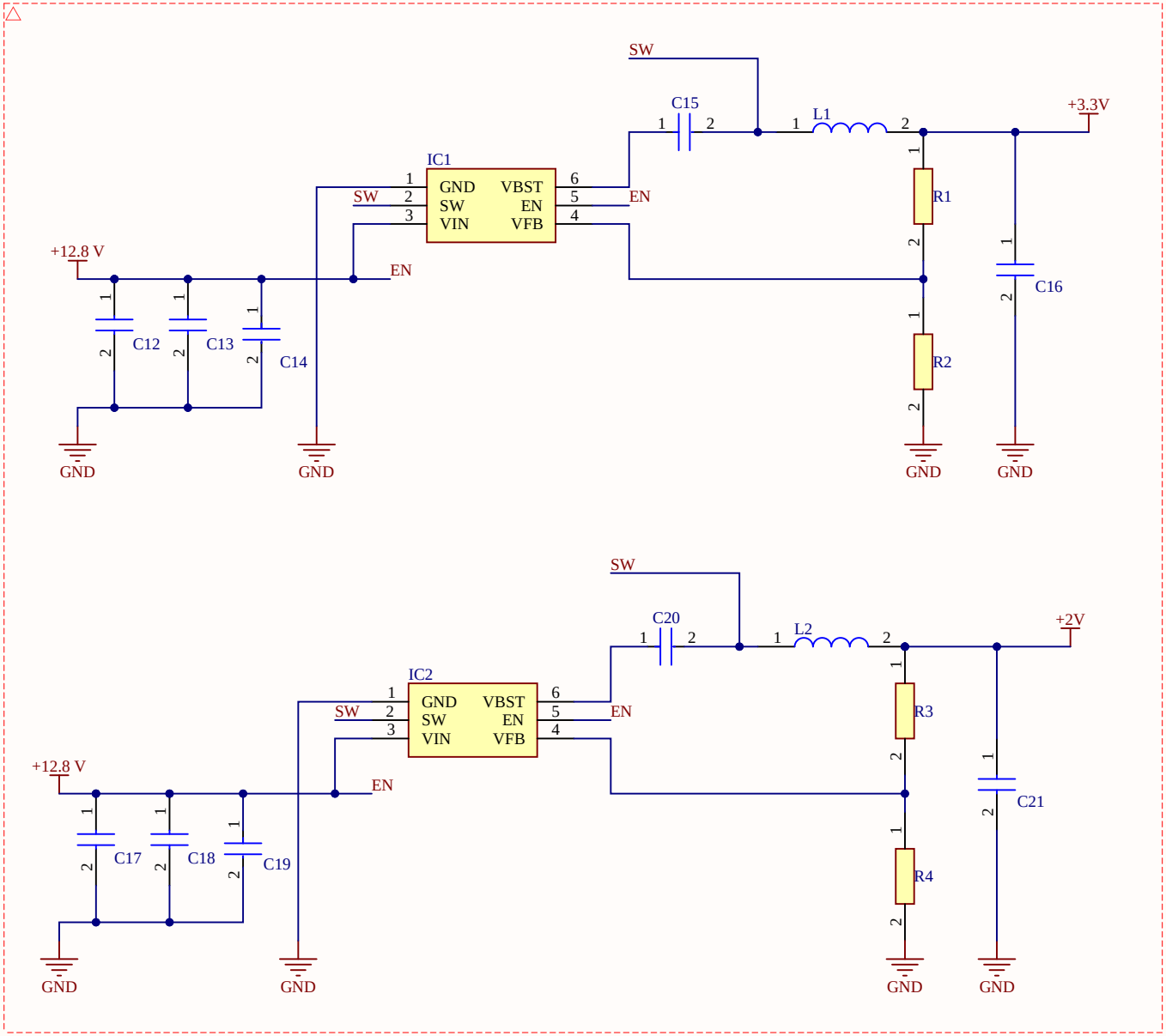

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Date: 5/01/2024		Sheet of
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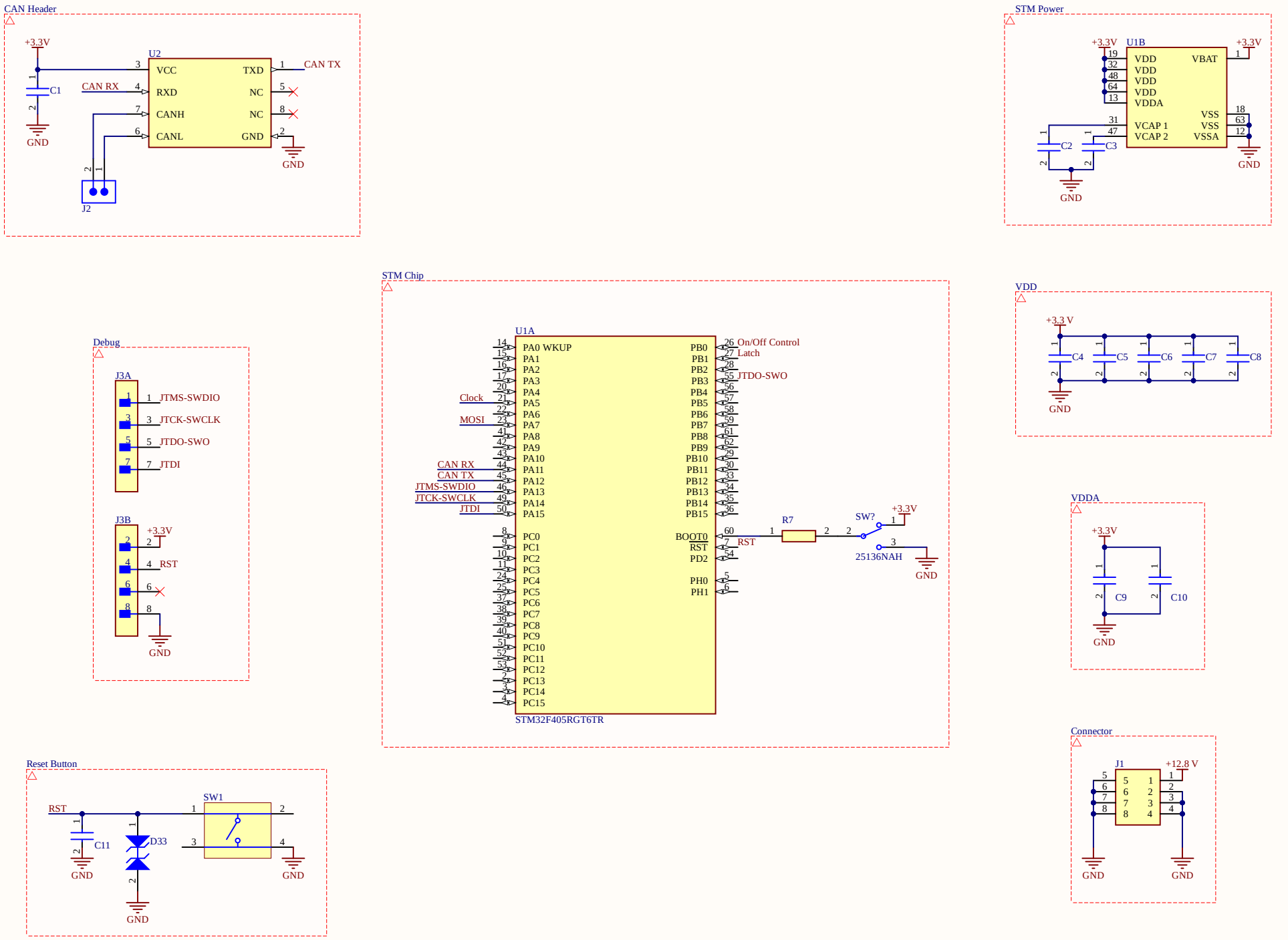
CAN Header
+3.3V
C1
GND
U2
VCC
RXD
CANH
CANL
TXD
NC
NC
GND
CAN RX
CAN TX
J2
STM Power
+3.3V
U1B
VDD
VDD
VDD
VDD
VDDA
VBAT
VSS
VSS
VSSA
VCAP 1
VCAP 2
C2
C3
GND
Debug
J3A
JTMS-SWDIO
JTCK-SWCLK
JTDO-SWO
JTDI
J3B
+3.3V
RST
GND
STM Chip
U1A
PA0 WKUP
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11
PA12
PA13
PA14
PA15
PC0
PC1
PC2
PC3
PC4
PC5
PC6
PC7
PC8
PC9
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PC11
PC12
PC13
PC14
PC15
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
BOOT0
RST
PD2
PH0
PH1
STM32F405RGT6TR
Clock
MOSI
CAN RX
CAN TX
JTMS-SWDIO
JTCK-SWCLK
JTDI
On/Off Control
Latch
JTDO-SWO
R7
SW?
25136NAH
VDD
+3.3 V
C4
C5
C6
C7
C8
GND
VDDA
+3.3V
C9
C10
GND
Reset Button
RST
SW1
C11
D33
GND
Connector
J1
+12.8 V
GND
GND

Board Stack Report