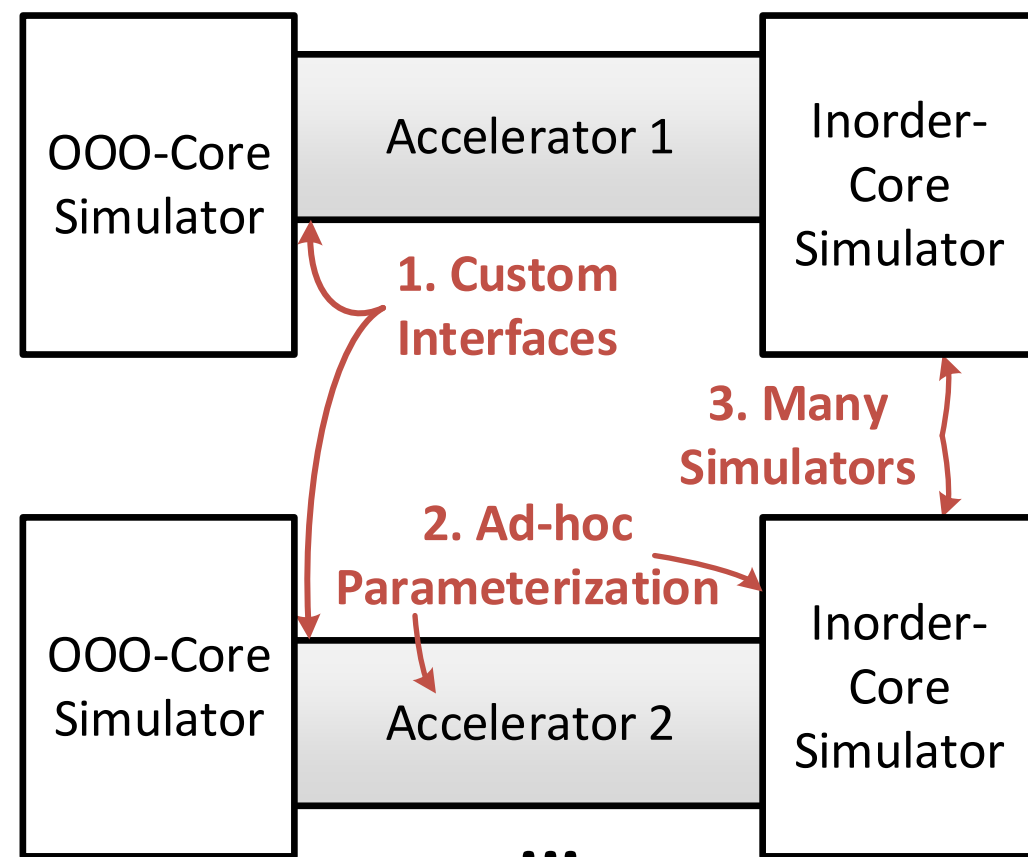


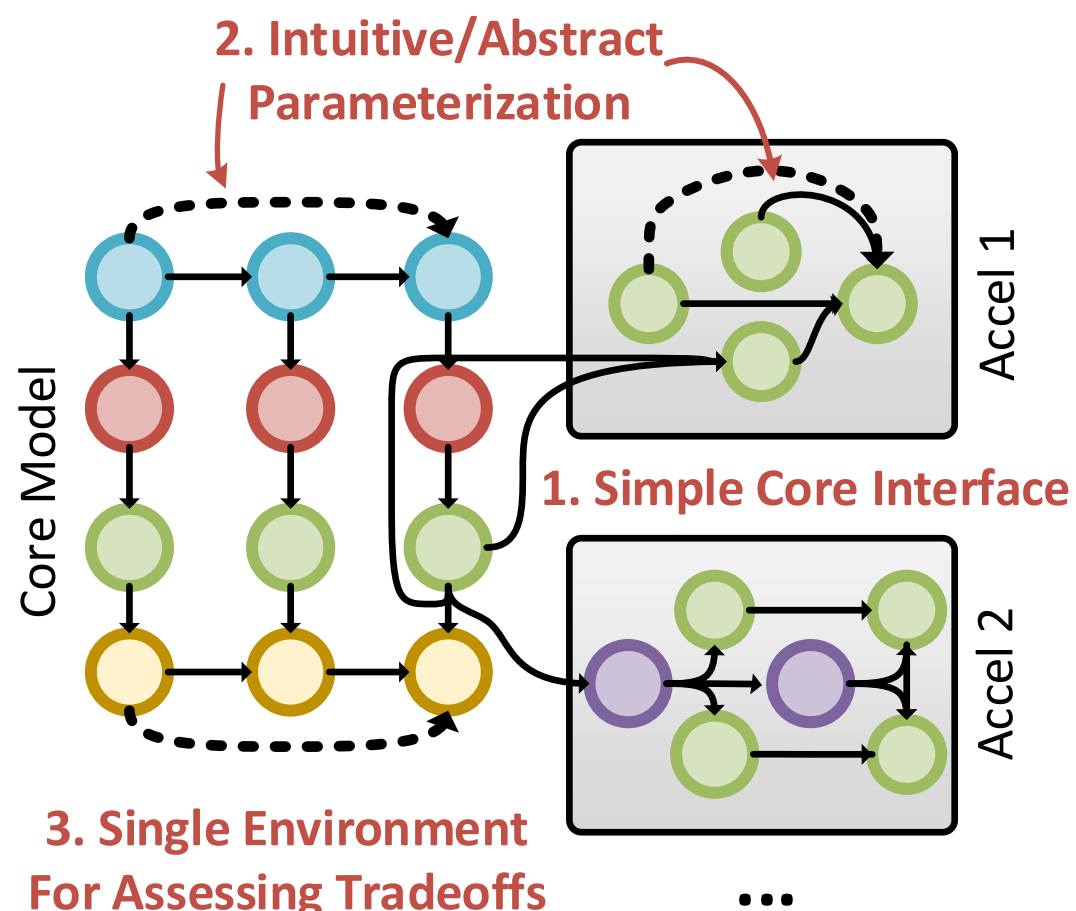
Current Approach

(a) Understanding Accelerator Limits & Opportunities



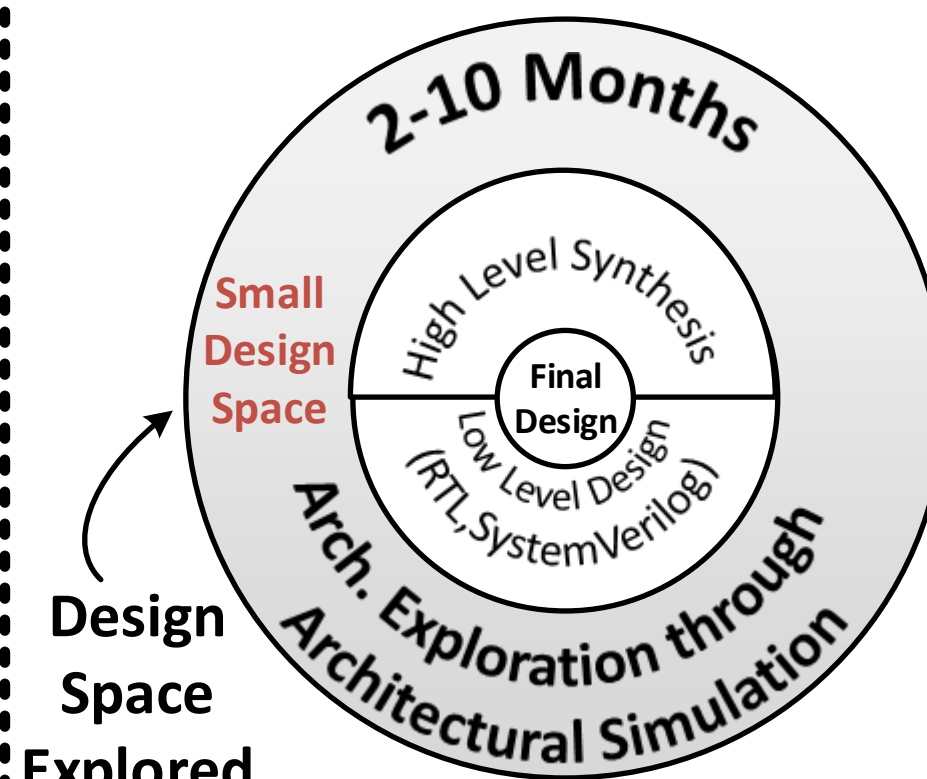
Problem: Design Effort is Impractical

Proposed Approach

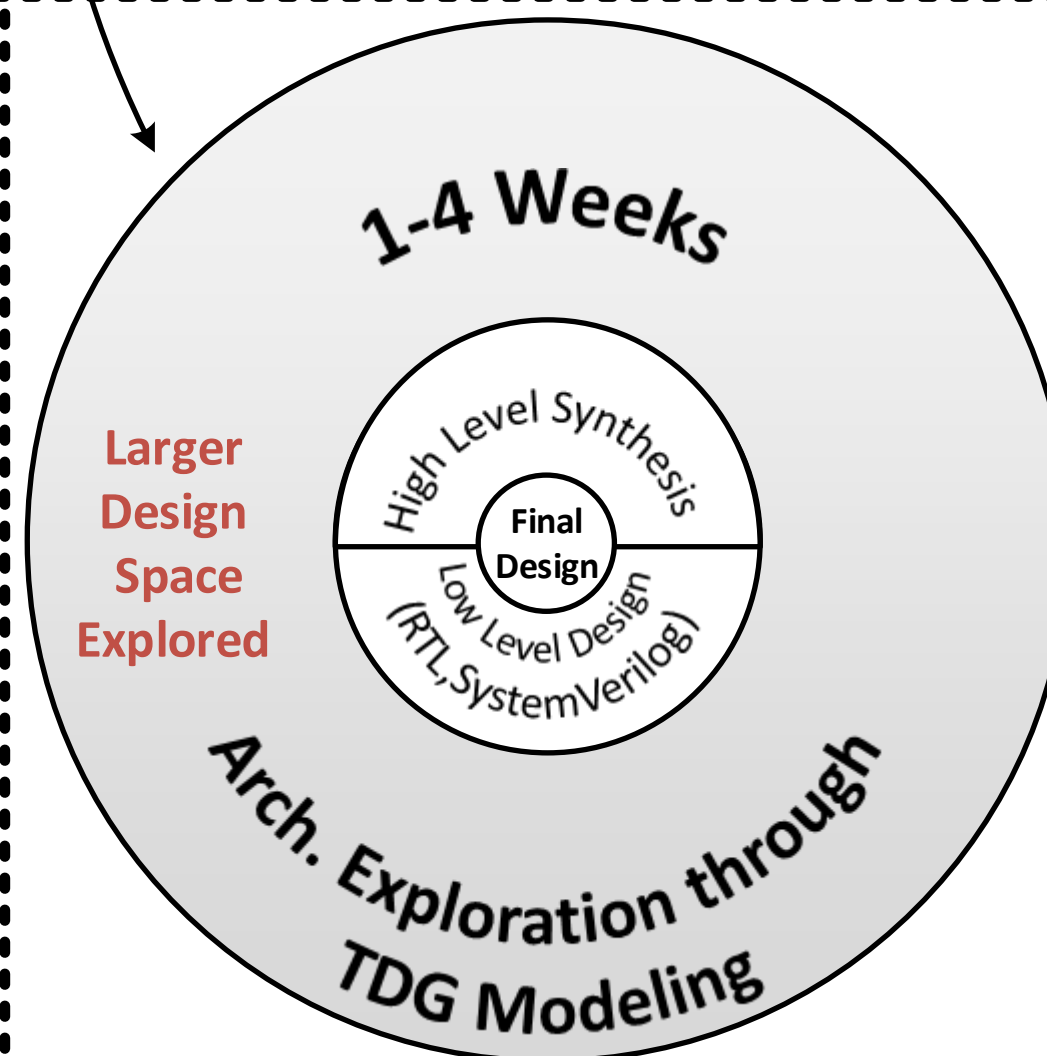


Goal: Fundamental Study of Accelerators

(b) Effective Processes/Practices for Accelerator Design

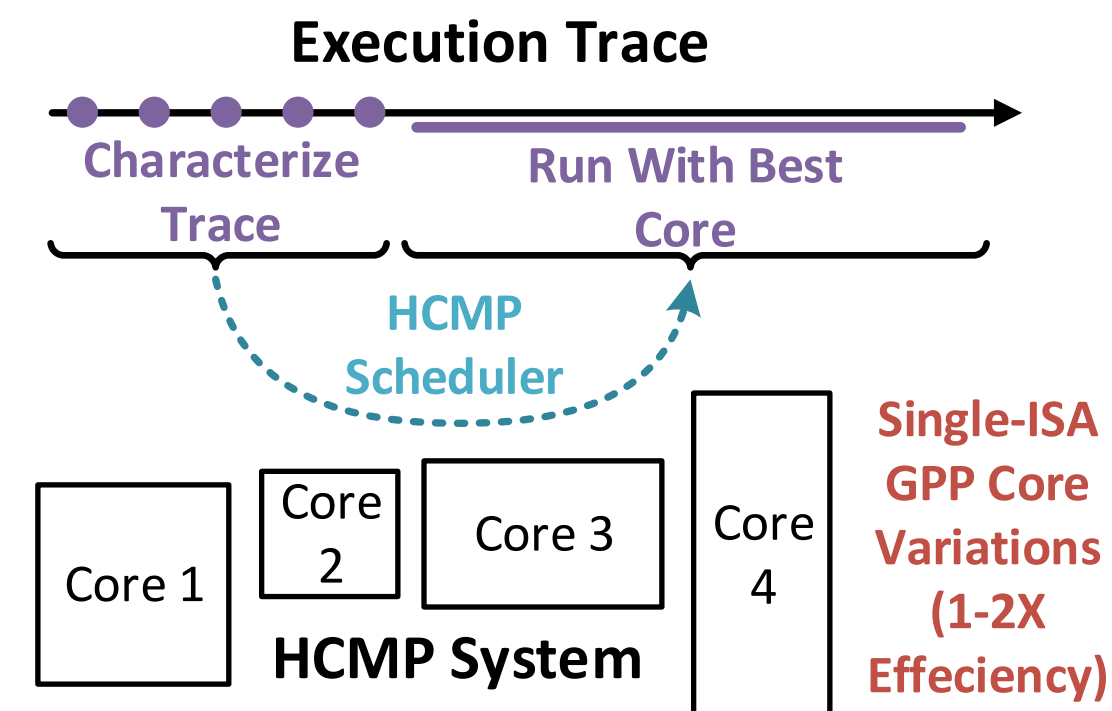


Problem: Time-Consuming, Restricted Design Space Search

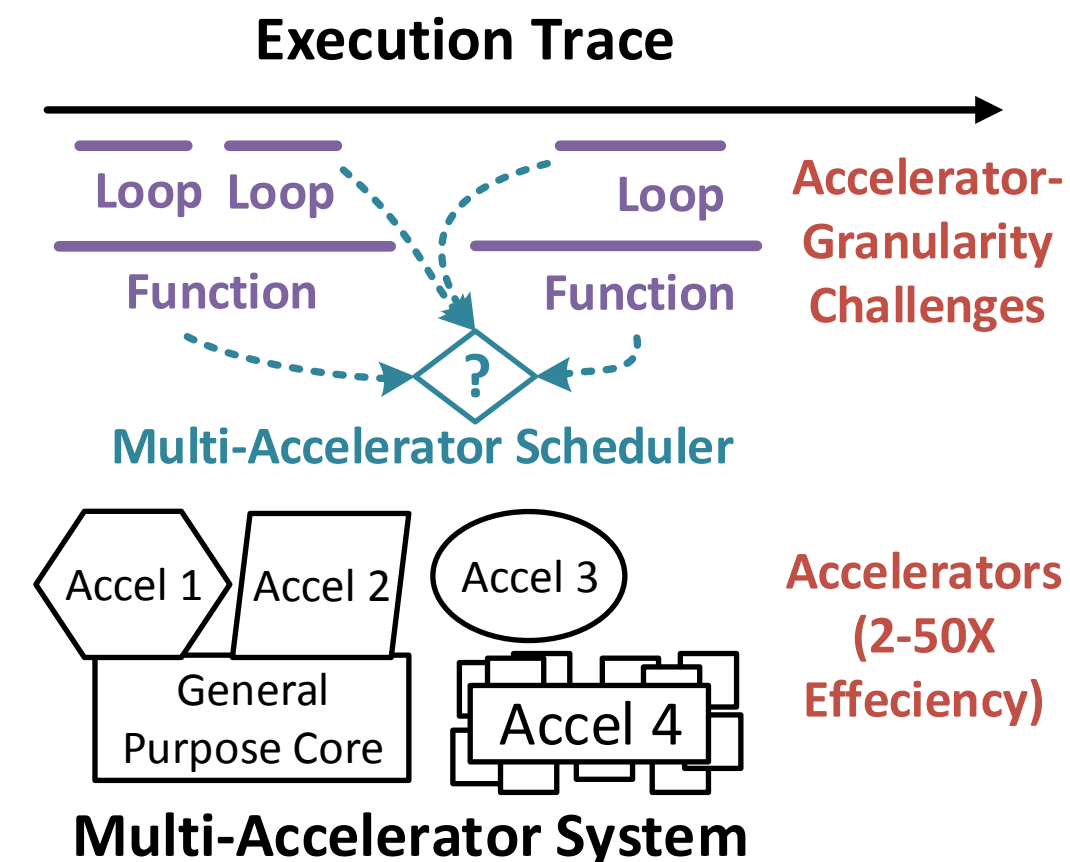


Goal: Broadened & Streamlined Accelerator Design

(c) Flexible Acceleration for Multiple Code Types



Problem: Limited-Benefit, Uniform-ISA Systems Targetted



Goal: Practical Multi-Acceleration for Order-Of-Magnitude Efficiency Gain

TDG Abstraction Enables