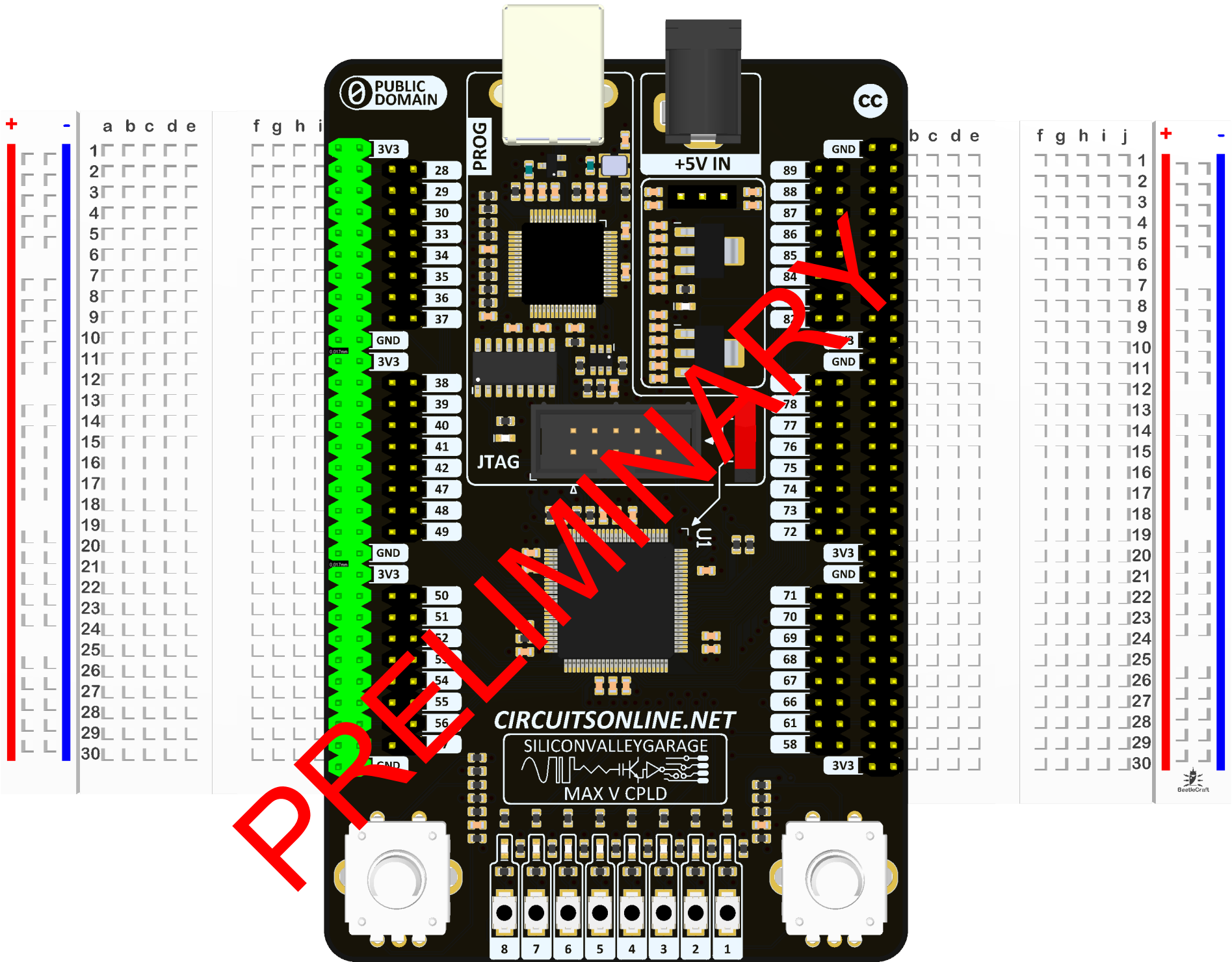
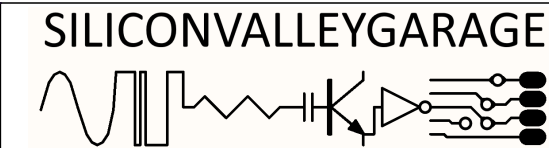


CPLD-V.PrjPcb

Realistic View



Design : Vincent Himpe



Breakout Board for ALTERA 5MXXV in TQFP100 package.

Features :

On-board USB programmer with external target mode (usb blaster compatible pinning).

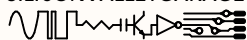
Programmer directly supported in Altera Quartus environment 32/64 bit

Side channel in 245 Async mode for communication with PC application

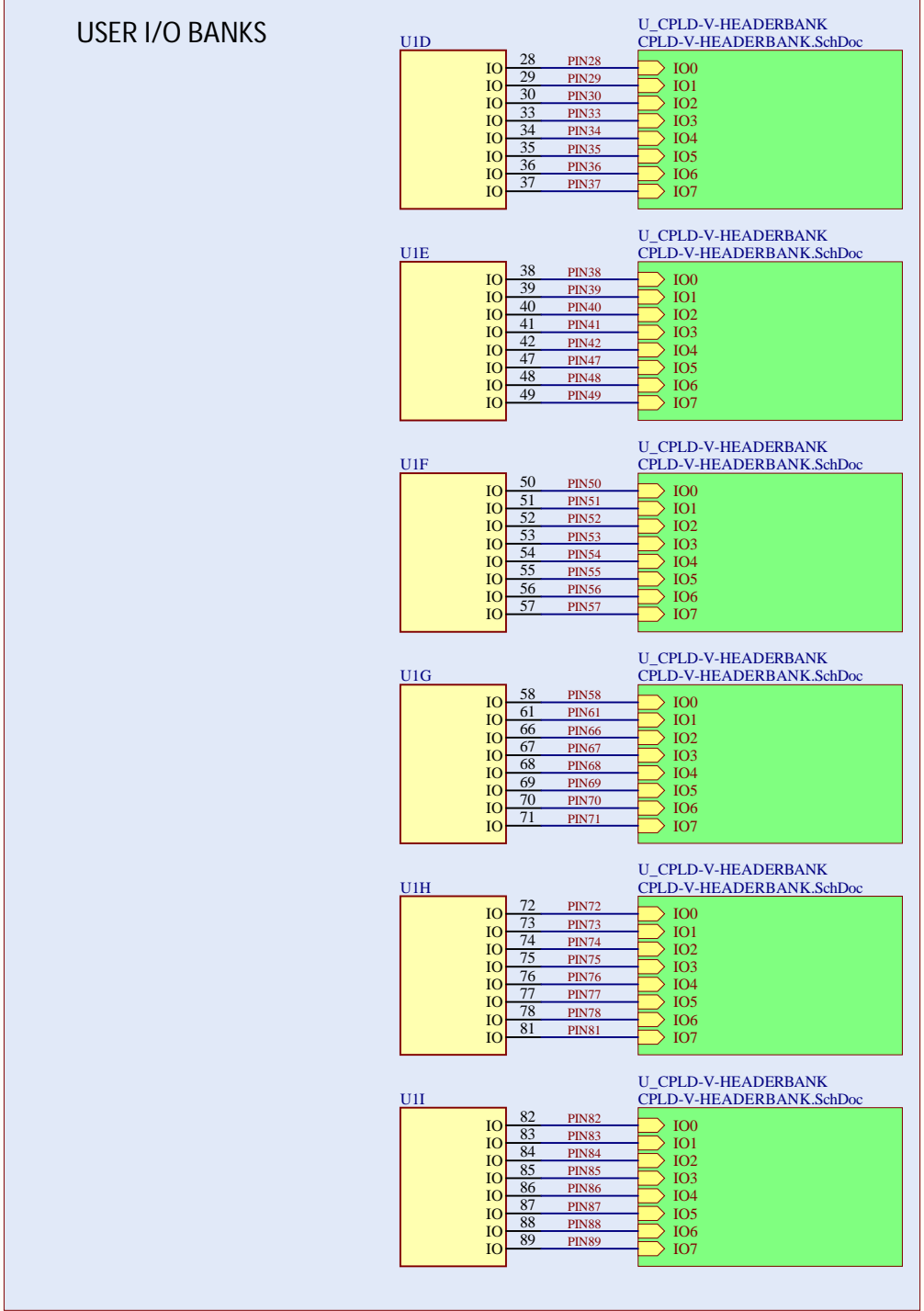
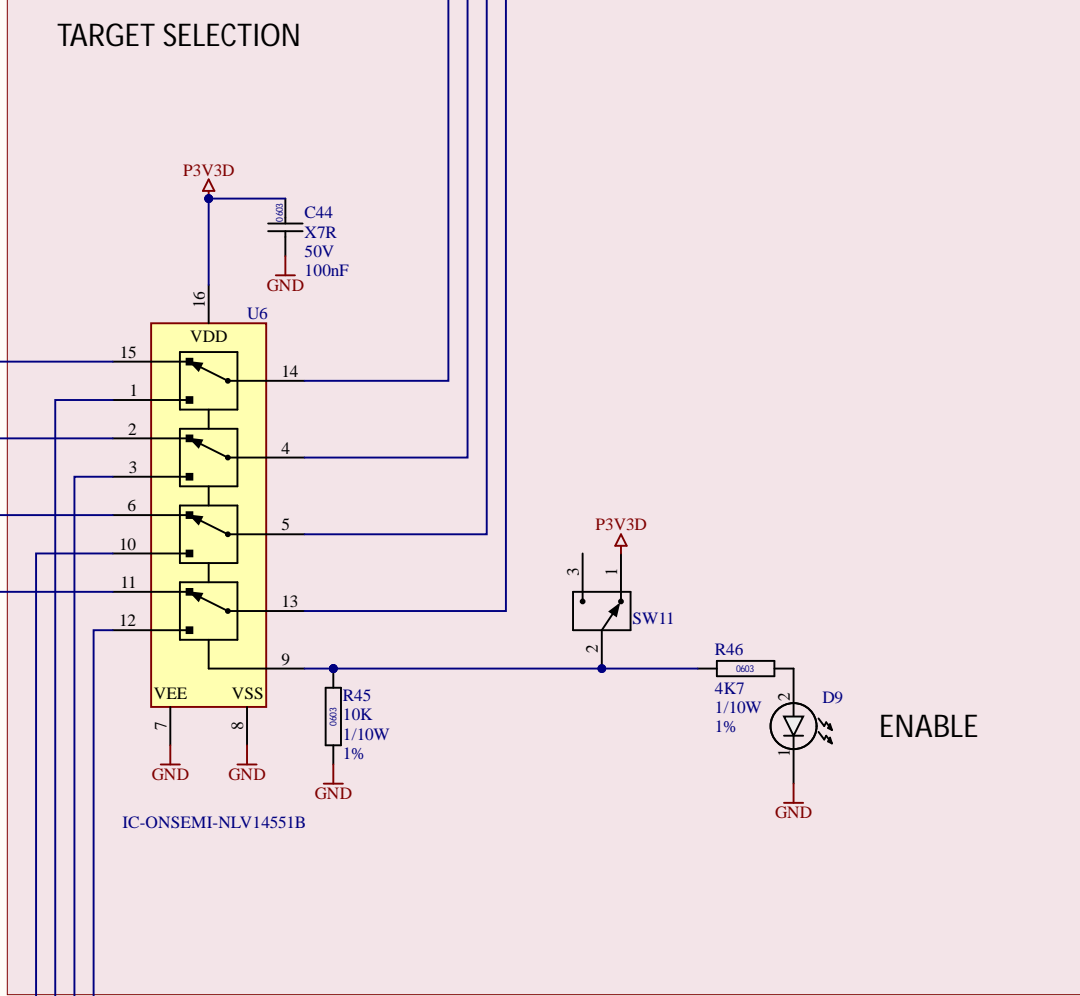
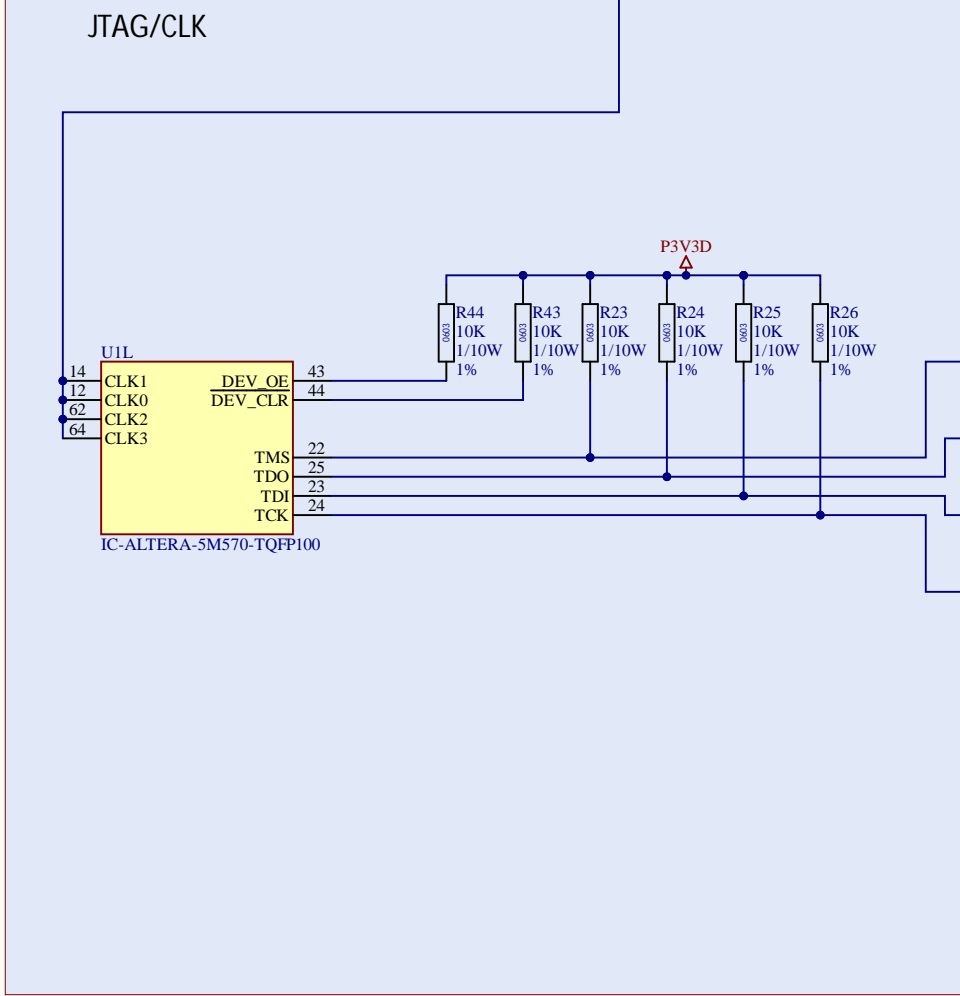
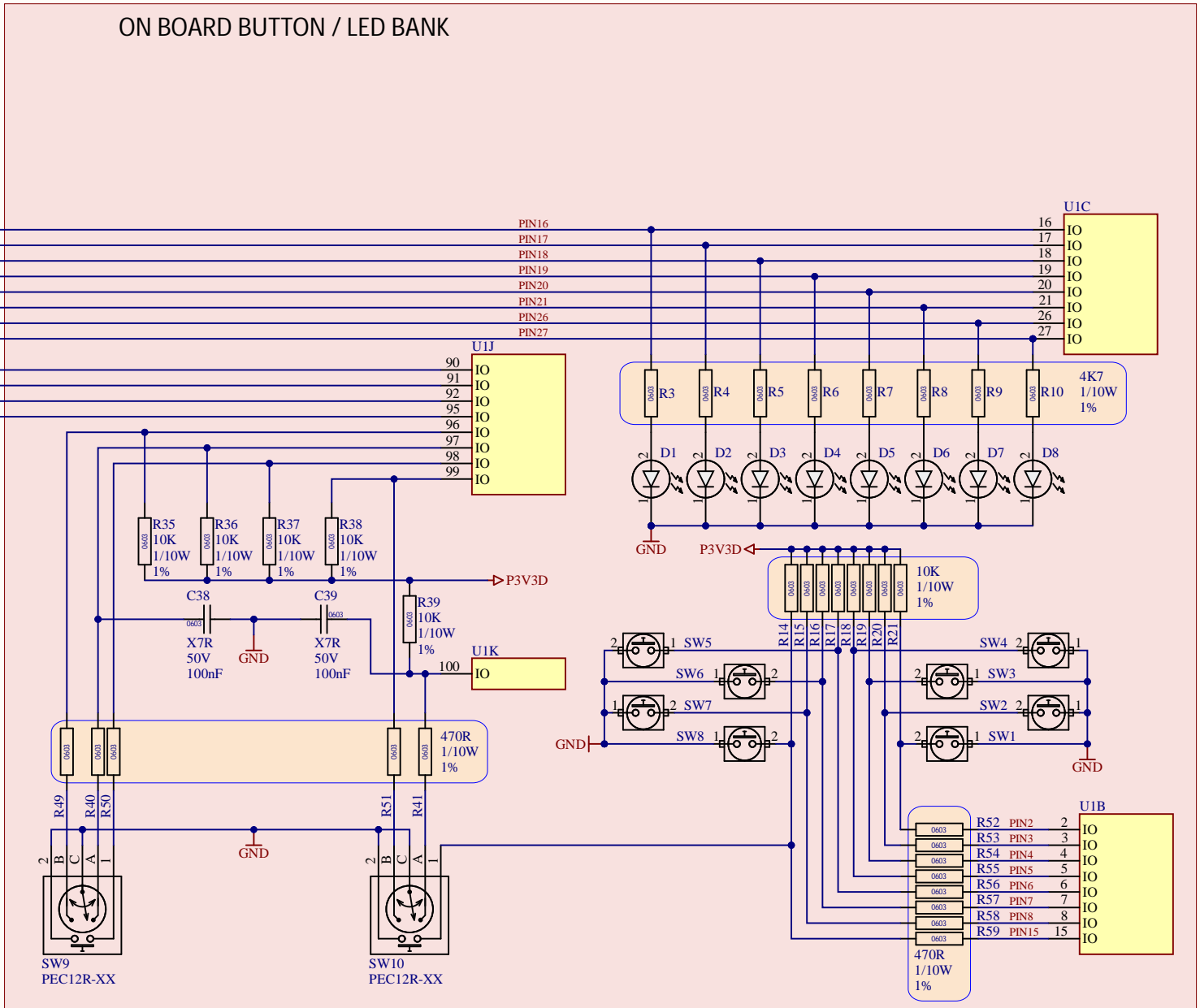
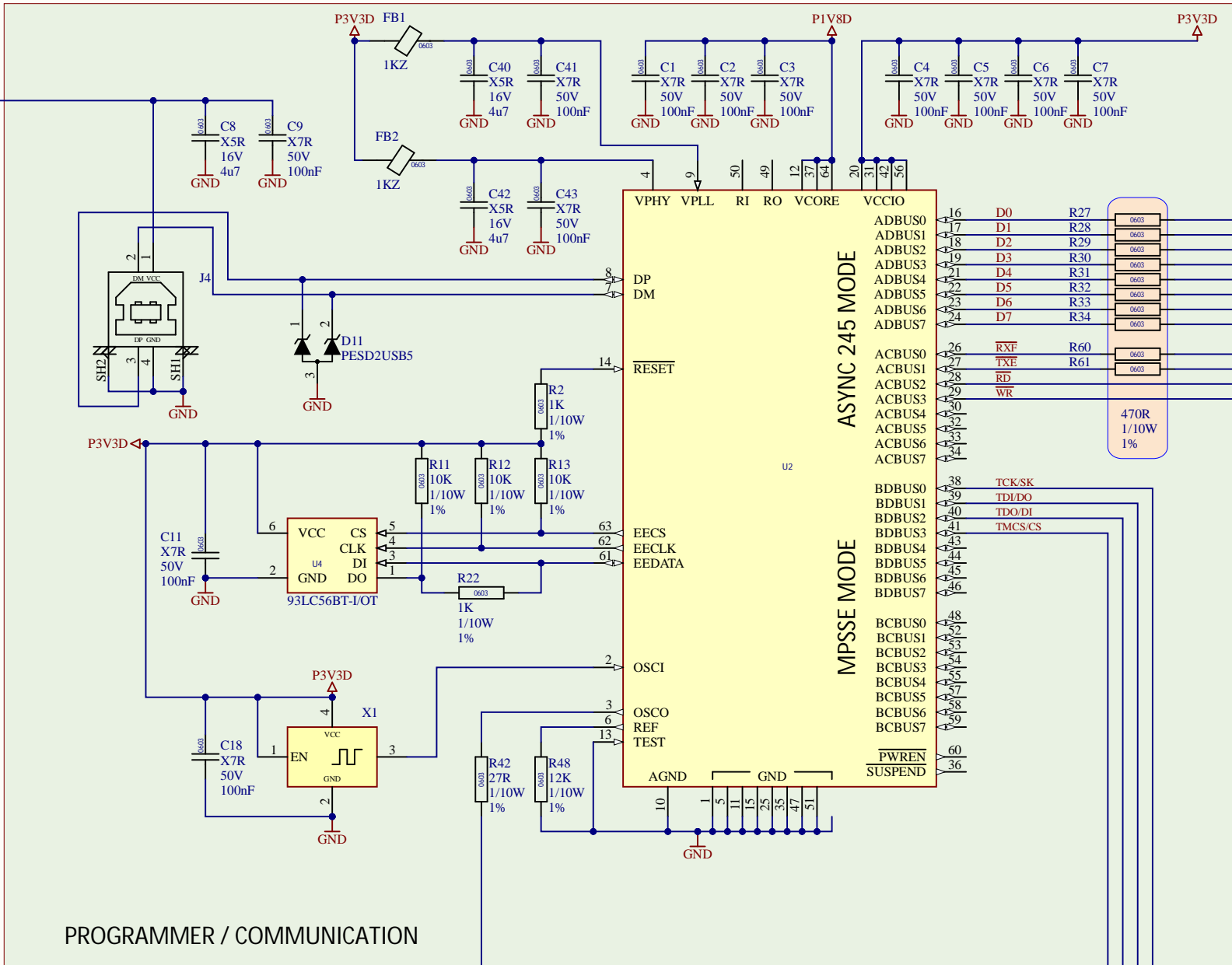
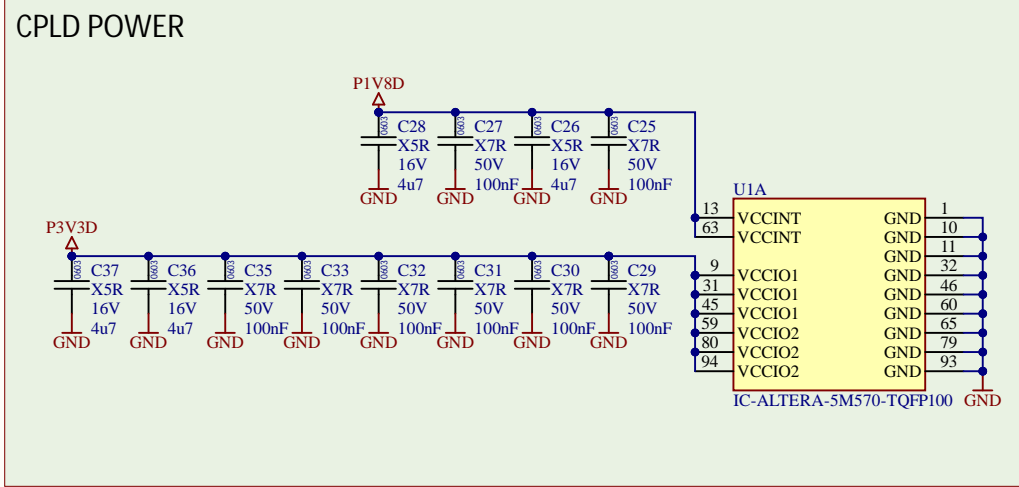
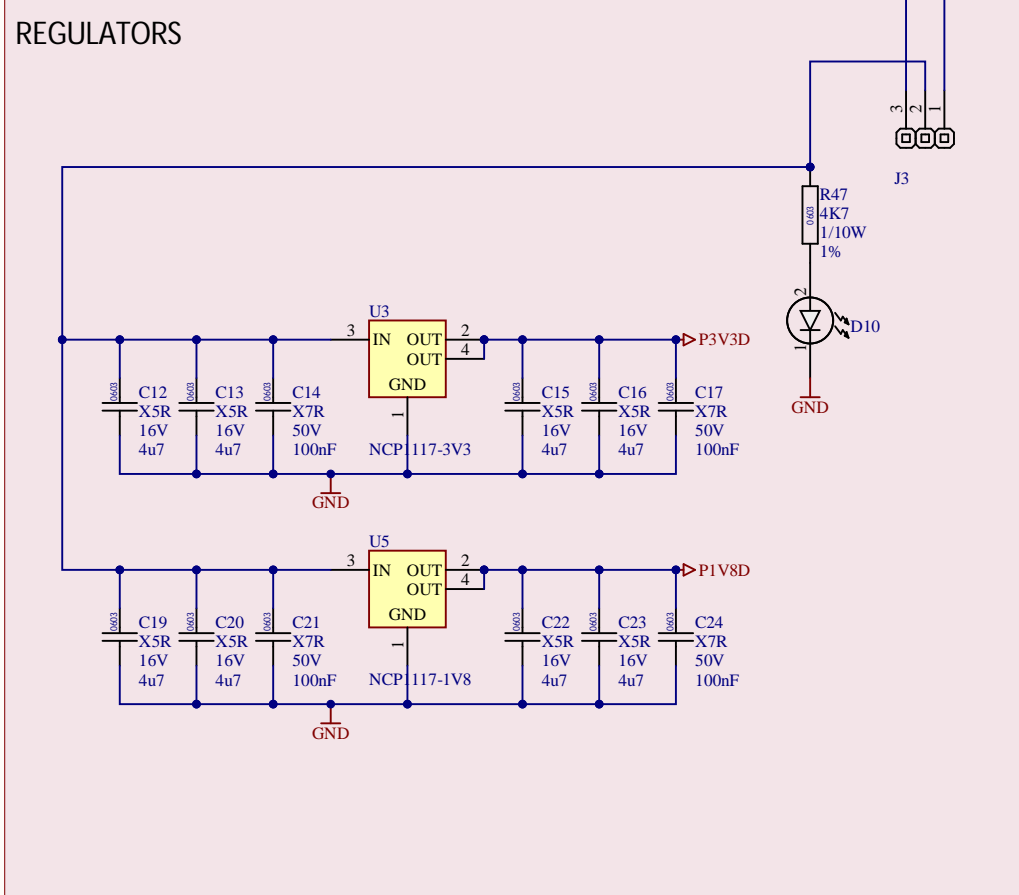
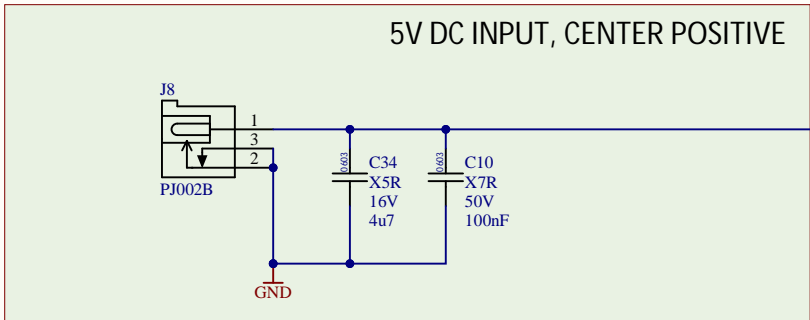
48 GPIO pin on standard headers for breadboard / dupont style connectors.
Each I/O has extra pin for logic probe



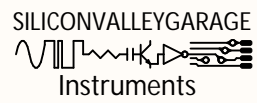
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SILICONVALLEYGARAGE

Instruments

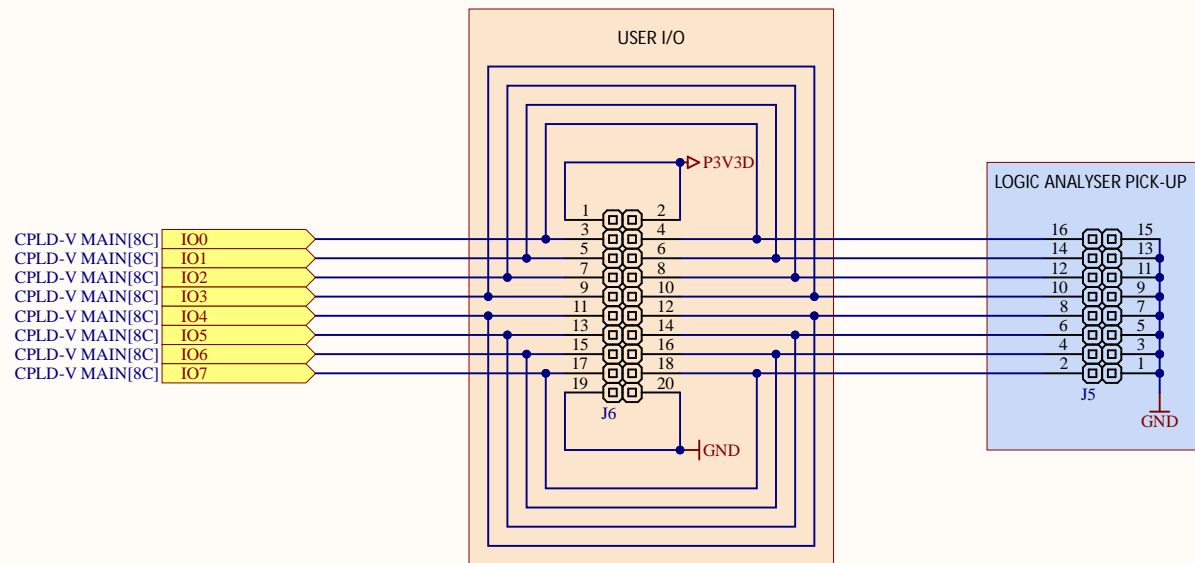
CPLD-V
Breakout Board



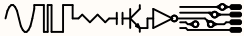
CIRCUITSONLINE.NET



CPLD-V
Breakout Board



SILICONVALLEYGARAGE



Instruments

CPLD-V

Breakout Board