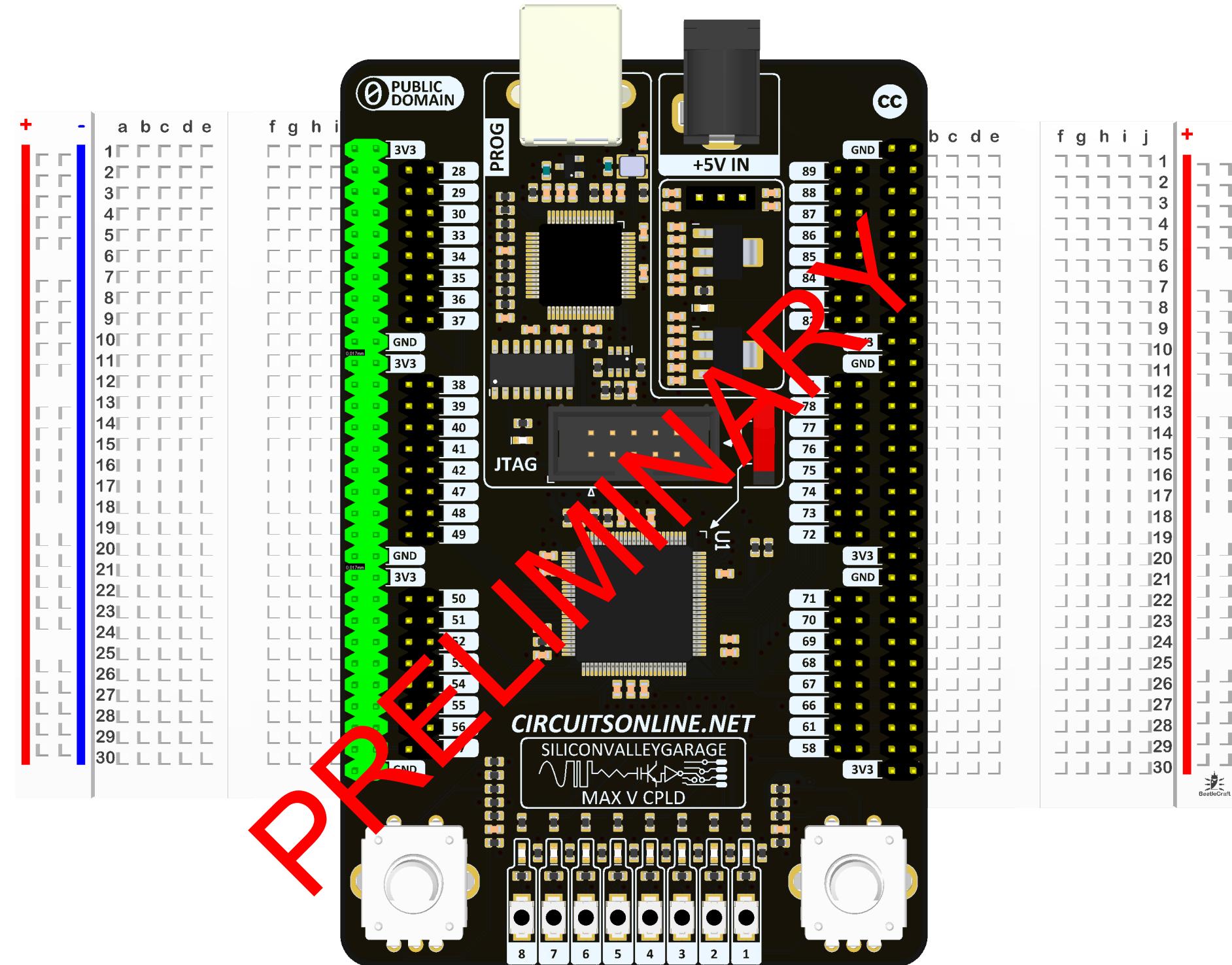


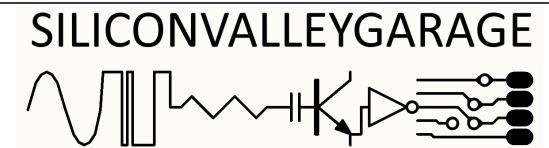
# CPLD-V.PrjPcb

Realistic View



Document Creation Date: 11/14/2025

Design : Vincent Himpe



A Breakout Board for ALTERA 5MXXV in TQFP100 package.

Features :

On-board USB programmer with external target mode (usb blaster compatible pinning).

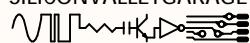
Programmer directly supported in Altera Quartus environment 32/64 bit

Side channel in 245 Async mode for communication with PC application

B 48 GPIO pin on standard headers for breadboard / dupont style connectors.  
Each I/O has extra pin for logic probe

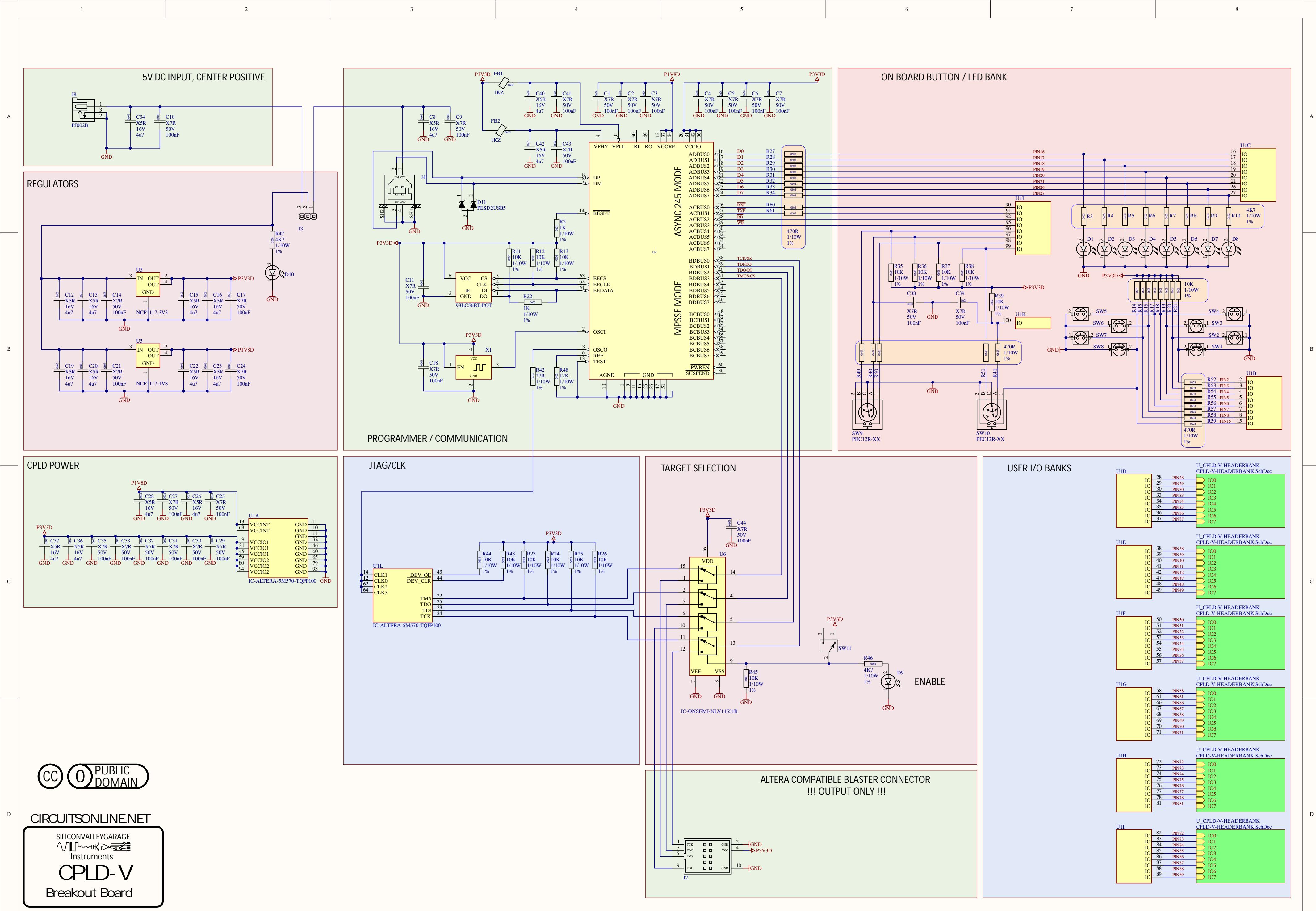


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SILICONVALLEYGARAGE  
  
Instruments

D CPLD-V

Breakout Board



A

A

B

B

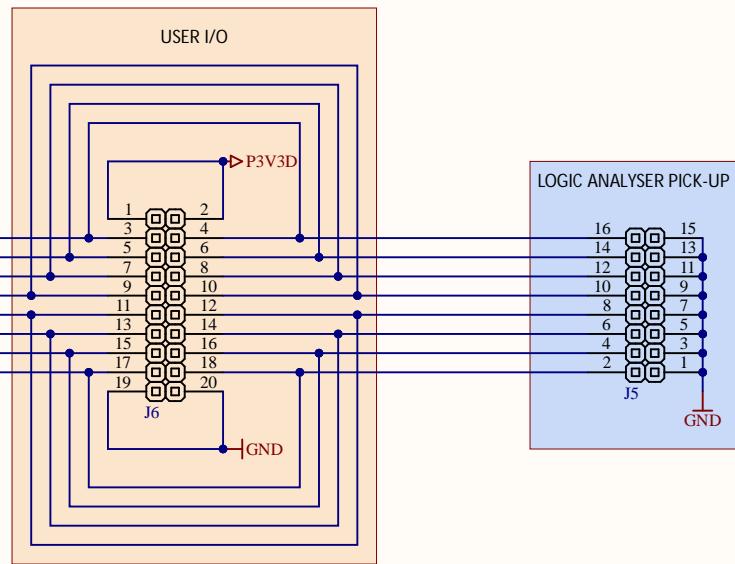
C

C

D

D

CPLD-V MAIN[8C]  
IO0  
CPLD-V MAIN[8C]  
IO1  
CPLD-V MAIN[8C]  
IO2  
CPLD-V MAIN[8C]  
IO3  
CPLD-V MAIN[8C]  
IO4  
CPLD-V MAIN[8C]  
IO5  
CPLD-V MAIN[8C]  
IO6  
CPLD-V MAIN[8C]  
IO7



SILICONVALLEYGARAGE  
Instruments

**CPLD-V**  
Breakout Board