## **Today**

- Prim's Algorithm
- Huffman Encoding

What is a cut in an undirected graph, G = (V, E)?

- (A) A set of edges whose removal disconnects a graph.
- (B) For partition of V, (S, V S), set of edges across it;  $E \cap (S \times V S)$ .

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(A)

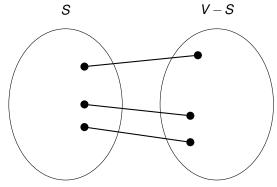
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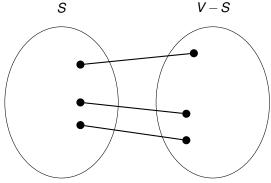
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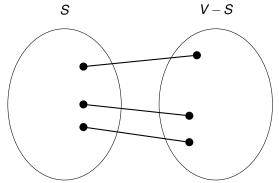


Note:

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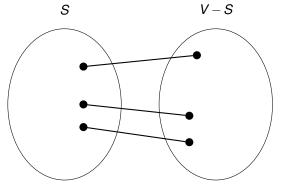


Note:sometimes specified as (S, V - S)

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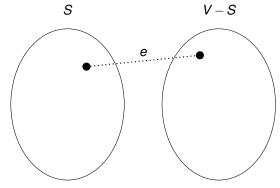


Note:sometimes specified as (S, V - S) ..... sometimes explicitly as subset of edges E'.

- (A) Any edge in a cut is in some mst.
- (B) The smallest edge in a cut is in some mst.
- (C) The largest edge in a cut cannot be in an mst.

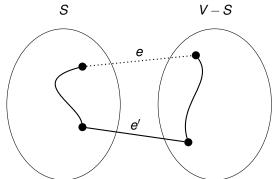
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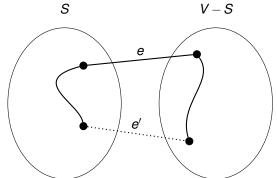
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**Cut Property:** Any edge of minimal weight in a cut is in some MST. (If unique, it must be in MST.)

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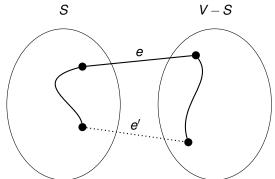


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Proof: replace,

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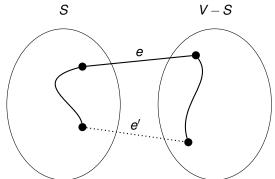
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**Cut Property:** Any edge of minimal weight in a cut is in some MST. (If unique, it must be in MST.) Proof: replace, n-1 edges

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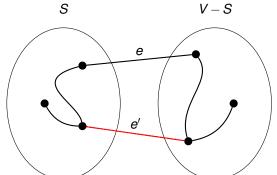


**Cut Property:** Any edge of minimal weight in a cut is in some MST. (If unique, it must be in MST.)

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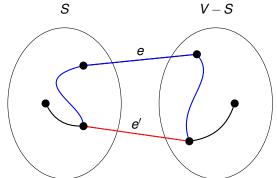


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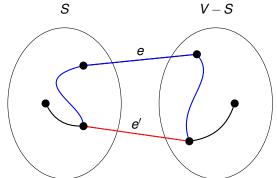


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Break ties for smallest edge according to lowest neighbors.

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Actually...

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Find smallest edge (x,y)? O(m) time.

O(nm) time.

Look at same edges over and over again!

Use a priority queue to keep edges!

Actually... use a priority queue to keep "closest" node.

#### Prim(G,s)

**foreach** 
$$v \in V$$
:  $c(v) = \infty$ ,  $prev(v) = nil$   $c(s) = 0$ ,  $prev(s) = s$ 

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 $H = make\_pqueue(V,c)$ 

## Prim(G,s) foreach $v \in V$ : $c(v) = \infty$ , prev(v) = nil c(s) = 0, prev(s) = s $H = make\_pqueue(V,c)$ while (v = deletemin(H)):

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decreaseKey(H,w)
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Runtime? \Theta(mn)? \Theta((m+n)\log n)?
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if 
$$(c(v)+w(v,w) < c(w))$$
:  
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Runtime?  $\Theta(mn)$ ?  $\Theta((m+n)\log n)$ ?  $\Theta(m+n\log n)$ ?  $O((m+n)\log n)$ 

With Fibonacci Heaps:  $O(m + n \log n)$ .

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16 characters alphabet, four bits/character.

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Separate using pauses in morse code.. for binary?

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Letters: A,B,C,D.

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A:00 B:01 C:10 D:11

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What is 100011?

First two: "C"

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What is 100011?

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What is 100011?

First two: "C" Next two: "A" Third two: "D"

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Can decode!

Another prefix free code for A,B,C,D.

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(A:0),

Another prefix free code for A,B,C,D.

(A:0), (B:10),

Another prefix free code for A,B,C,D.

(A:0),(B:10),(C:110),

Another prefix free code for A,B,C,D.

(A:0),(B:10),(C:110),(D:111)

Another prefix free code for A,B,C,D.

"110010" ???

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"110010"???

C

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C A

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"110010" ???

CAB

Consists of letters A, C, T, G with varying frequencies.

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

A: .4

C: .1

T: .2

G: .3

Consists of letters A, C, T, G with varying frequencies.

A: .4

C: .1

T: .2 G: .3

Expected length of fixed length encoding for *N* chars:

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

A: .4

C: .1

T: .2 G: .3

Expected length of fixed length encoding for N chars: 2N bits.

Consists of letters A, C, T, G with varying frequencies.

A: .4

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T: .2 G: .3

Expected length of fixed length encoding for *N* chars: 2*N* bits.

A: .4

Consists of letters A, C, T, G with varying frequencies.

A: .4

C: .1

T: .2

G: .3

Expected length of fixed length encoding for N chars: 2N bits.

A: .4 0

C: .1

Consists of letters A, C, T, G with varying frequencies.

A: .4

C: .1

T: .2

G: .3

Expected length of fixed length encoding for N chars: 2N bits.

A: .4 (

C: .1 100

T: .2

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

A: .4

C: .1

T: .2

G: .3

Expected length of fixed length encoding for N chars: 2N bits.

A: .4 0

C: .1 100

T: .2 101

G: .3

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

A: .4

C: .1

T: .2

G: .3

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A: .4 0

C: .1 100

T: .2 101

G: .3 11

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

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G: .3

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T: .2

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Expected length of fixed length encoding for N chars: 2N bits.

A: .4 C

C: .1 100

T: .2 101

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#### Yes!

Expected length: N(.4\*1

Consists of letters *A*, *C*, *T*, *G* with varying frequencies.

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C: .1

T: .2 G: .3

Expected length of fixed length encoding for N chars: 2N bits.

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C: .1 100

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### Prefix Free?

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• • •

### Yes!

Expected length: N(.4\*1 + .1\*3 +

Consists of letters A, C, T, G with varying frequencies.

A: .4

C: .1

T: .2

G: .3

Expected length of fixed length encoding for N chars: 2N bits.

A: .4 C

C: .1 100

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#### Yes!

Expected length: N(.4\*1 + .1\*3 + .2\*3)

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#### Prefix Free?

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#### Yes!

Expected length: N(.4\*1 + .1\*3 + .2\*3 + .3\*2) = 1.9N

Each prefix-free codes corresponds to a full binary trees:

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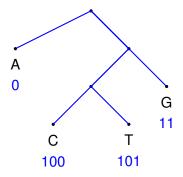
Our Example:

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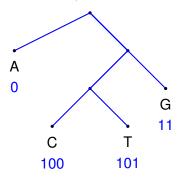


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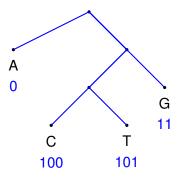
011101100

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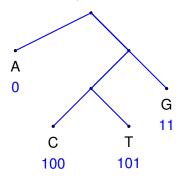
011101100 0 11 101 100

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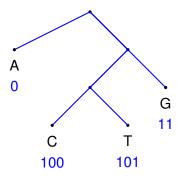
011101100 0 11 101 100 A

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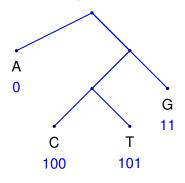
011101100 0 11 101 100 A G

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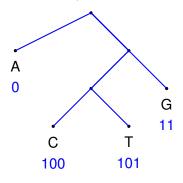
011101100 0 11 101 100 A G C

Each prefix-free codes corresponds to a full binary trees:

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#### Our Example:



011101100 0 11 101 100 AGCT

Given symbol frequencies  $f_1, \ldots, f_n$ , find "best" prefix code.

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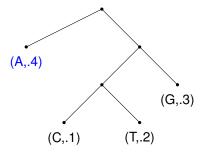
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Cost: .4 \* 1

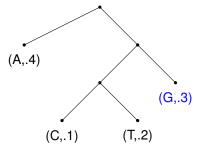
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Cost: .4 \* 1 + .3 \* 2

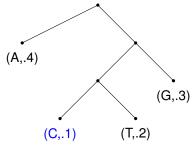
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Cost: .4 \* 1 + .3 \* 2 + .1 \* 3

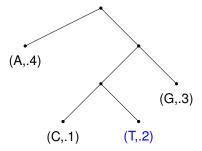
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Cost: .4 \* 1 + .3 \* 2 + .1 \* 3 + .2 \* 3

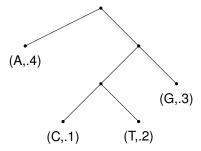
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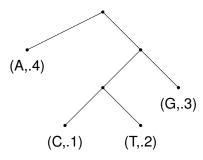
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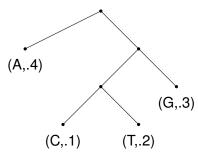
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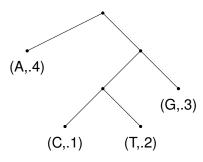
Example: (A,.4), (C,.1),(T,.2),(G,.3)



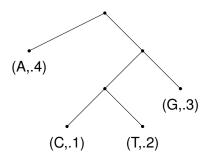
Cost: .4 \* 1 + .3 \* 2 + .1 \* 3 + .2 \* 3 = 1.9



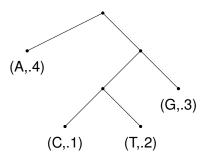




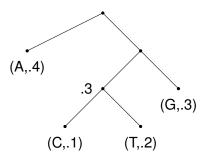
$$.4 + .1$$



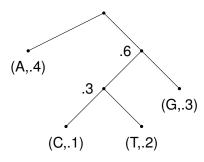
$$.4 + .1 + .2$$



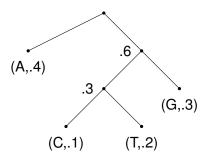
$$.4 + .1 + .2 + .3$$



$$.4 + .1 + .2 + .3 + .3$$



$$.4 + .1 + .2 + .3 + .3 + .6$$



$$.4 + .1 + .2 + .3 + .3 + .6 = 1.9$$

Recursive View:

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Might as well merge two lowest frequency symbols... to make low freq internal symbol.

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 $(\{A,\{\{C,T\},G\}\},1)$ 

Cost2: Sum over all nodes, except root, of their "frequency".

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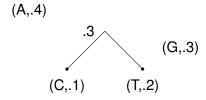
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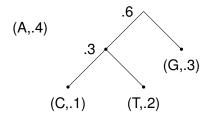


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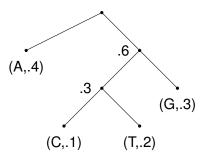


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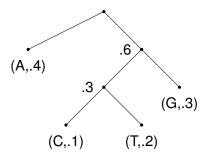
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Implementation: priority queue to get lowest frequency trees.

Recall MST: added a "could have" edge in tree every time.

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Huffman: An optimal tree

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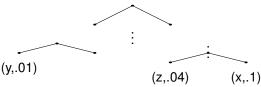
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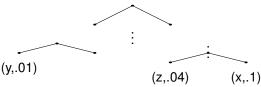
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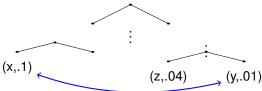
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Cost gets better with this switch.

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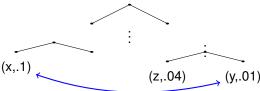
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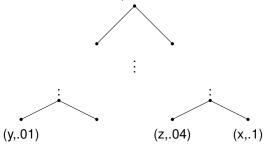
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Otherwise ... switch each with deepest pair of siblings improves tree.

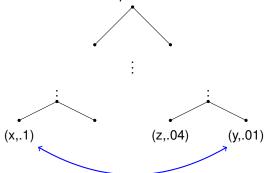


Cost gets better with this switch. Lowest frequency made siblings.

#### What about same depth?

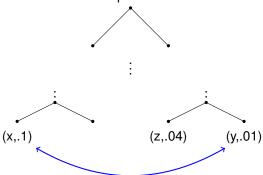


#### What about same depth?



Cost stays the same,

#### What about same depth?



Cost stays the same, but can make lowest into sibling.

## Huffman codes wrap-up

Questions ??

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Questions ?? Can the shape of a Huffman tree be a

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- 1. Caterpillar? Yes / No?
- 2. Full balanced binary tree on *k* levels? Yes/No?

## Huffman codes wrap-up

Questions ??

Can the shape of a Huffman tree be a

- 1. Caterpillar? Yes / No?
- 2. Full balanced binary tree on *k* levels? Yes/No?
- 3. Any shape of a full binary tree? Yes/No?

a = http.read\_response();

```
a = http.read_response();
```

```
a = http.read\_response();

\vdots

b = a + c;

\vdots
```

```
a = http.read_response();
:
b = a + c;
:
d = sql_command(b);
```

```
a = http.read_response();
:
b = a + c;
:
d = sql_command(b);
a is input from web.
```

```
a = http.read_response();
:
b = a + c;
:
d = sql_command(b);
a is input from web.
"a is tainted."
```

```
a = http.read_response();

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d = sql_command(b);

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a is tainted."

if a is tainted b is tainted."
```

```
a = http.read_response();
:
b = a + c;
:
d = sql_command(b);
a is input from web.
"a is tainted."
"if a is tainted b is tainted."
"b should not be tainted."
```

```
a = http.read_response();
b = a + c;
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a is input from web.
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Logic representation:
```

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a = http.read_response();
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Logic representation:
A - "a is tainted"
```

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A - "a is tainted"
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\implies A
```

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"a is tainted."
"if a is tainted b is tainted."
"b should not be tainted."
Logic representation:
A - "a is tainted"
B - "b is tainted"
\implies A, A \implies B
```

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a = http.read_response();
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a is input from web.
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A - "a is tainted"
B - "b is tainted"
\implies A, A \implies B, \overline{B}.
```

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a is input from web.
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```

Satisfiable?

```
a = http.read_response();
b = a + c;
d = sql_{command}(b);
a is input from web.
"a is tainted."
"if a is tainted b is tainted."
"b should not be tainted."
Logic representation:
A - "a is tainted"
B - "b is tainted"
\implies A, A \implies B, \overline{B}.
```

Satisfiable?

Not in this case.

Implications:

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**And** of positive literals imply **one** positive literal.

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$$X \wedge Y \rightarrow Z$$

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Negative clauses:

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### Negative clauses:

**Or** of negative literals.

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Or of negative literals.

$$\overline{u} \vee \overline{v}$$

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**And** of positive literals imply **one** positive literal.

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Taint Example:

#### Implications:

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$$X \wedge V \rightarrow Z$$

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Or of negative literals.

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Taint Example:

$$\implies$$
 A, A  $\implies$  B,  $\overline{B}$ .

Implications:

**And** of positive literals imply **one** positive literal.

$$X \wedge Y \rightarrow Z$$

Negative clauses:

Or of negative literals.

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Taint Example:

$$\implies$$
 A, A  $\implies$  B,  $\overline{B}$ .

Is this satisfiable?

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

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Problem: Find consistent assignment with fewest "True" variables.

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm:

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Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

#### Example:

 $x_1$  must be true

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

#### Example:

 $x_1$  must be true so  $x_3$  must be true

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

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#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true

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Problem: Find consistent assignment with fewest "True" variables.

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#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

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#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true Solution:

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

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#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true Solution:  $\{x_1, x_2, x_3, x_4\}$  are True

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true Solution:  $\{x_1, x_2, x_3, x_4\}$  are True

Could also set  $x_5$  to true, or both  $x_5$  and  $x_6$  to true...

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true Solution:  $\{x_1, x_2, x_3, x_4\}$  are True

Could also set  $x_5$  to true, or both  $x_5$  and  $x_6$  to true...but don't!

$$\begin{array}{ccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

Problem: Find consistent assignment with fewest "True" variables.

Greedy algorithm: Only set variables to true if you have to.

#### Example:

 $x_1$  must be true so  $x_3$  must be true so  $x_2$  must be true so  $x_4$  must be true

Solution:  $\{x_1, x_2, x_3, x_4\}$  are True

Could also set  $x_5$  to true, or both  $x_5$  and  $x_6$  to true...but don't!

Same as horn sat!

Horn SAT had negative clauses.

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No negative clauses for above algorithm.

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No negative clauses for above algorithm.

Algorithm: Set a variable true

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Algorithm: Set a variable true ..if you have to!

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Algorithm: Set a variable true .. if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

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Algorithm: Set a variable true .. if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

By induction.

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Algorithm: Set a variable true ..if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

By induction. First *k* set to true...

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Algorithm: Set a variable true ..if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

By induction. First *k* set to true... must be!

The k+1 set variable set to true

Horn SAT had negative clauses.

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Algorithm: Set a variable true .. if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

By induction. First *k* set to true... must be!

The k+1 set variable set to true

is set to true to satisfy a clause

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Algorithm: Set a variable true ..if you have to!

Property: any variable set to true must be true in *any* satisfying assignment.

By induction. First k set to true... must be! The k+1 set variable set to true is set to true to satisfy a clause so it must be true.

Horn SAT had negative clauses.

No negative clauses for above algorithm.

Algorithm: Set a variable true ..if you have to!

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Horn has negative clauses.

Negative clauses only problem for true variables.

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The k+1 set variable set to true

is set to true to satisfy a clause so it must be true.

Horn has negative clauses.

Negative clauses only problem for true variables.

Any variable that is true must be true.

So if a negative clause is false, it must be.

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

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For each clause: keep count of true antecedents:

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

For each clause: keep count of true antecedents: When all antecedents true, than make consequent true.

$$\begin{array}{cccc} x_1 \wedge x_2 & \Longrightarrow & x_4 \\ x_3 & \Longrightarrow & x_2 \\ x_1 & \Longrightarrow & x_3 \\ x_5 \wedge x_1 & \Longrightarrow & x_3 \\ x_2 \wedge x_6 & \Longrightarrow & x_5 \\ & \Longrightarrow & x_1 \end{array}$$

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Data Structure:

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For each clause: keep count of true antecedents:

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#### Data Structure:

Connect variable to clauses with var as antecendent.

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For each clause: keep count of true antecedents:

When all antecedents true, than make consequent true.

#### Data Structure:

Connect variable to clauses with var as antecendent.

When variable is set to true see if connected clauses are invoked.