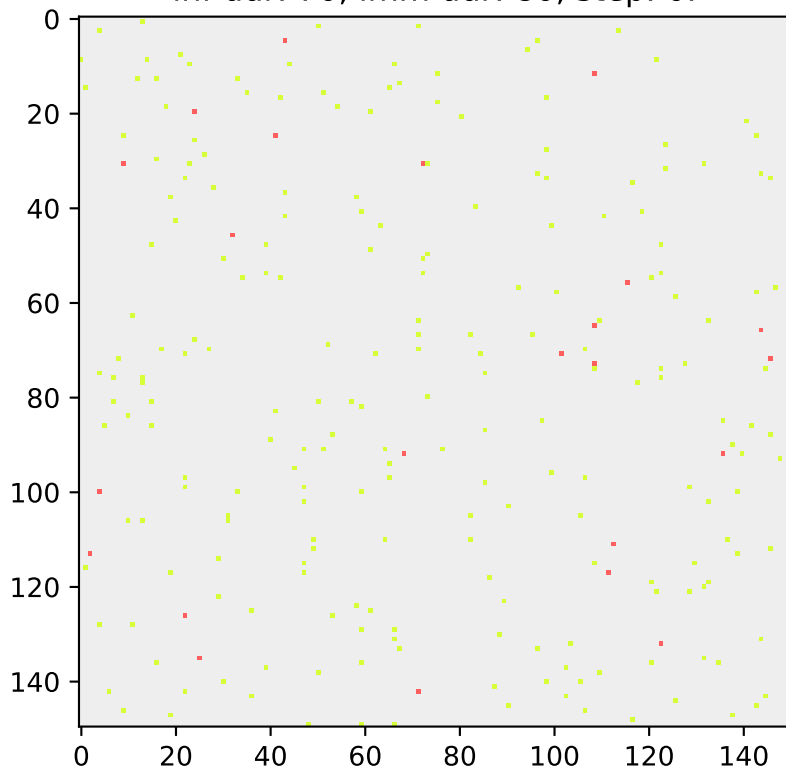
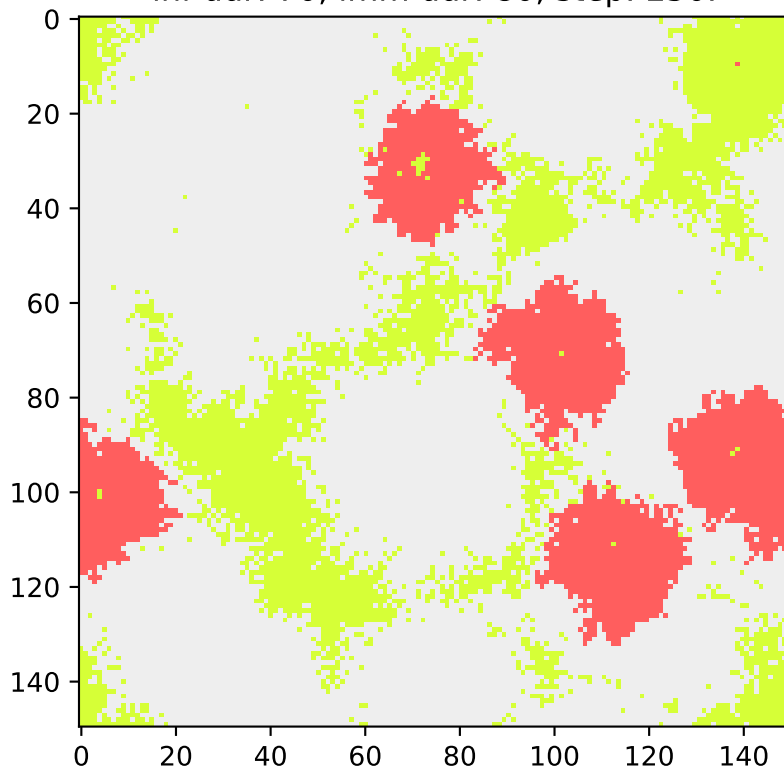


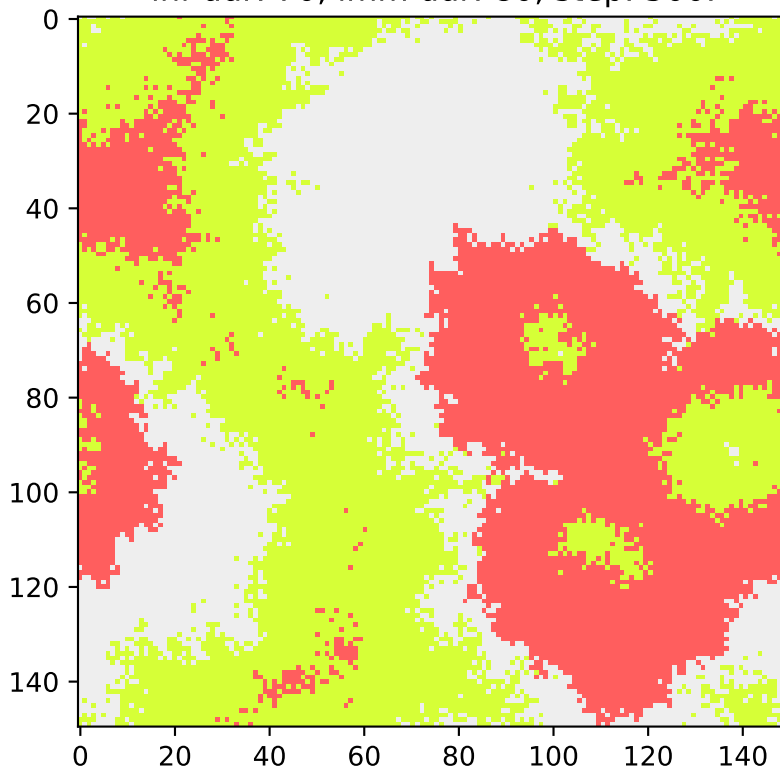
inf dur: 70, imm dur: 80, step: 0.



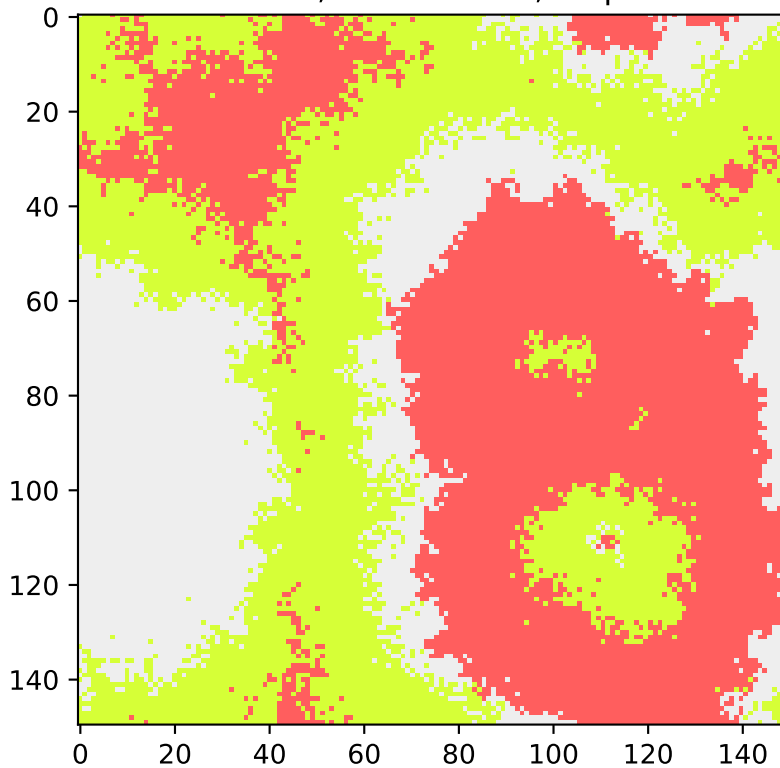
inf dur: 70, imm dur: 80, step: 250.



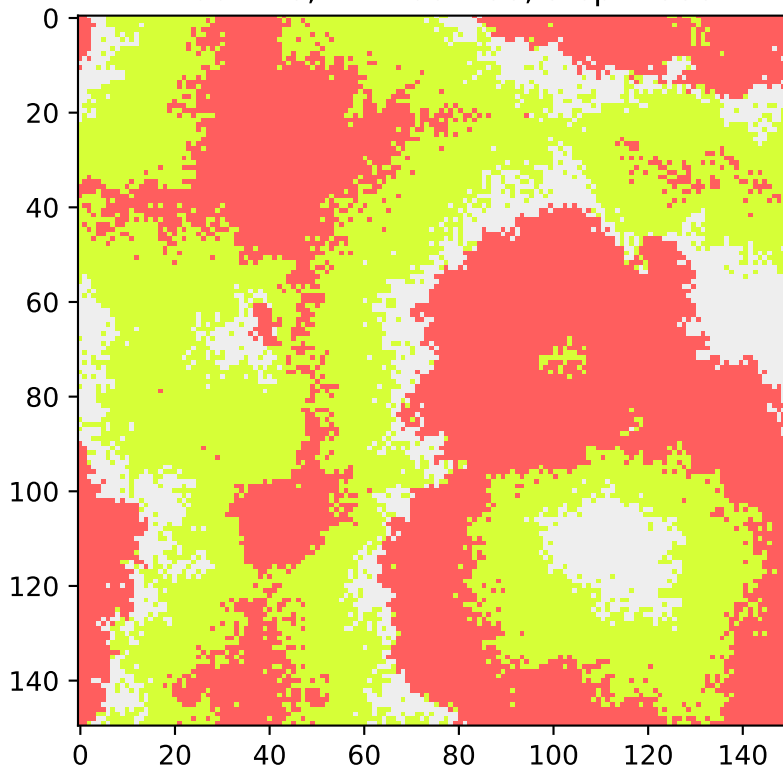
inf dur: 70, imm dur: 80, step: 500.



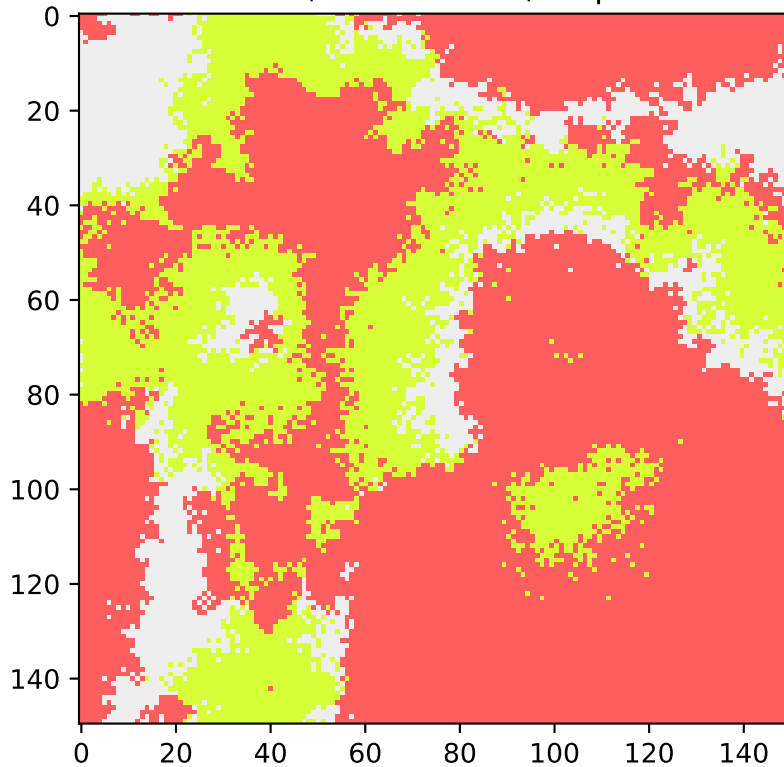
inf dur: 70, imm dur: 80, step: 750.



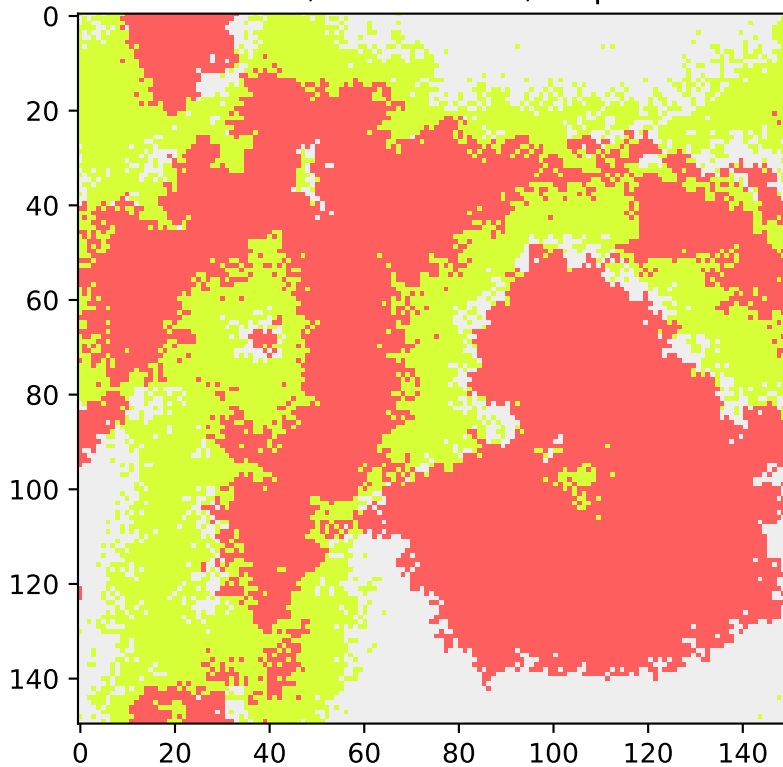
inf dur: 70, imm dur: 80, step: 1000.



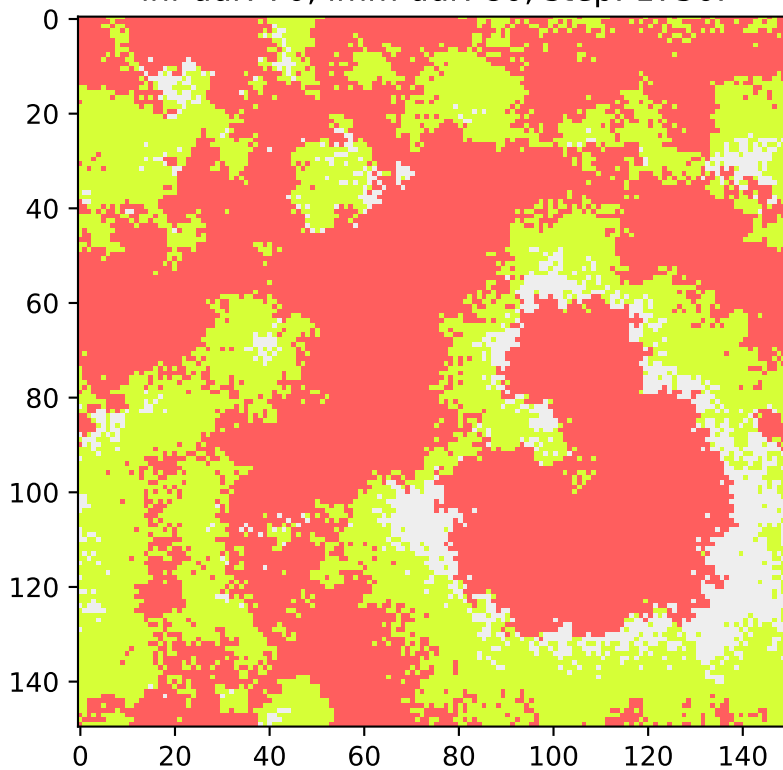
inf dur: 70, imm dur: 80, step: 1250.



inf dur: 70, imm dur: 80, step: 1500.

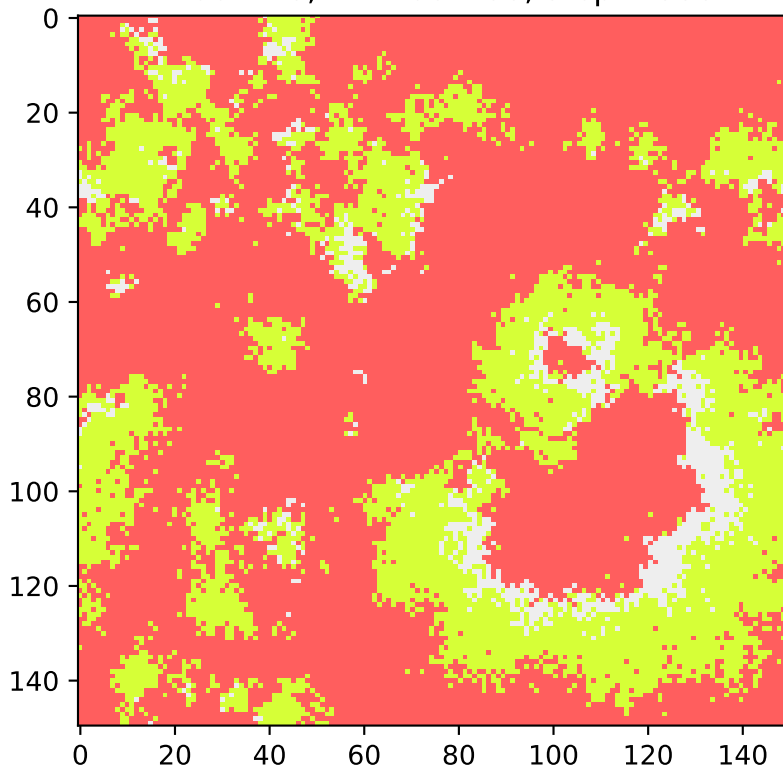


inf dur: 70, imm dur: 80, step: 1750.

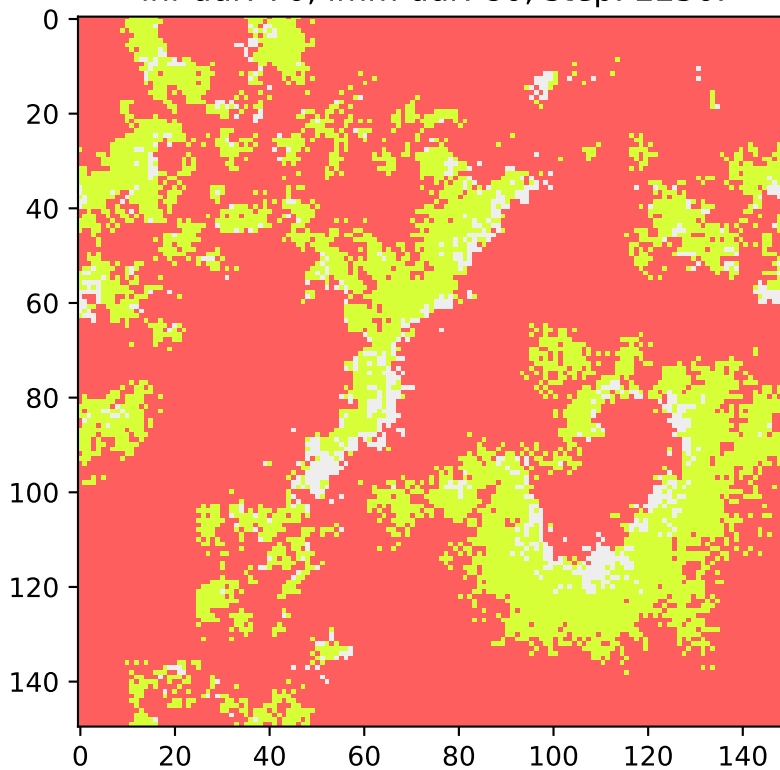




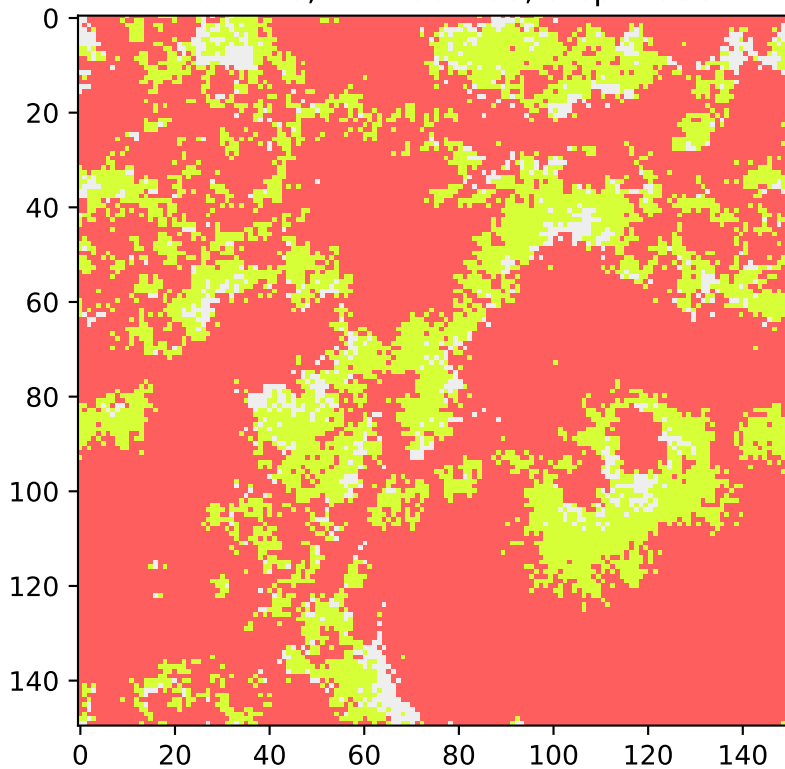
inf dur: 70, imm dur: 80, step: 2000.



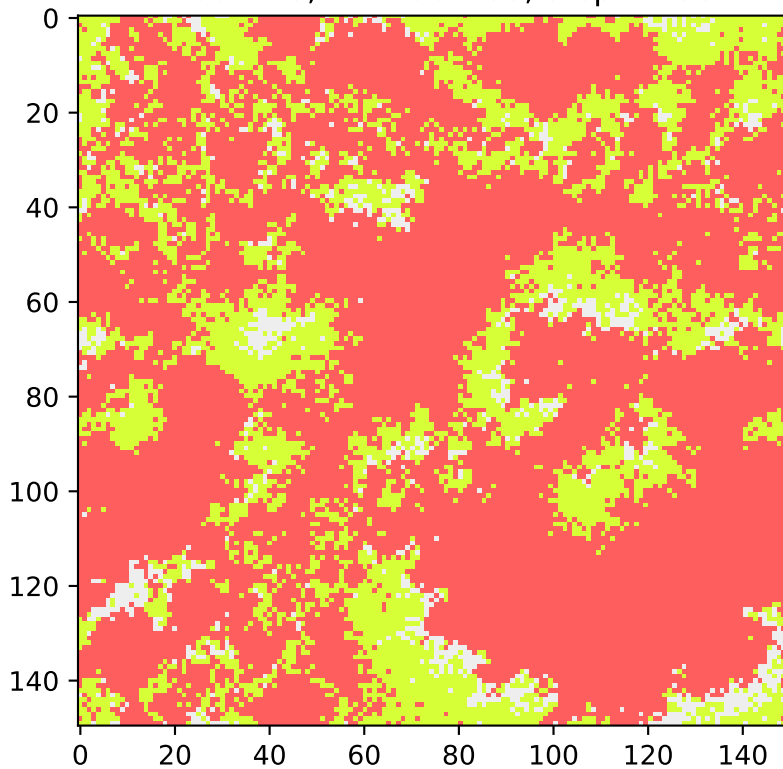
inf dur: 70, imm dur: 80, step: 2250.



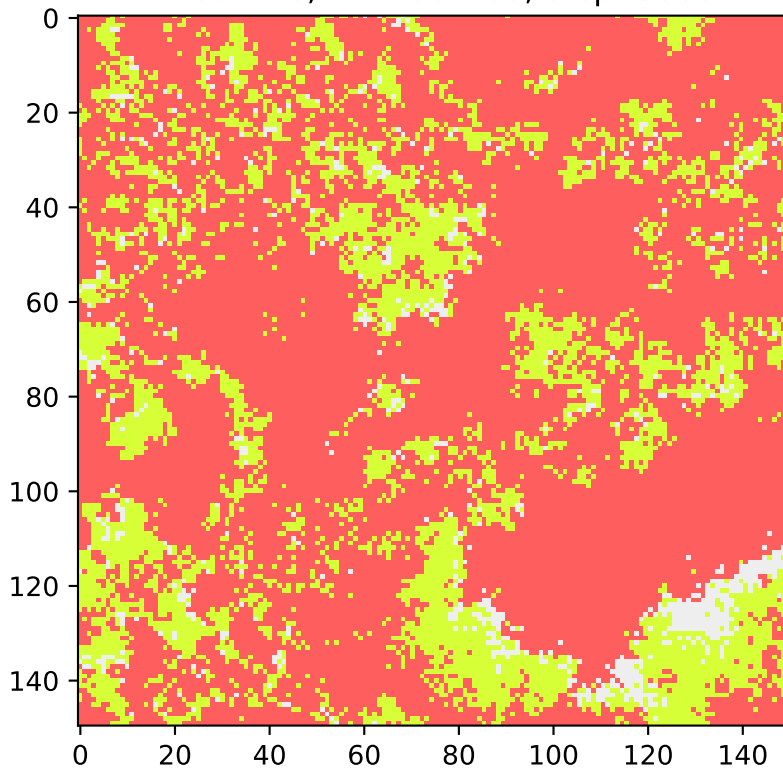
inf dur: 70, imm dur: 80, step: 2500.



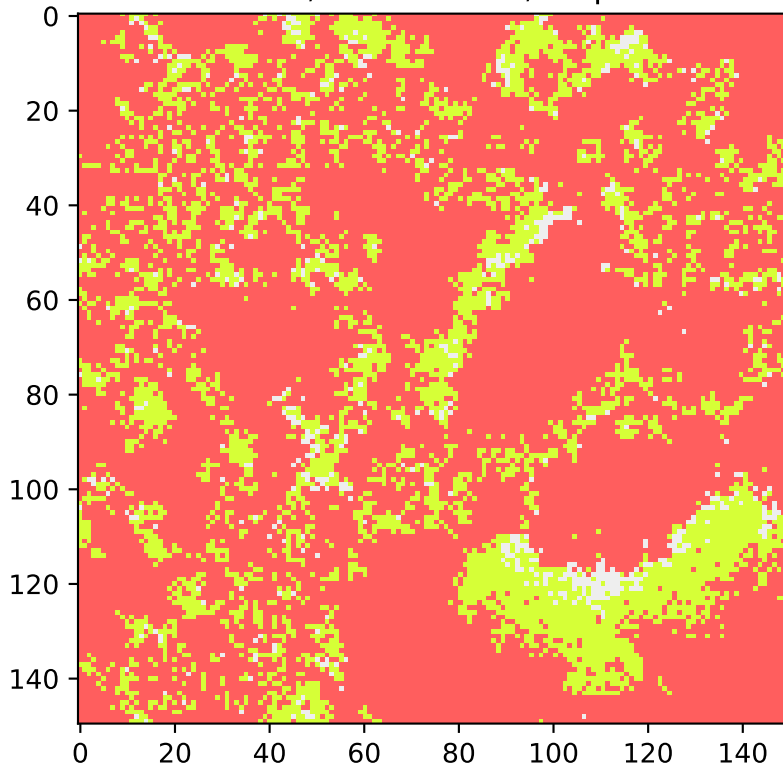
inf dur: 70, imm dur: 80, step: 2750.



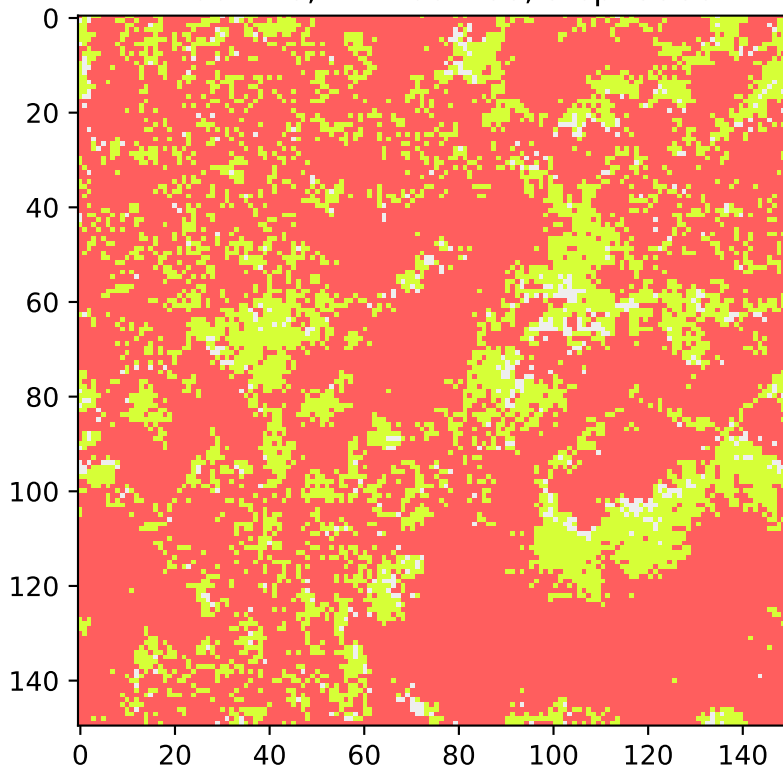
inf dur: 70, imm dur: 80, step: 3000.



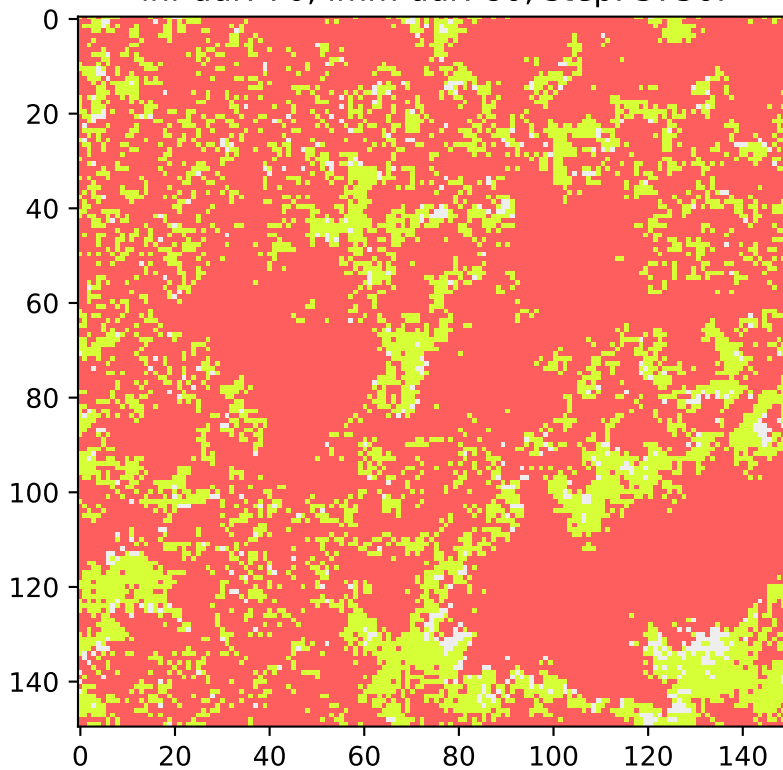
inf dur: 70, imm dur: 80, step: 3250.



inf dur: 70, imm dur: 80, step: 3500.

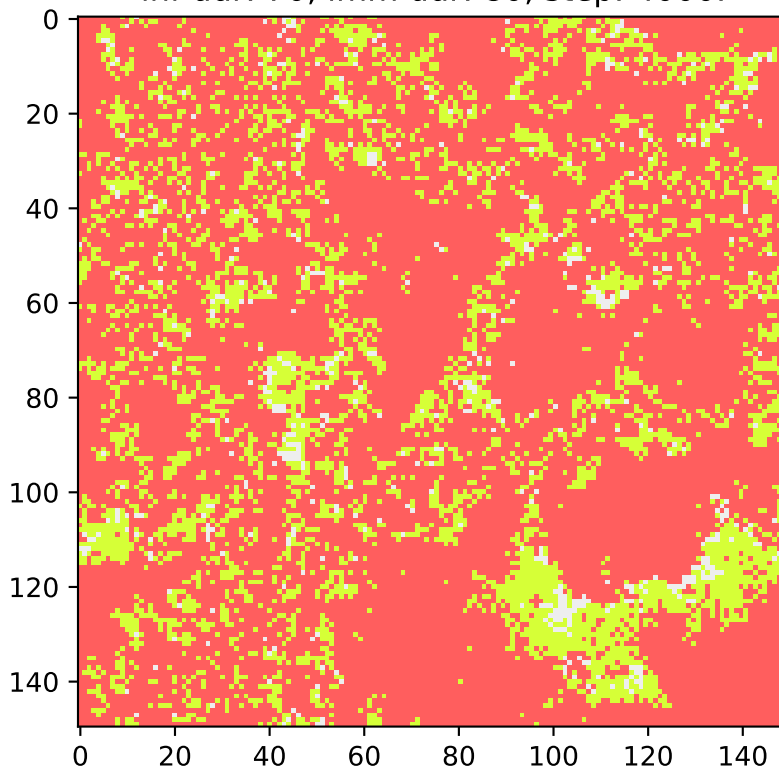


inf dur: 70, imm dur: 80, step: 3750.

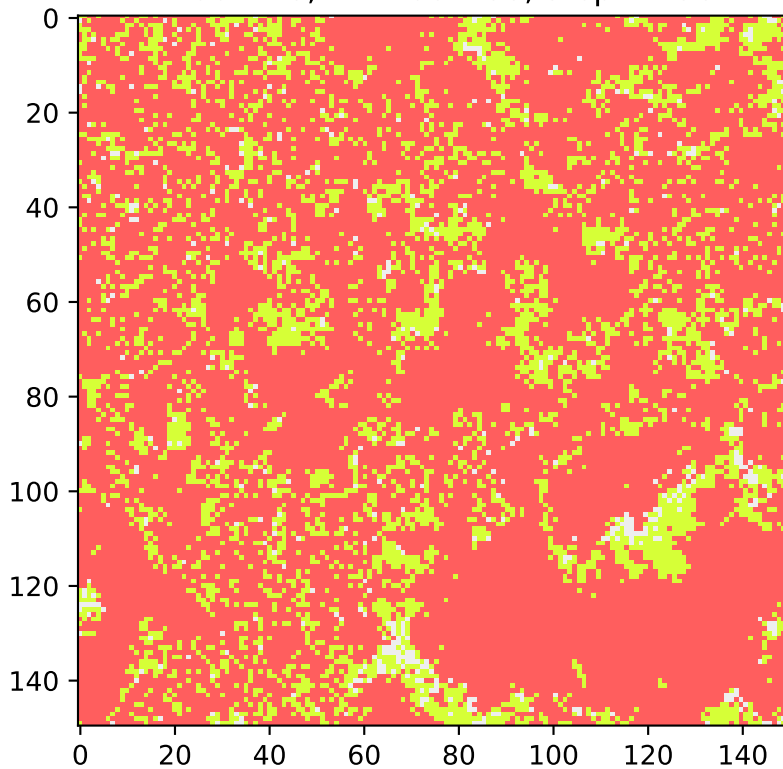




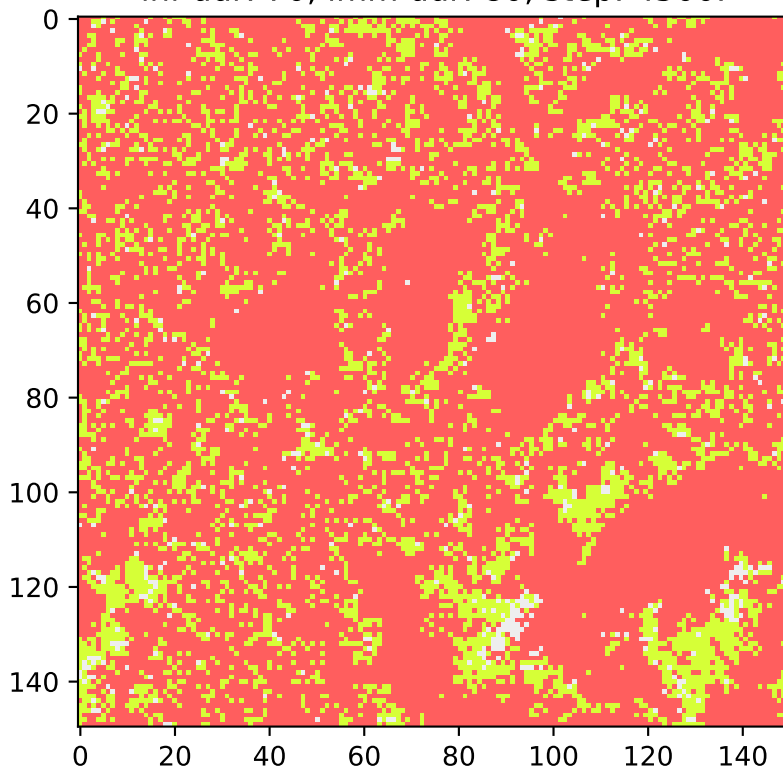
inf dur: 70, imm dur: 80, step: 4000.



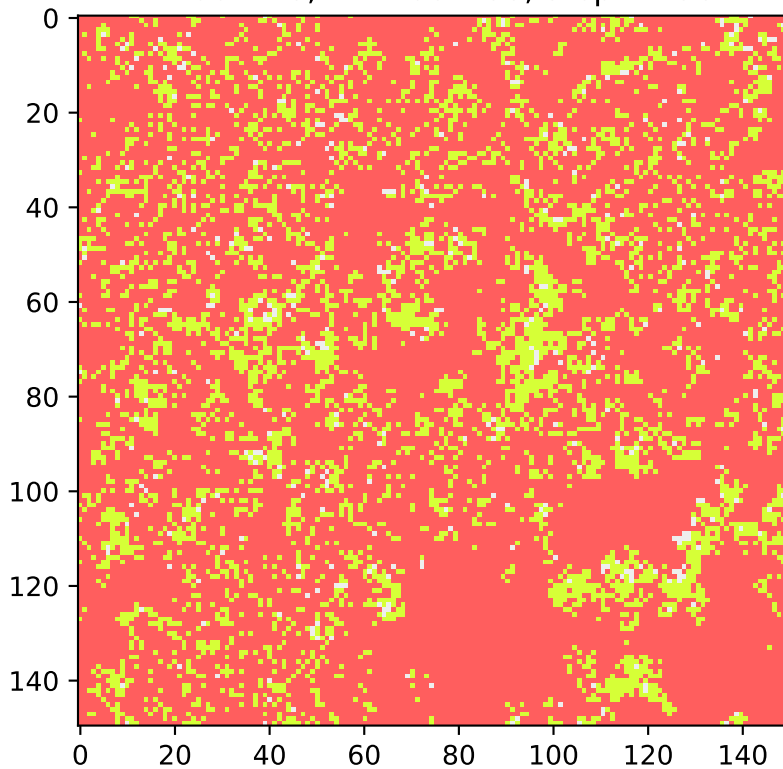
inf dur: 70, imm dur: 80, step: 4250.



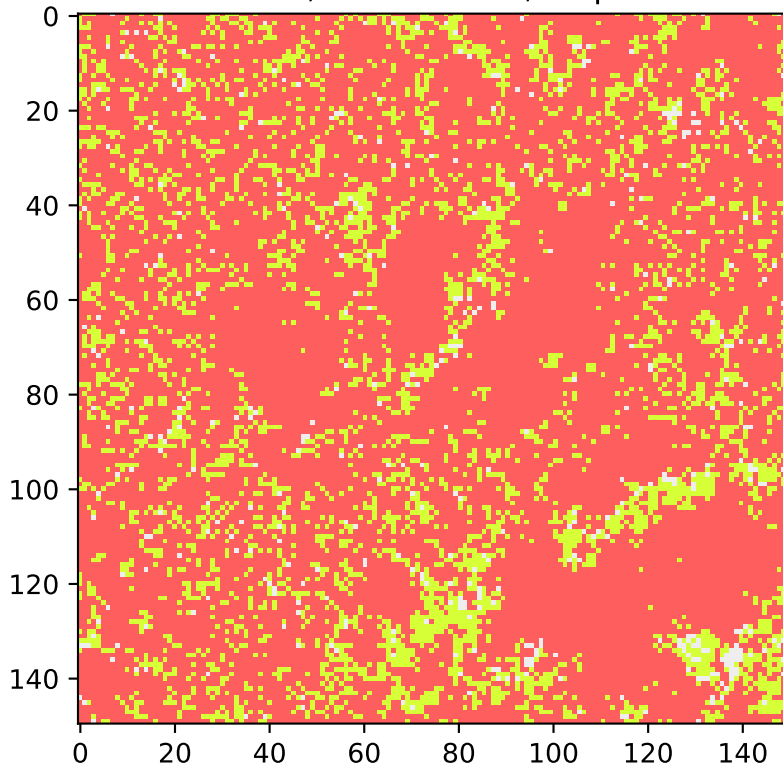
inf dur: 70, imm dur: 80, step: 4500.



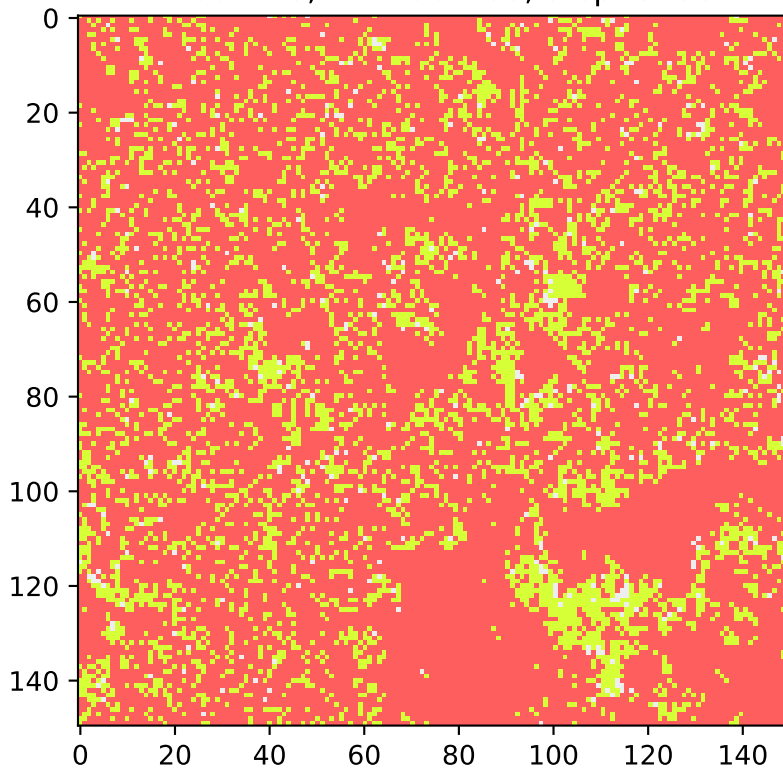
inf dur: 70, imm dur: 80, step: 4750.



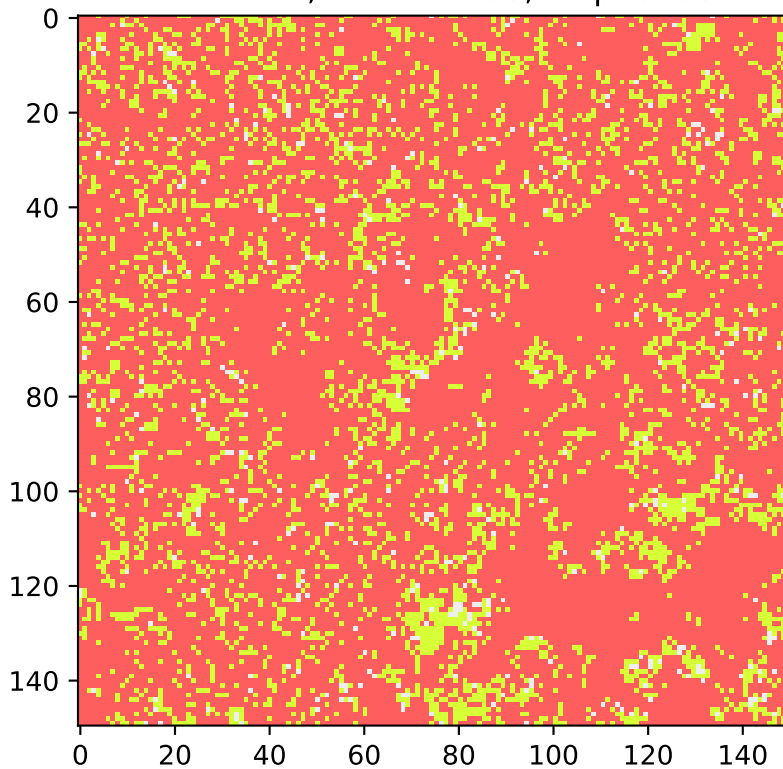
inf dur: 70, imm dur: 80, step: 5000.



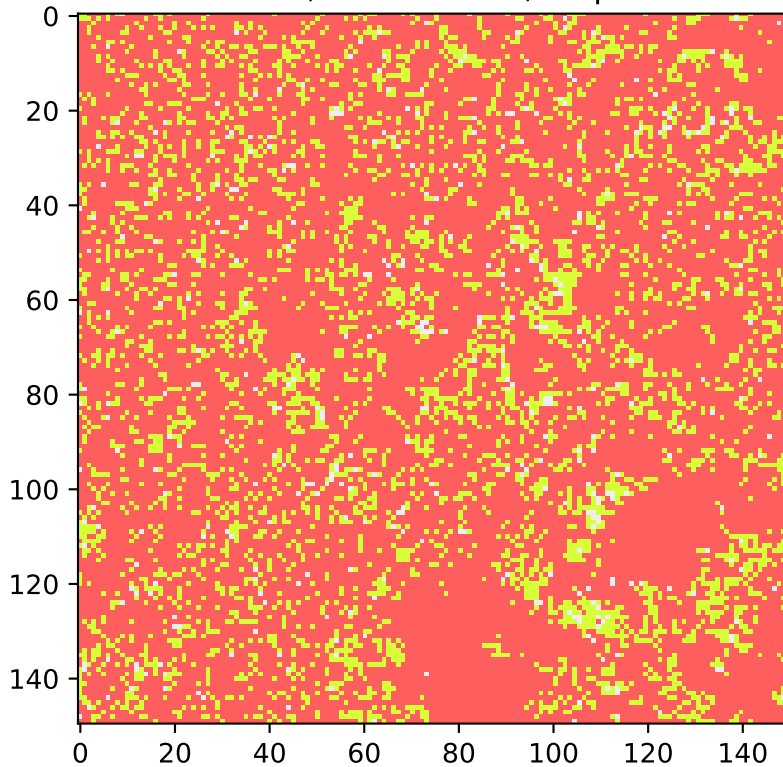
inf dur: 70, imm dur: 80, step: 5250.



inf dur: 70, imm dur: 80, step: 5500.

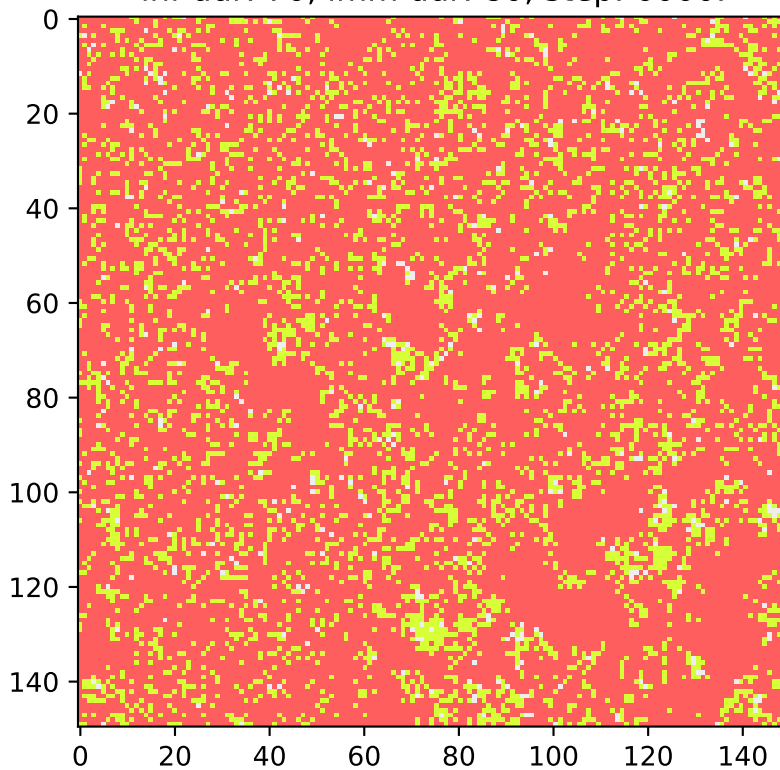


inf dur: 70, imm dur: 80, step: 5750.

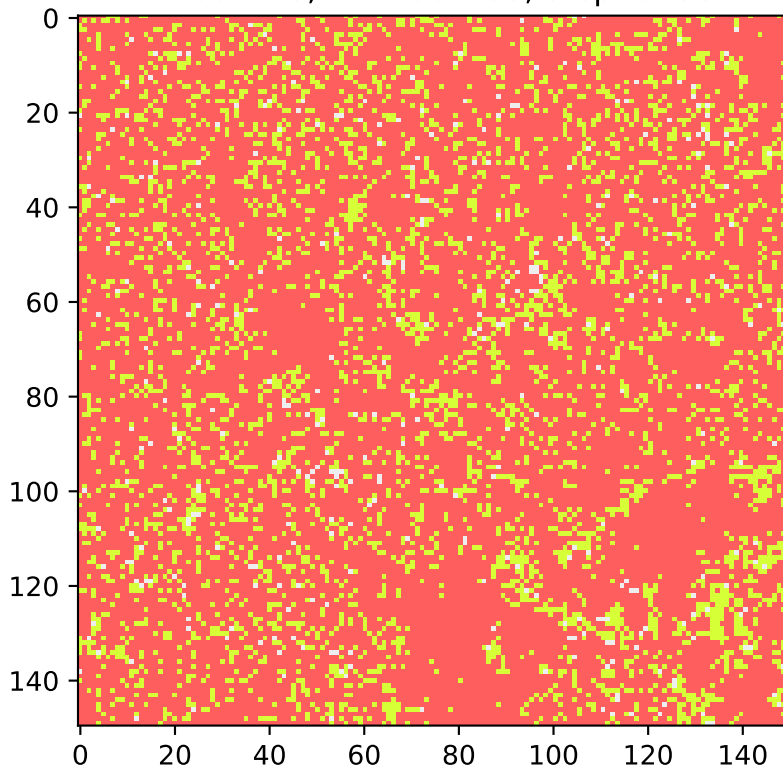




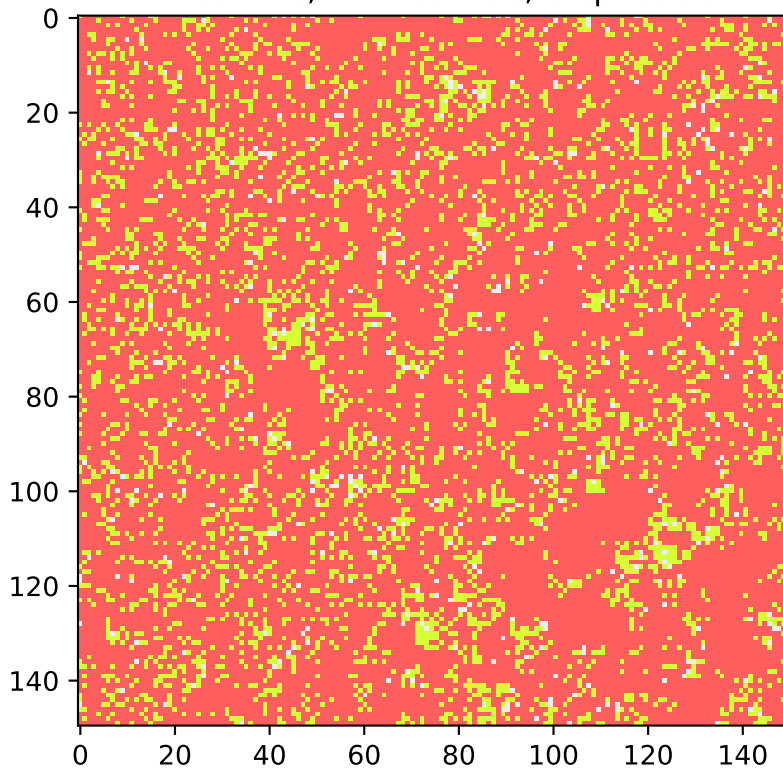
inf dur: 70, imm dur: 80, step: 6000.



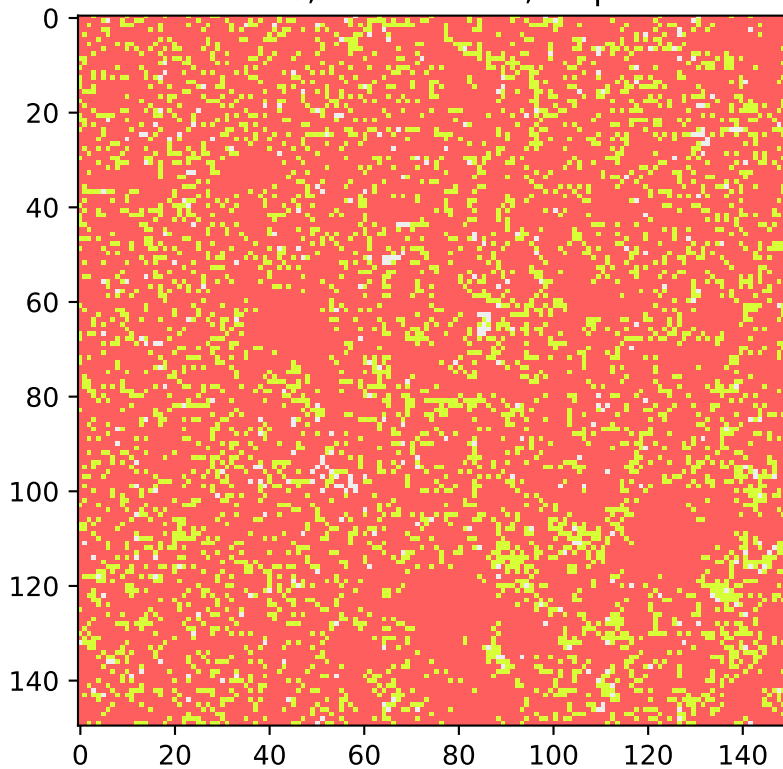
inf dur: 70, imm dur: 80, step: 6250.



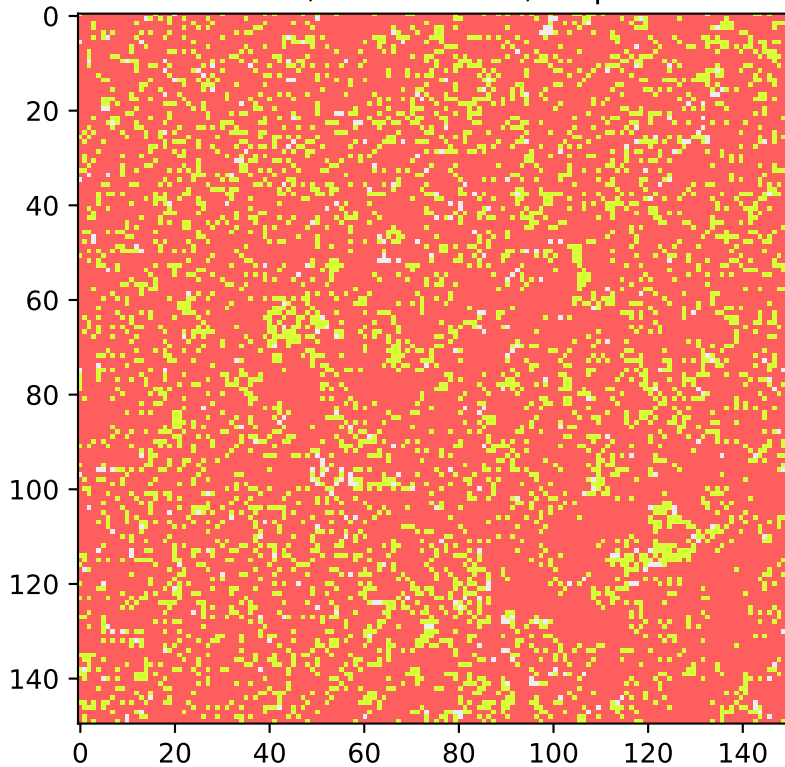
inf dur: 70, imm dur: 80, step: 6500.



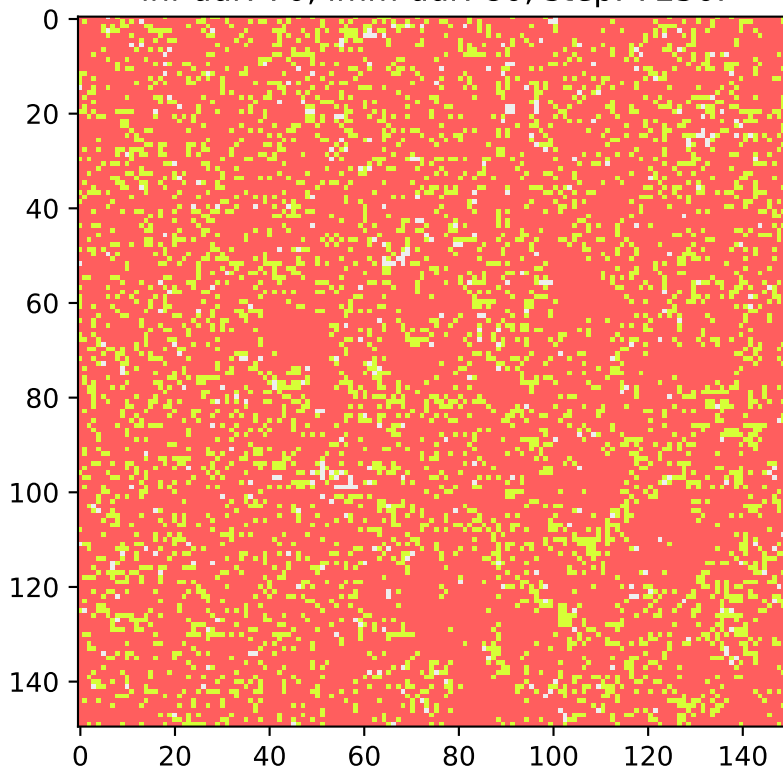
inf dur: 70, imm dur: 80, step: 6750.



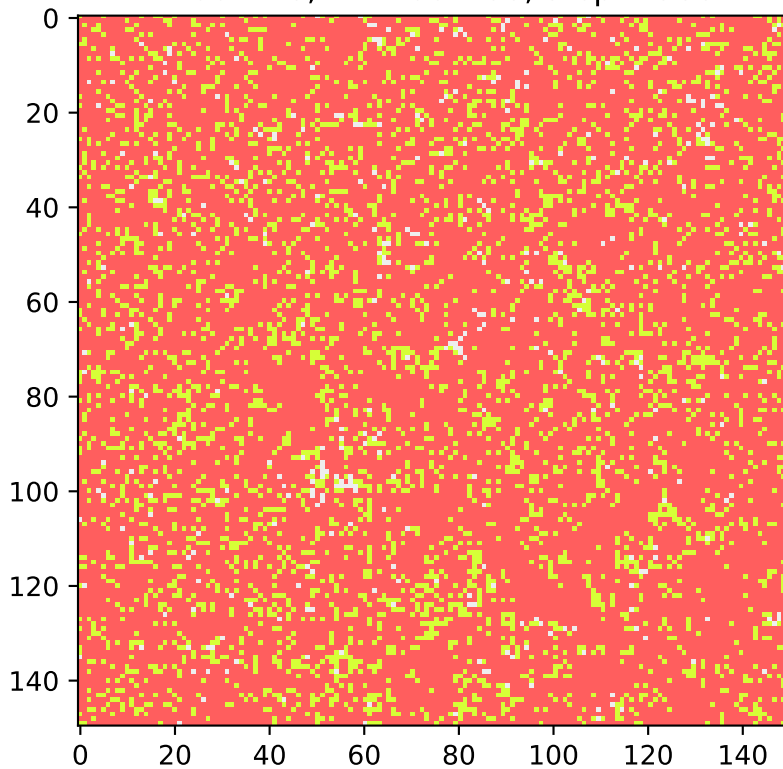
inf dur: 70, imm dur: 80, step: 7000.



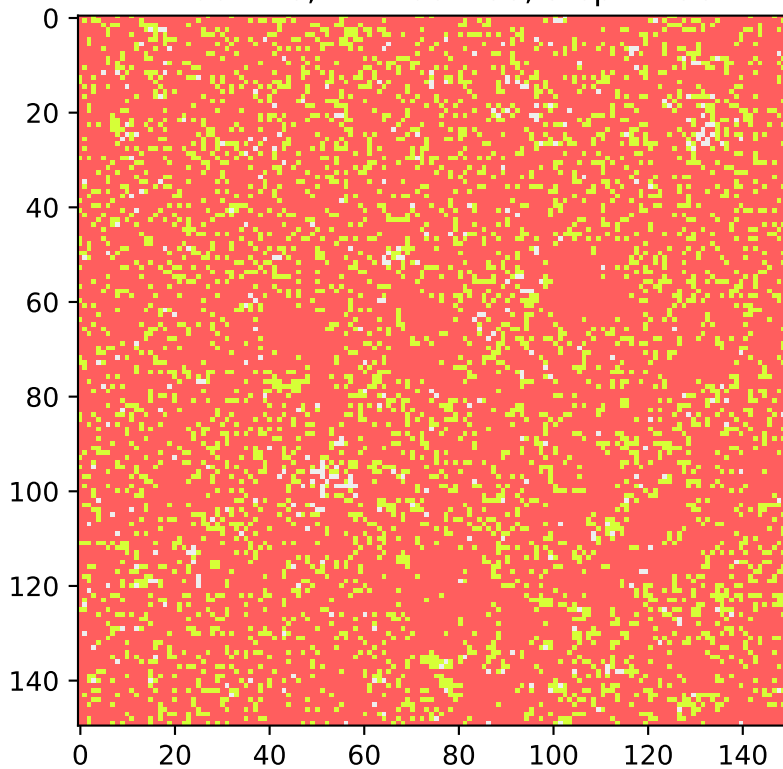
inf dur: 70, imm dur: 80, step: 7250.



inf dur: 70, imm dur: 80, step: 7500.

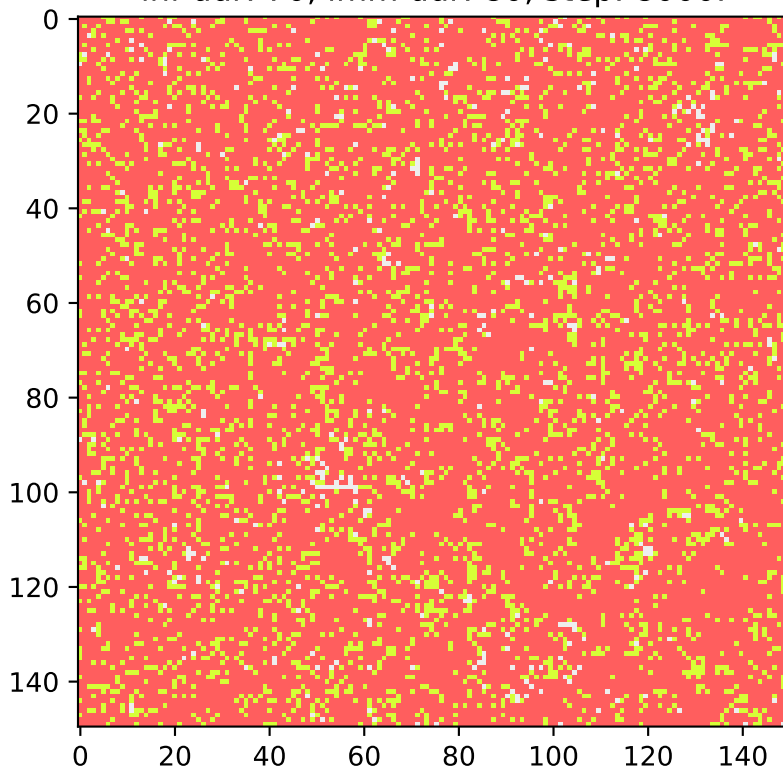


inf dur: 70, imm dur: 80, step: 7750.

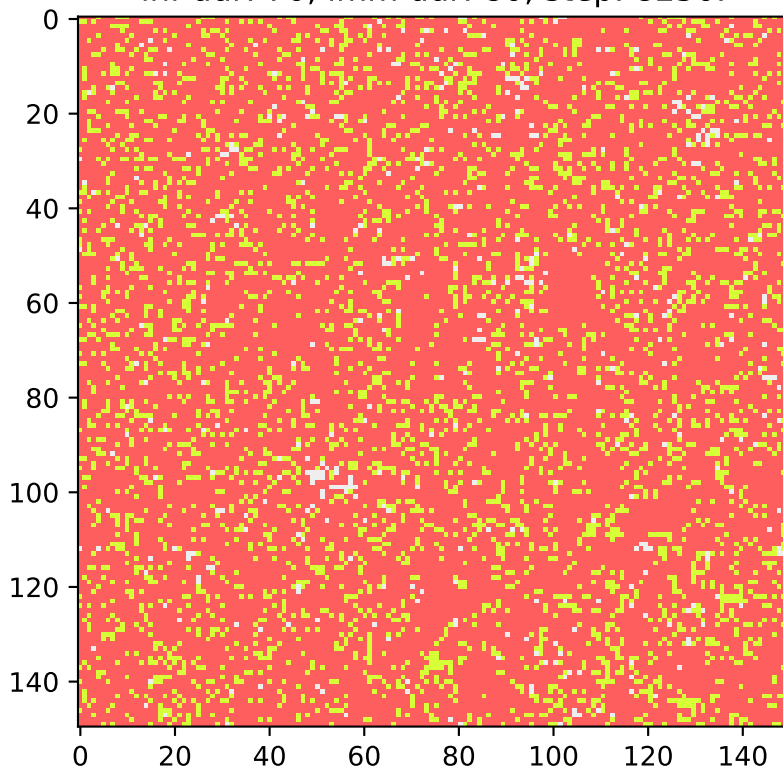




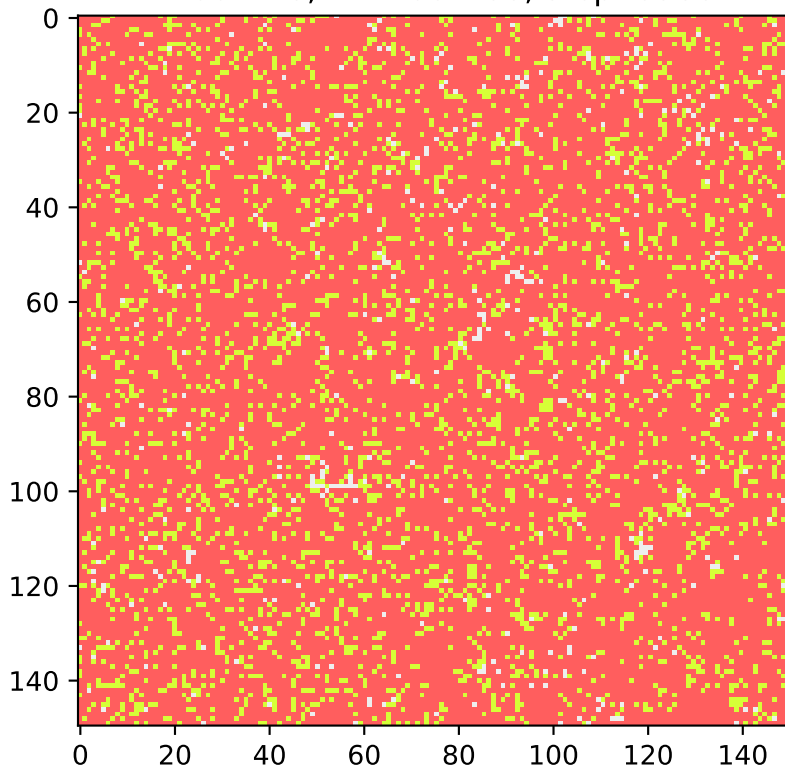
inf dur: 70, imm dur: 80, step: 8000.



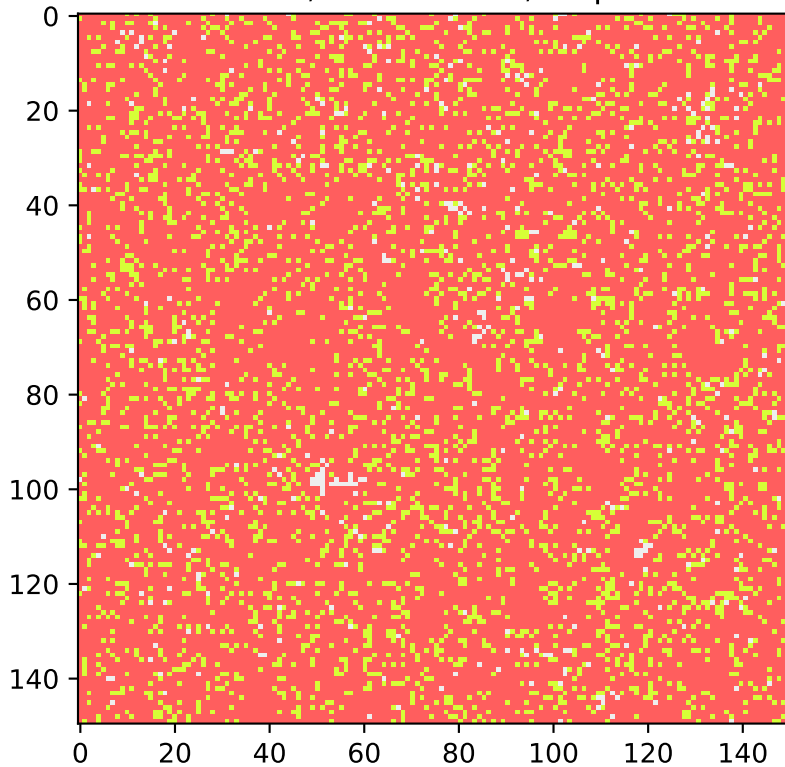
inf dur: 70, imm dur: 80, step: 8250.



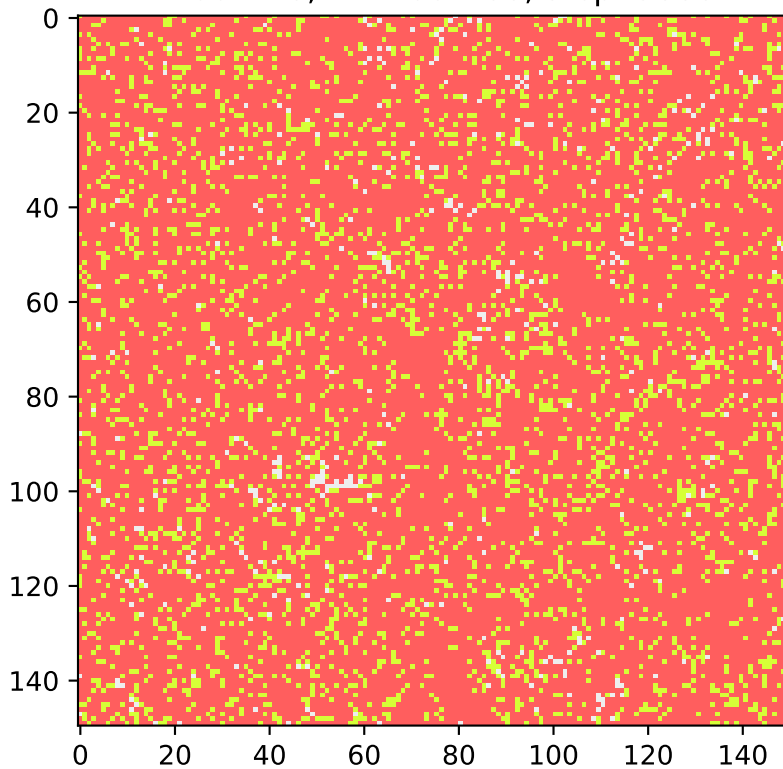
inf dur: 70, imm dur: 80, step: 8500.



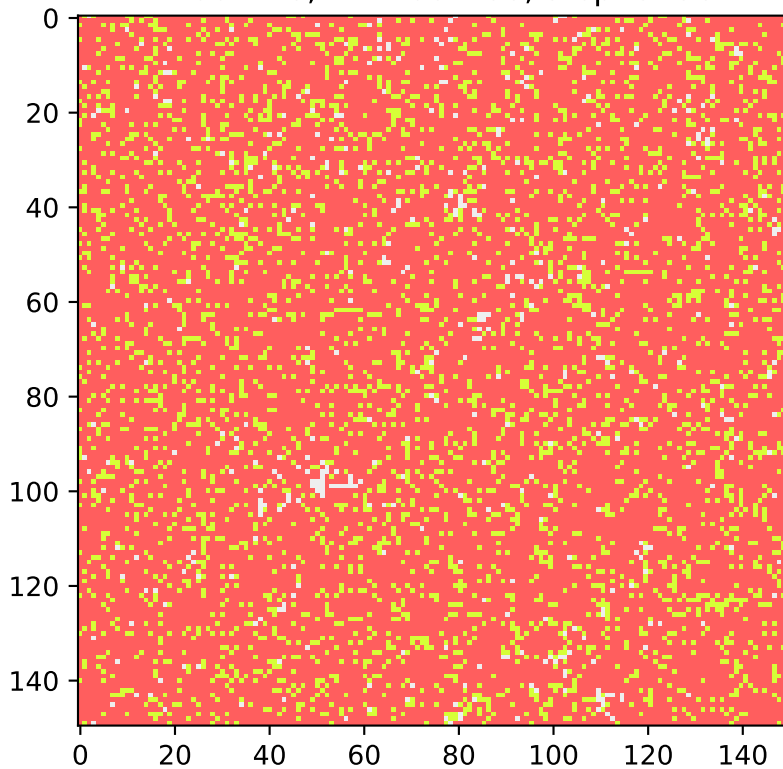
inf dur: 70, imm dur: 80, step: 8750.



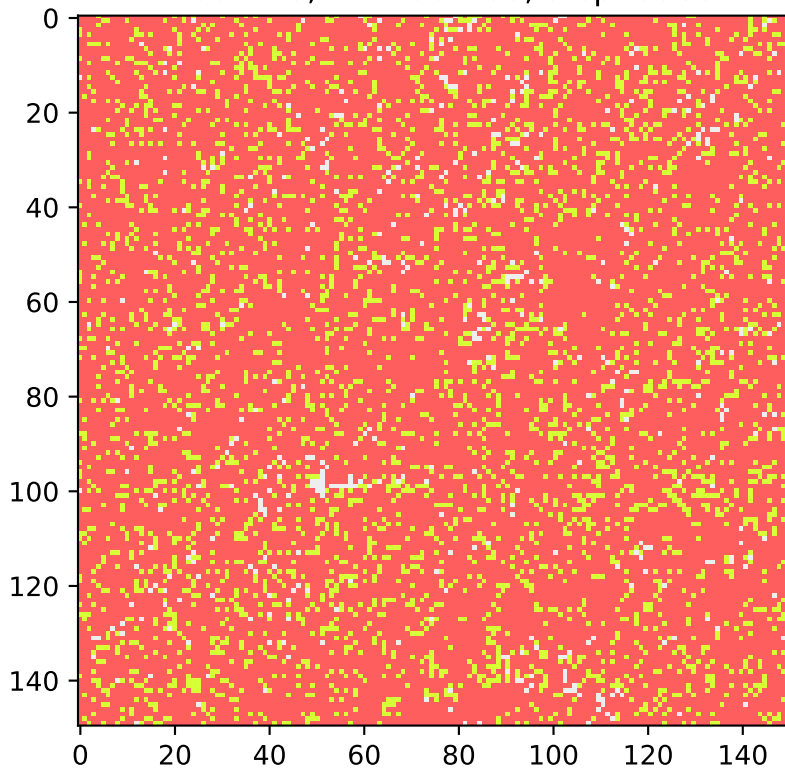
inf dur: 70, imm dur: 80, step: 9000.



inf dur: 70, imm dur: 80, step: 9250.



inf dur: 70, imm dur: 80, step: 9500.



inf dur: 70, imm dur: 80, step: 9750.

