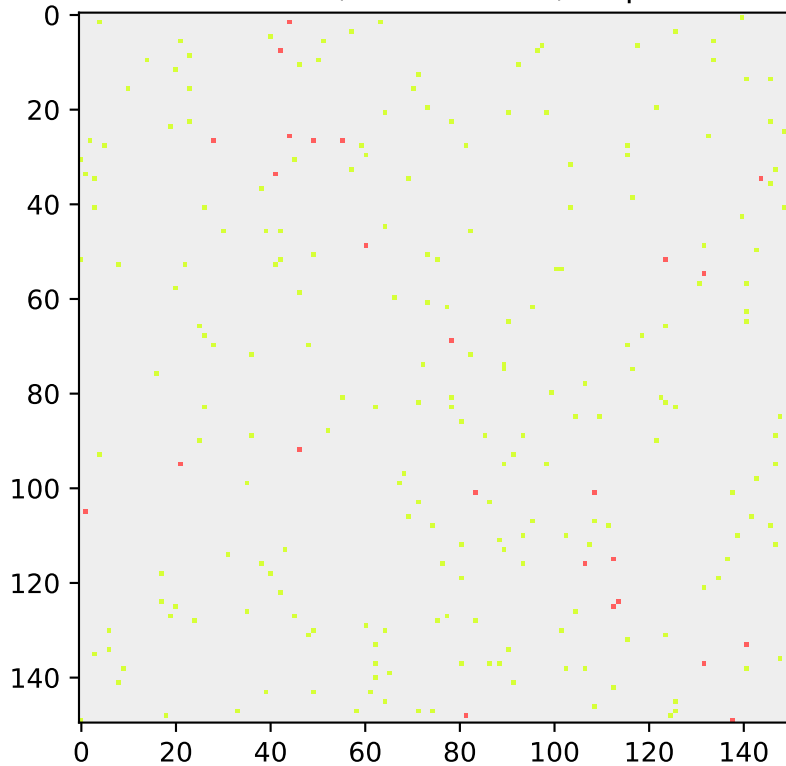
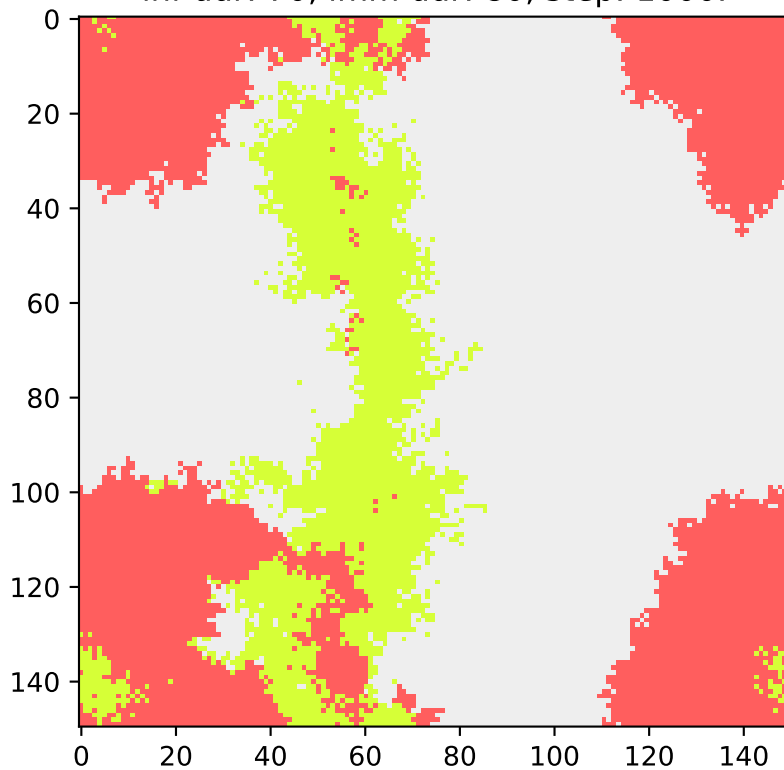


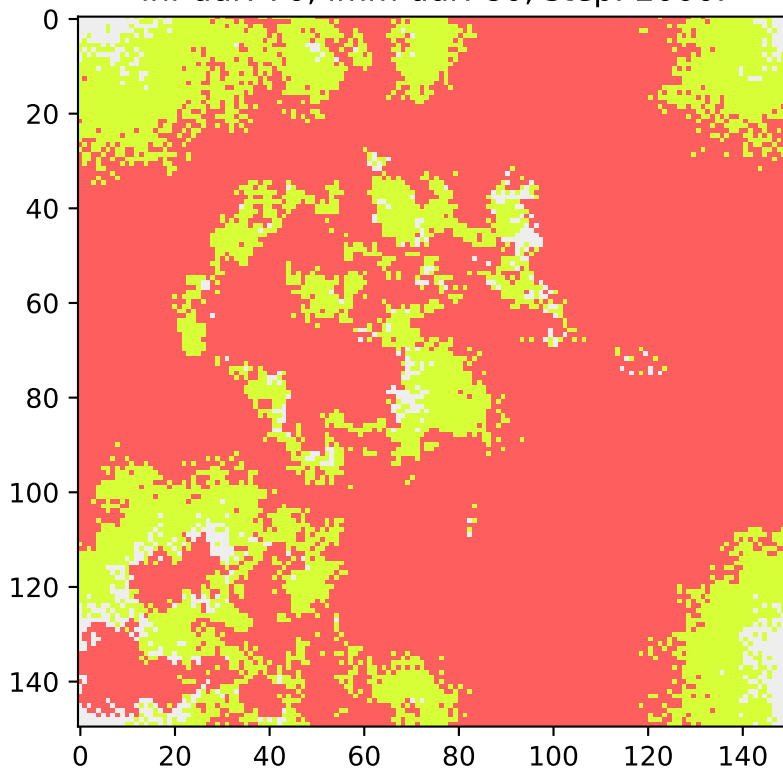
inf dur: 70, imm dur: 80, step: 0.



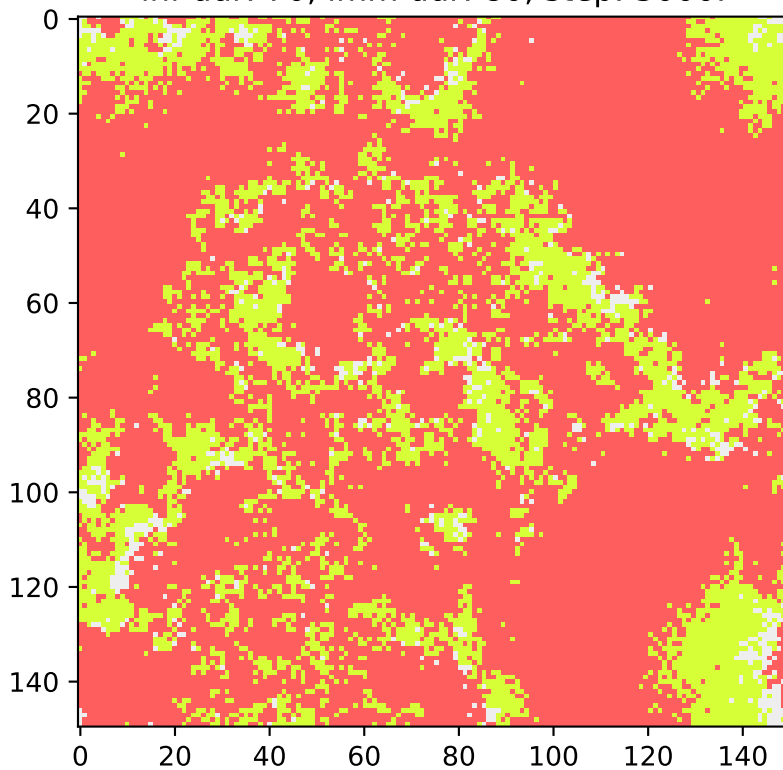
inf dur: 70, imm dur: 80, step: 1000.



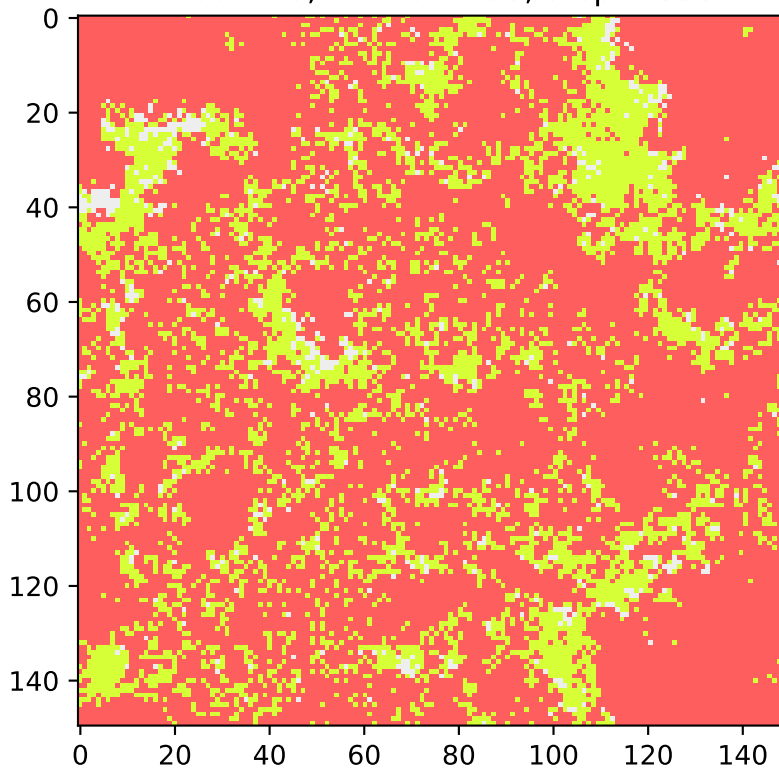
inf dur: 70, imm dur: 80, step: 2000.



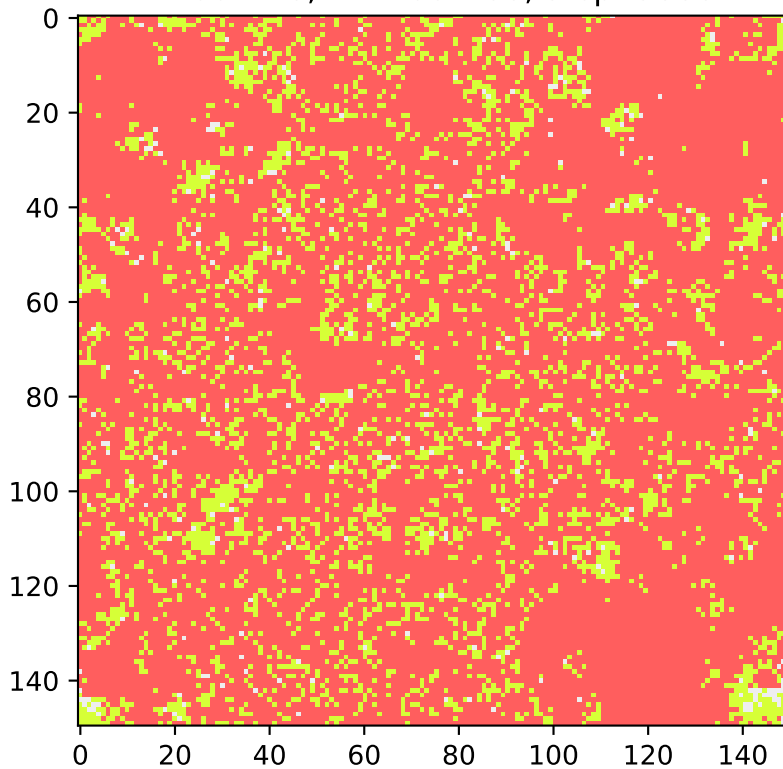
inf dur: 70, imm dur: 80, step: 3000.



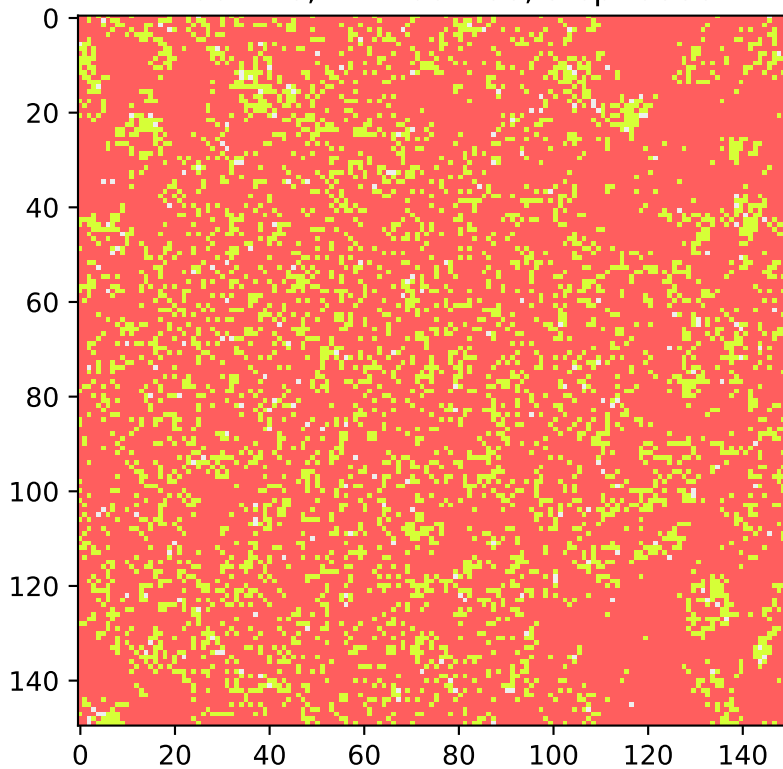
inf dur: 70, imm dur: 80, step: 4000.



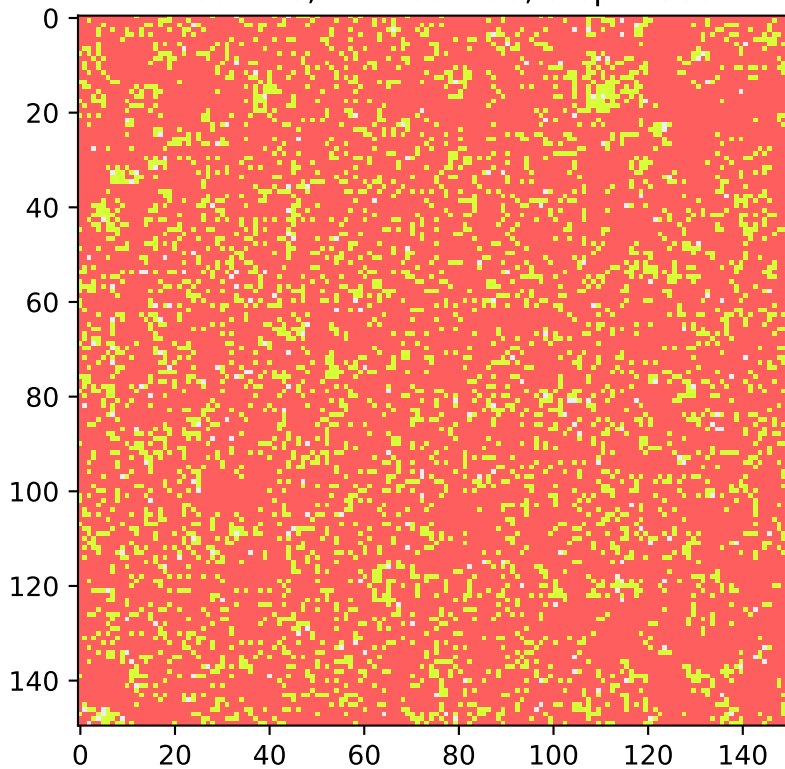
inf dur: 70, imm dur: 80, step: 5000.



inf dur: 70, imm dur: 80, step: 6000.

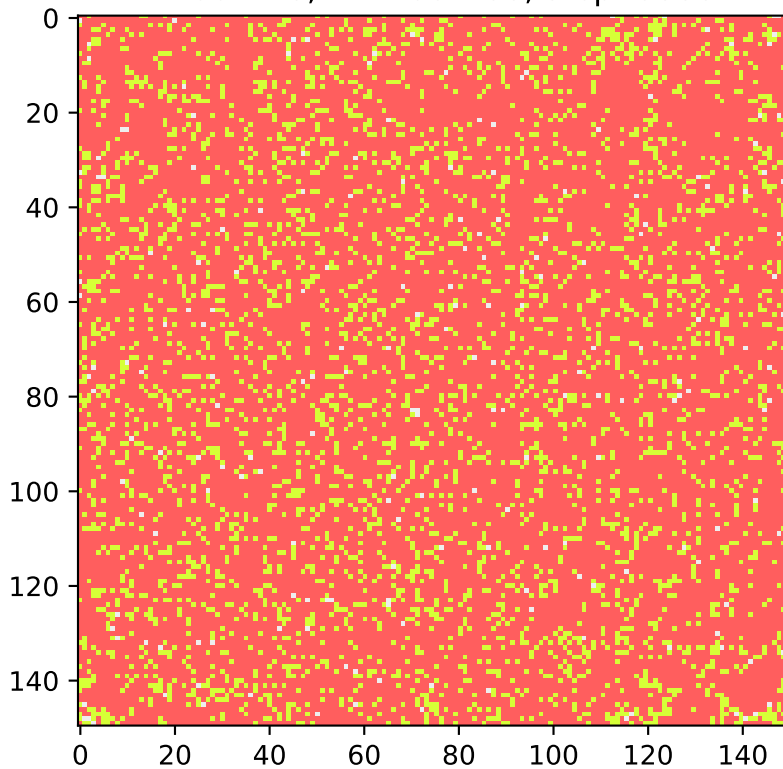


inf dur: 70, imm dur: 80, step: 7000.

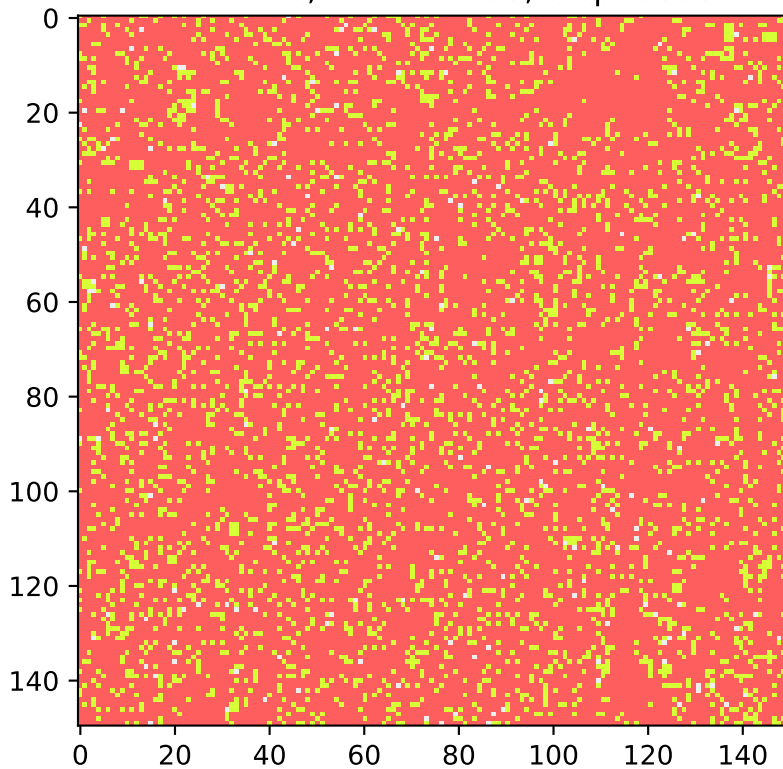




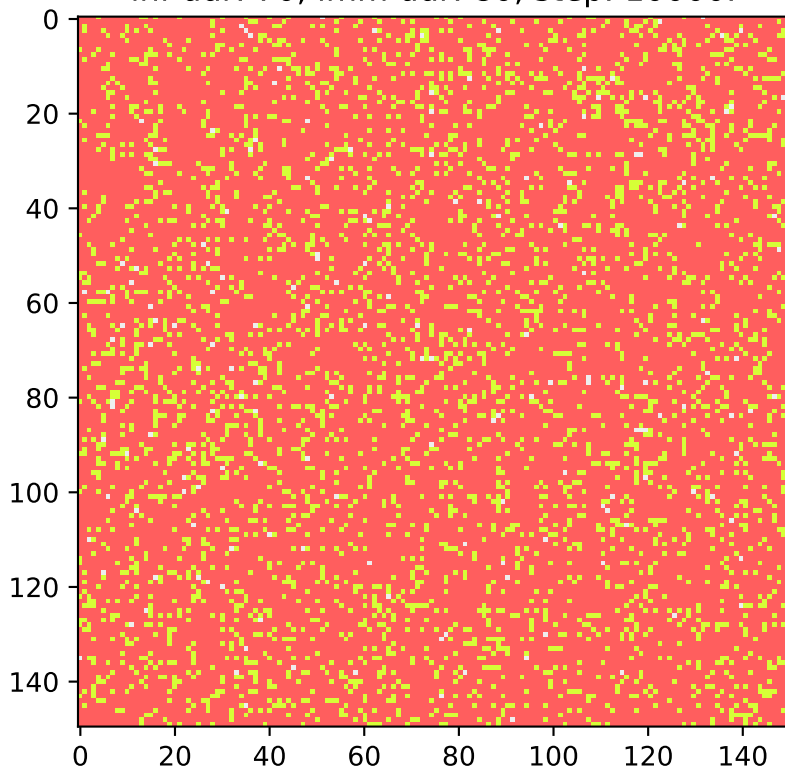
inf dur: 70, imm dur: 80, step: 8000.



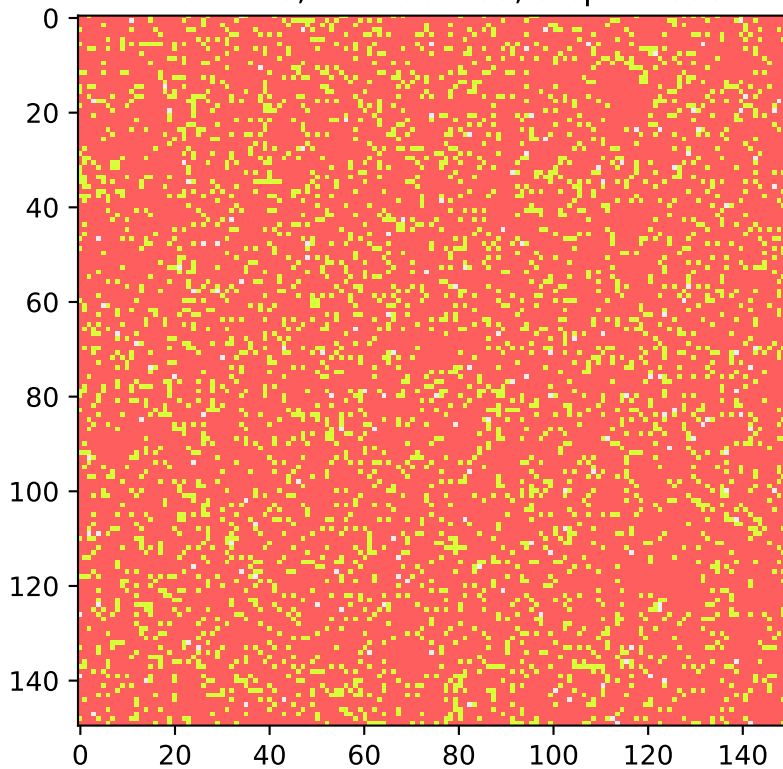
inf dur: 70, imm dur: 80, step: 9000.



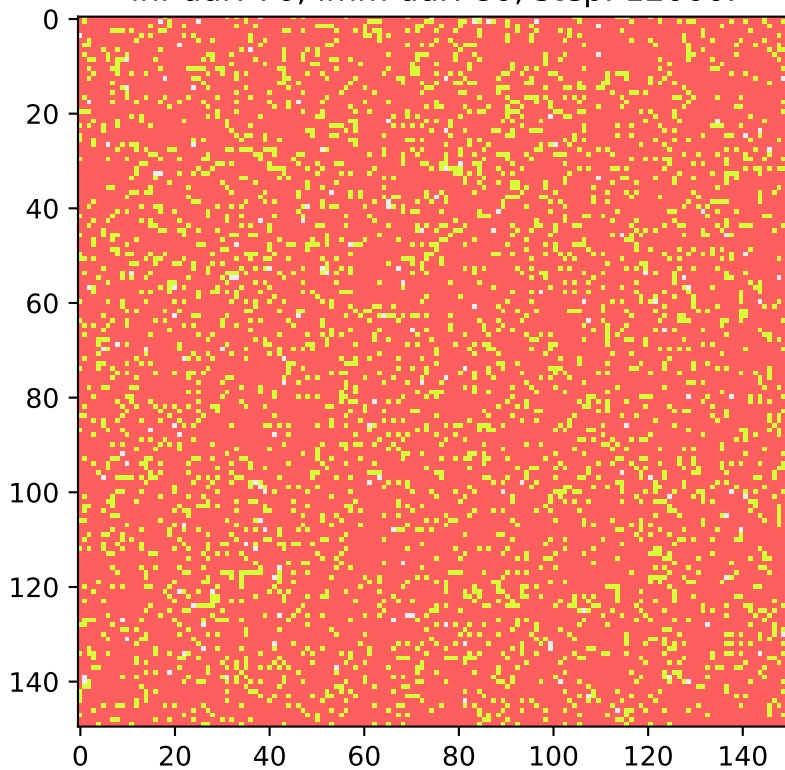
inf dur: 70, imm dur: 80, step: 10000.



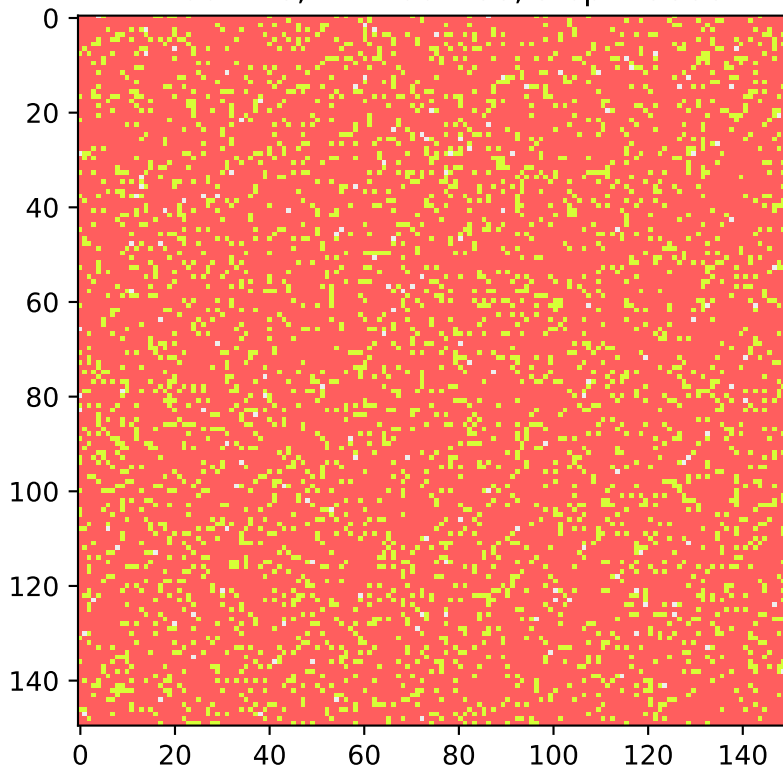
inf dur: 70, imm dur: 80, step: 11000.



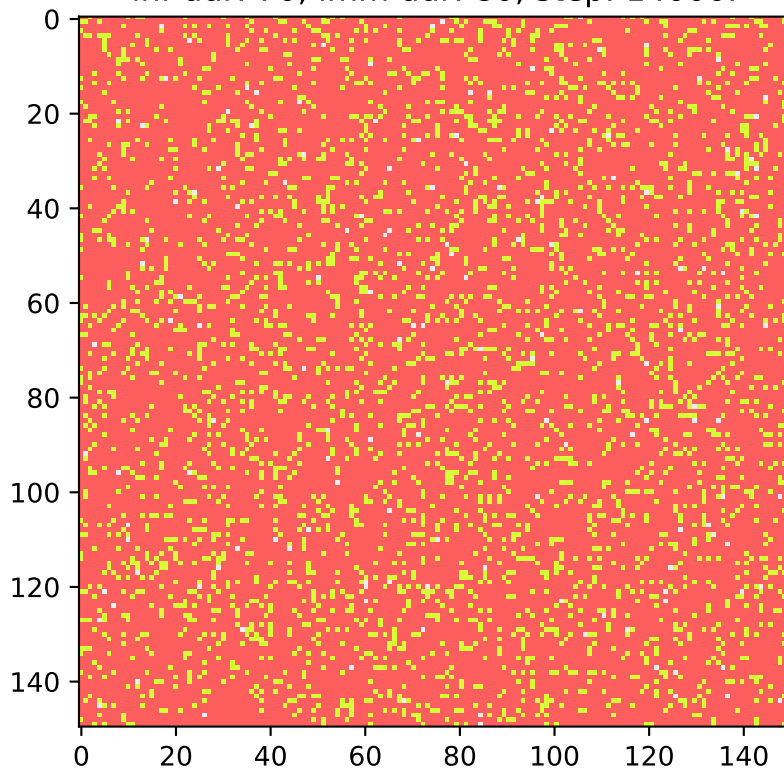
inf dur: 70, imm dur: 80, step: 12000.



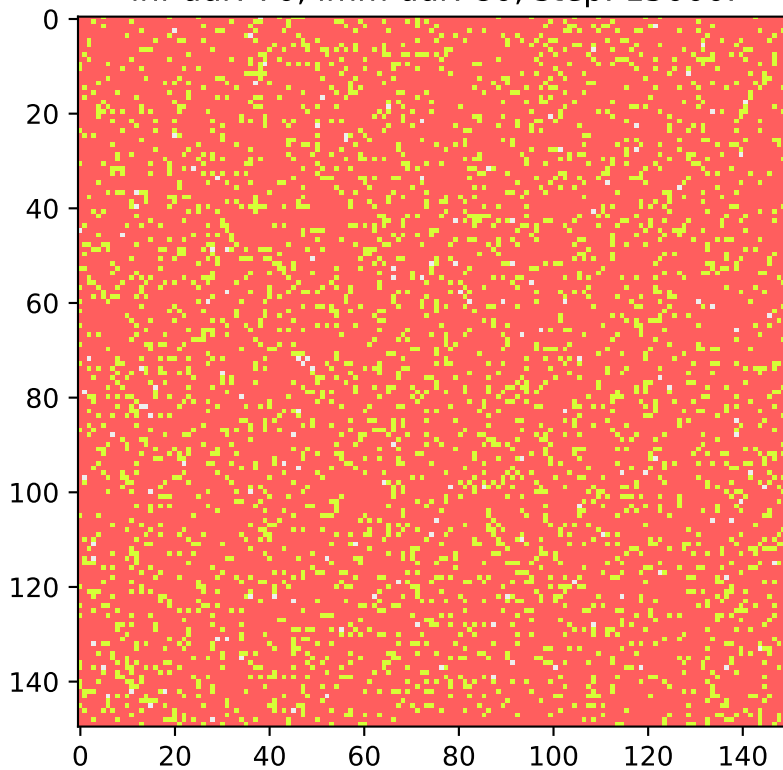
inf dur: 70, imm dur: 80, step: 13000.



inf dur: 70, imm dur: 80, step: 14000.

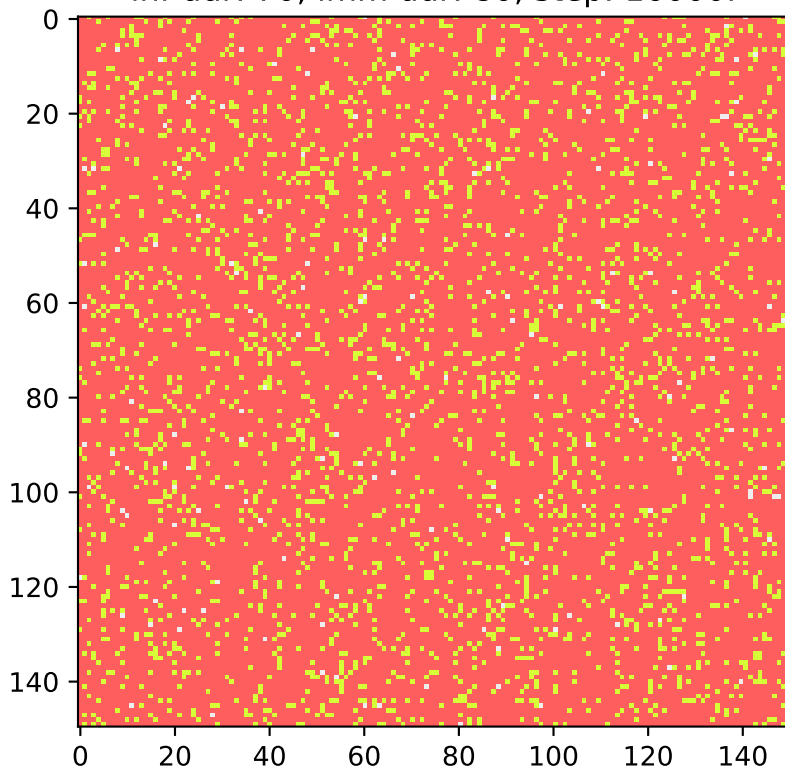


inf dur: 70, imm dur: 80, step: 15000.

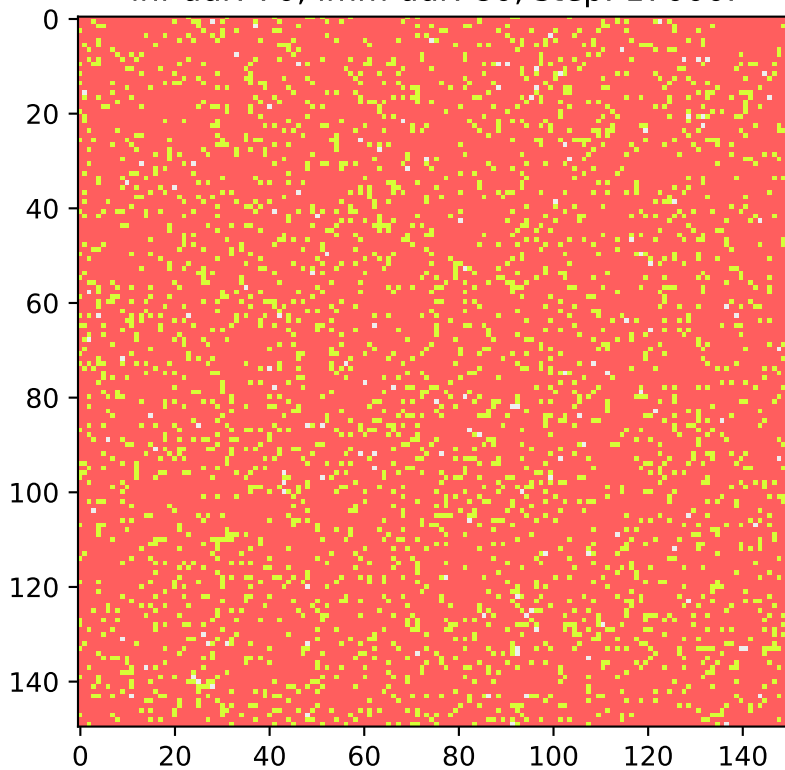




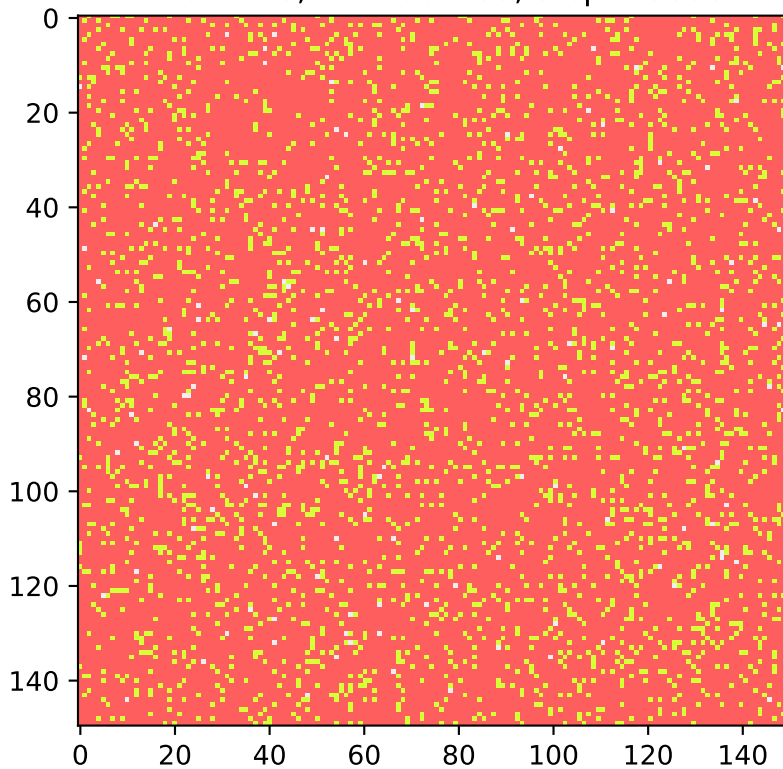
inf dur: 70, imm dur: 80, step: 16000.



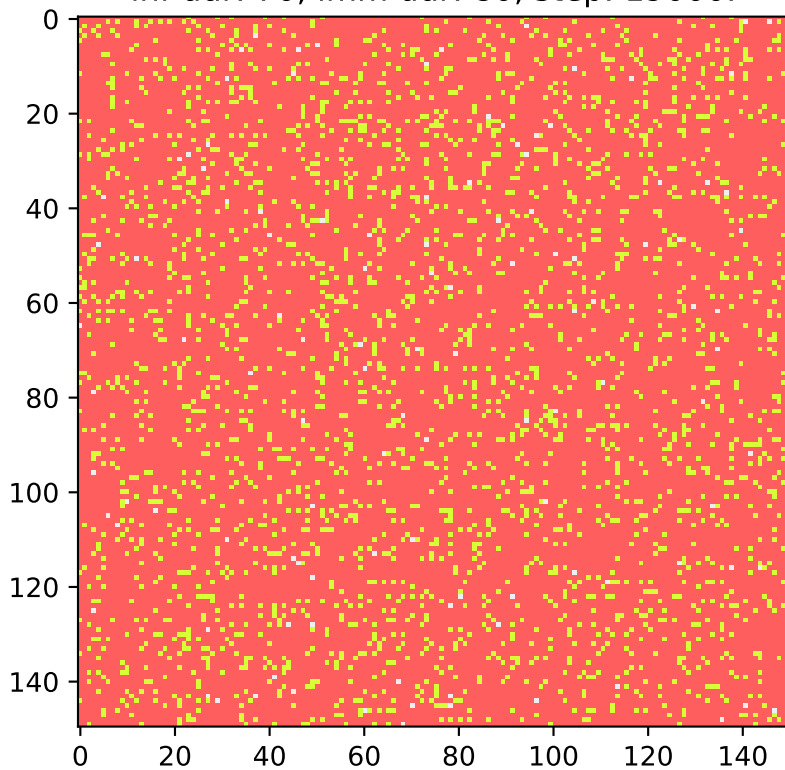
inf dur: 70, imm dur: 80, step: 17000.



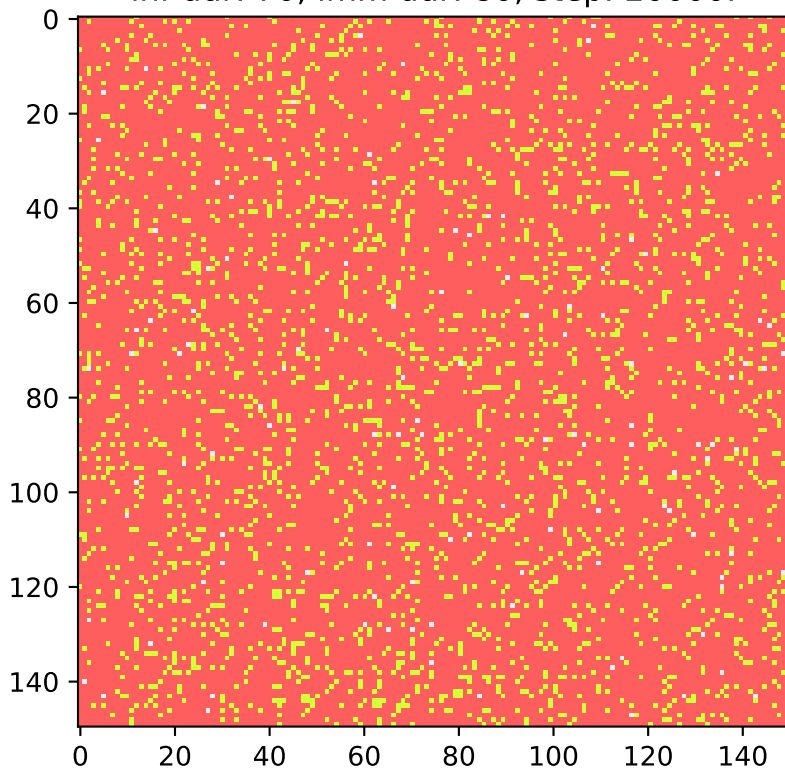
inf dur: 70, imm dur: 80, step: 18000.



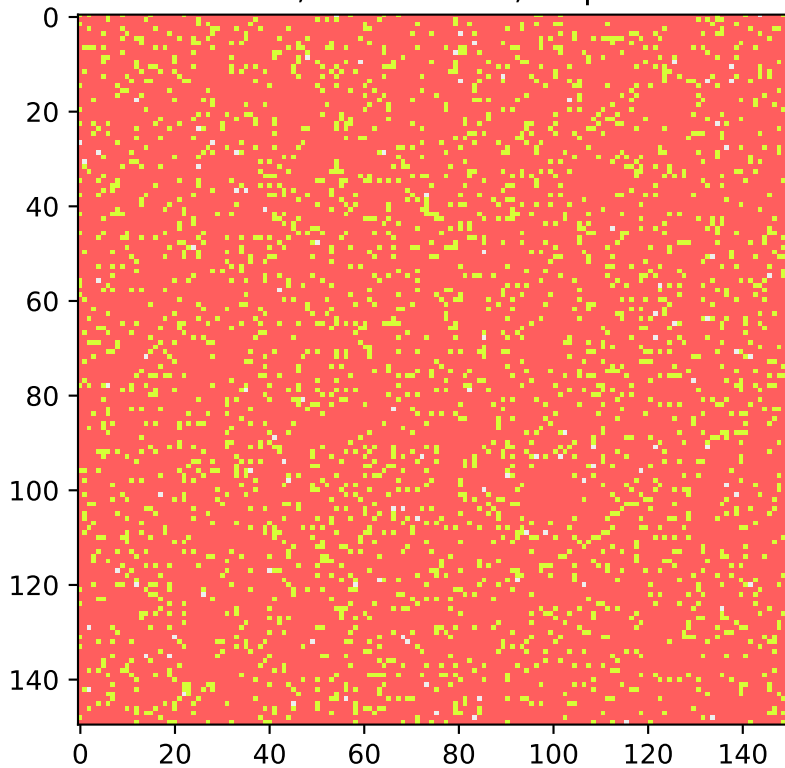
inf dur: 70, imm dur: 80, step: 19000.



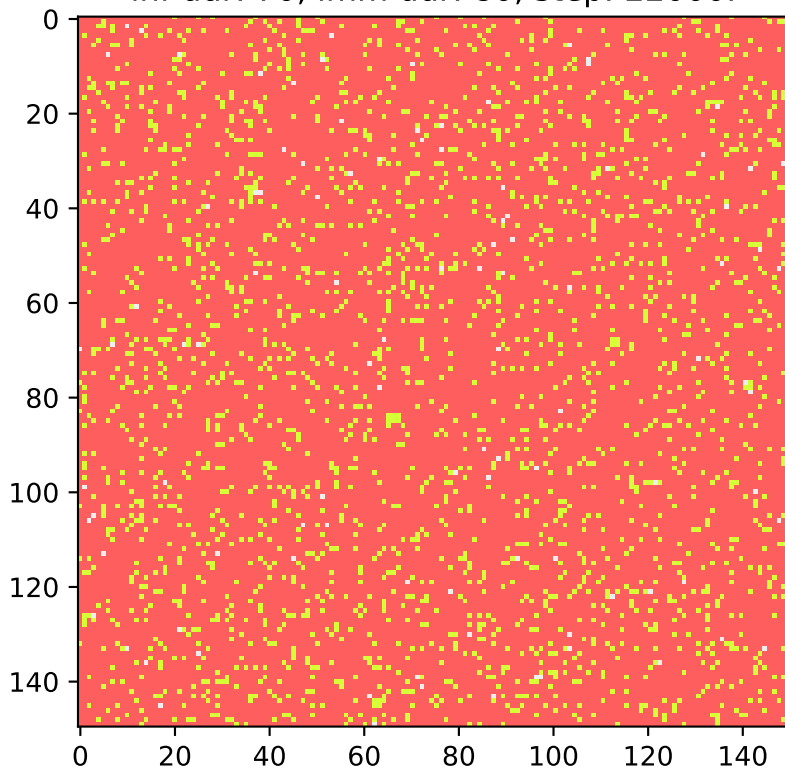
inf dur: 70, imm dur: 80, step: 20000.



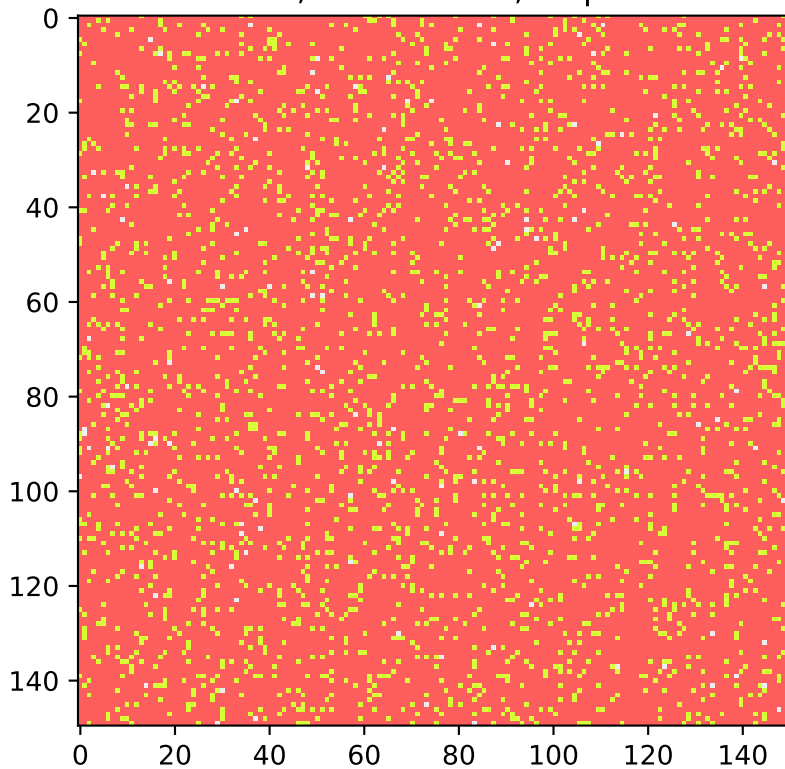
inf dur: 70, imm dur: 80, step: 21000.



inf dur: 70, imm dur: 80, step: 22000.

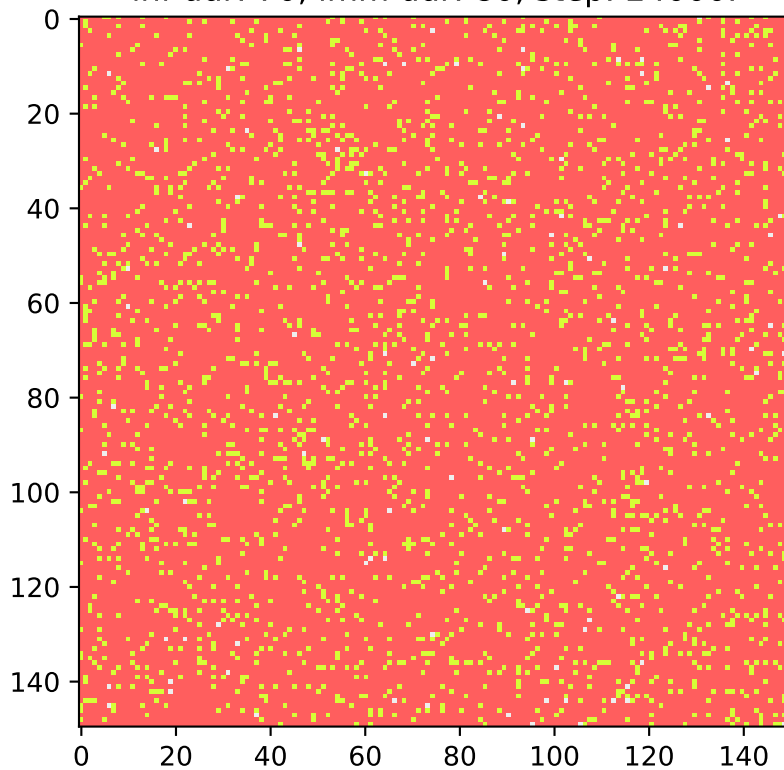


inf dur: 70, imm dur: 80, step: 23000.

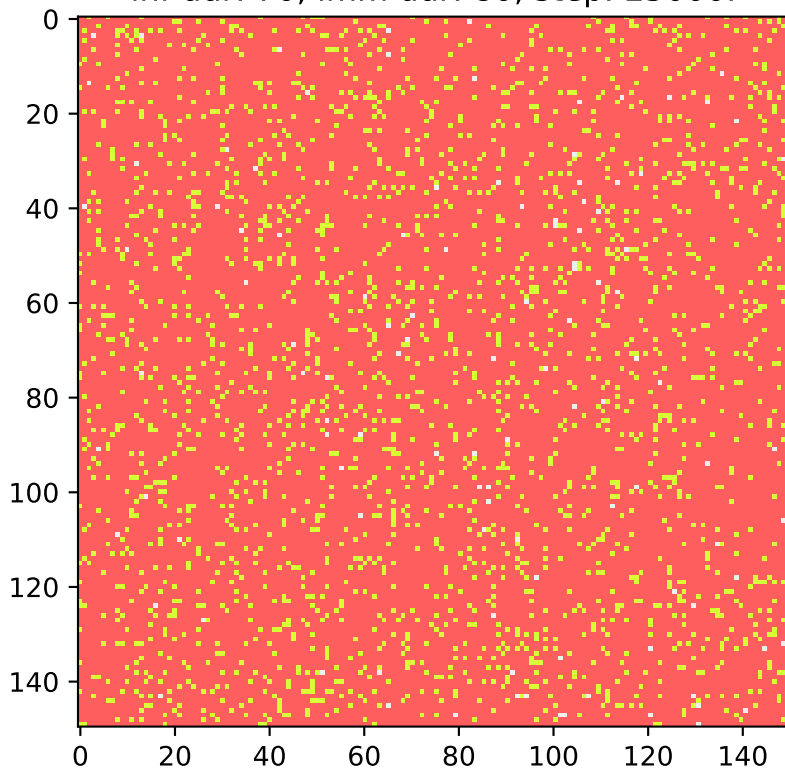




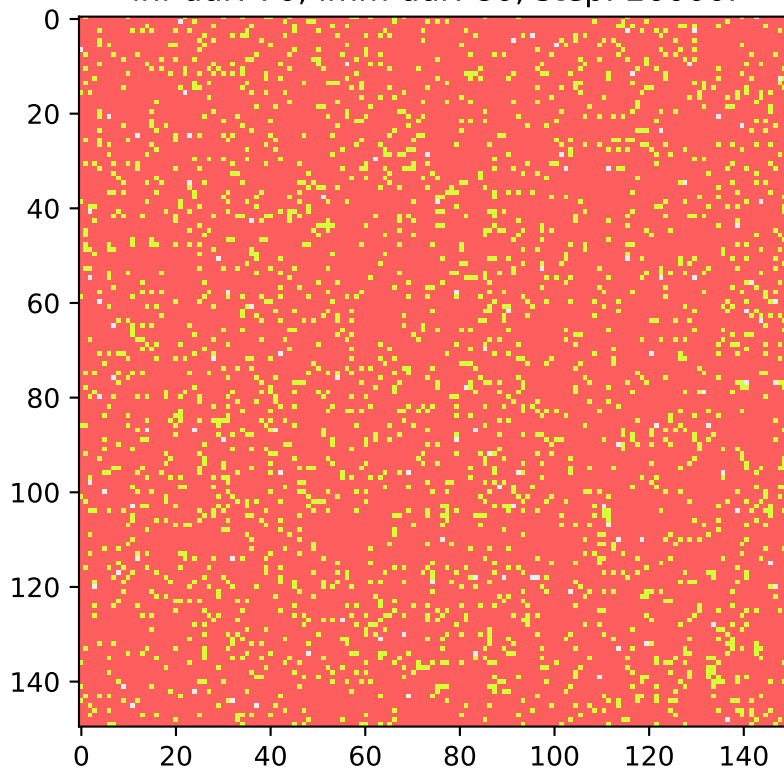
inf dur: 70, imm dur: 80, step: 24000.



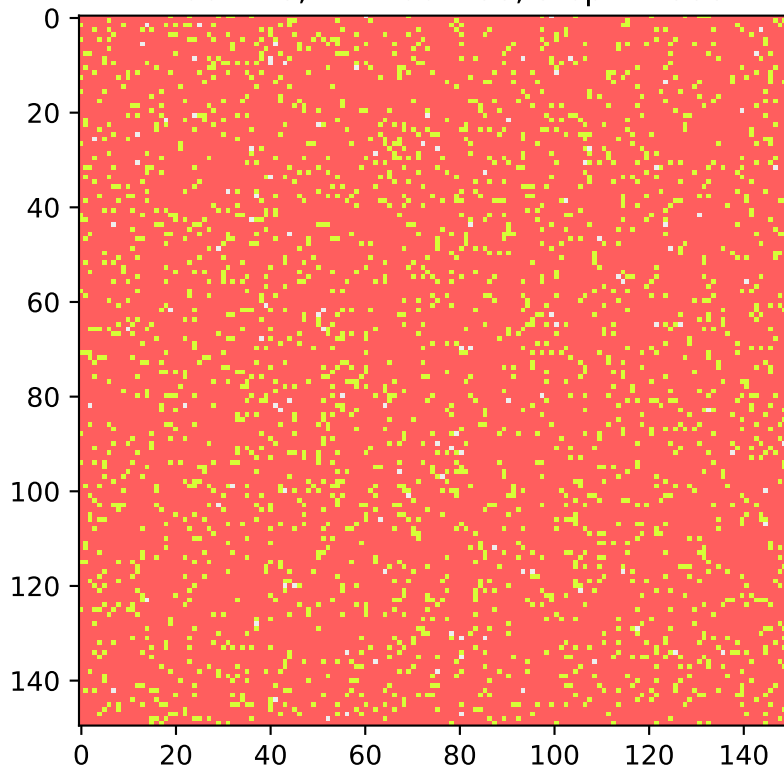
inf dur: 70, imm dur: 80, step: 25000.



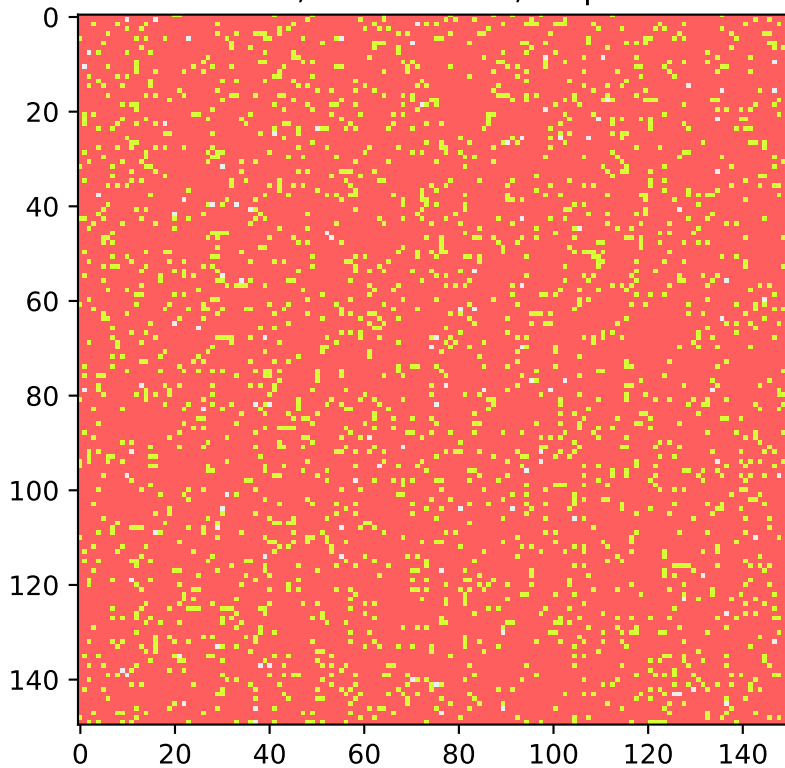
inf dur: 70, imm dur: 80, step: 26000.



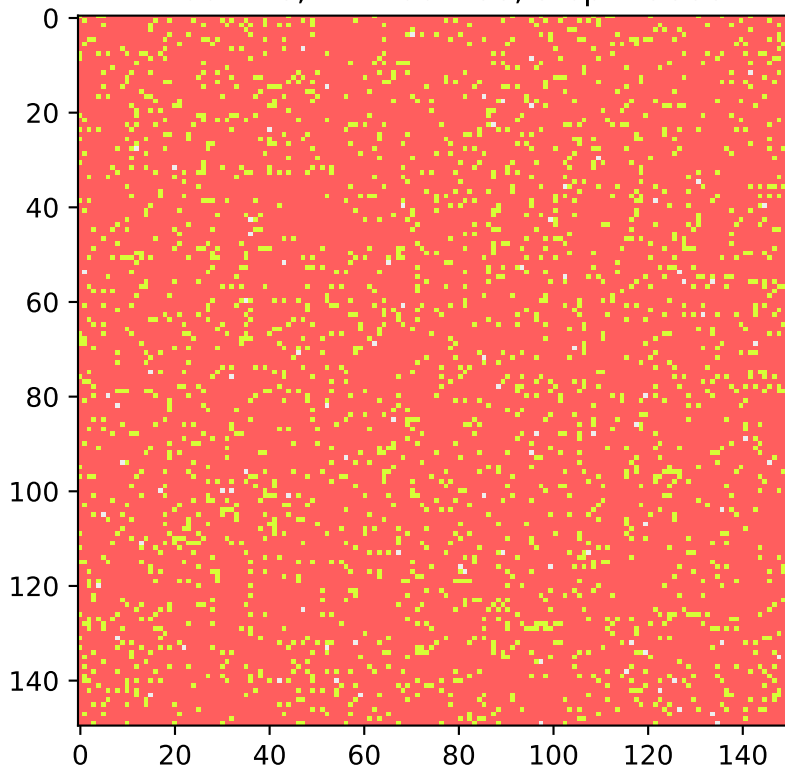
inf dur: 70, imm dur: 80, step: 27000.



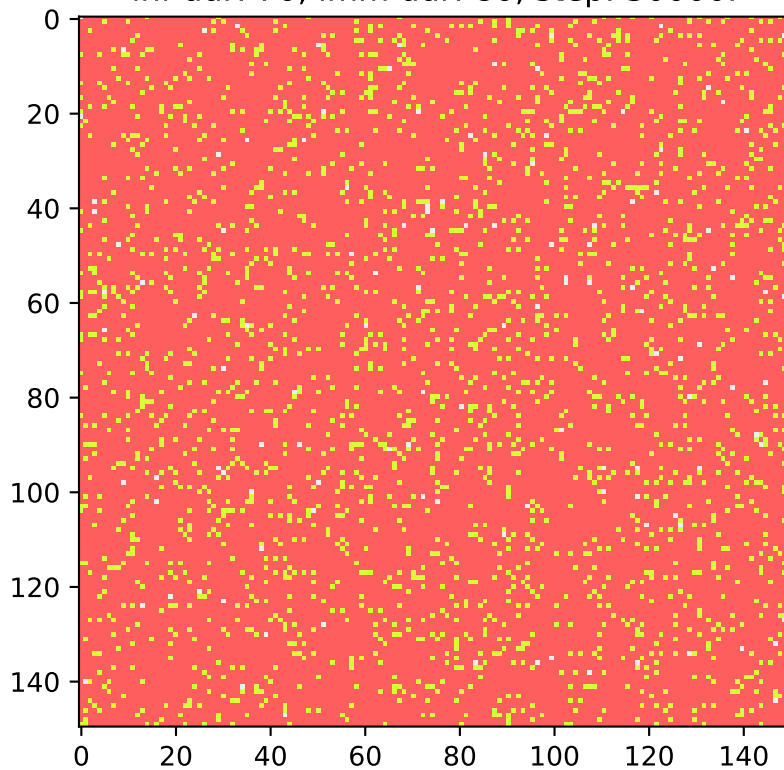
inf dur: 70, imm dur: 80, step: 28000.



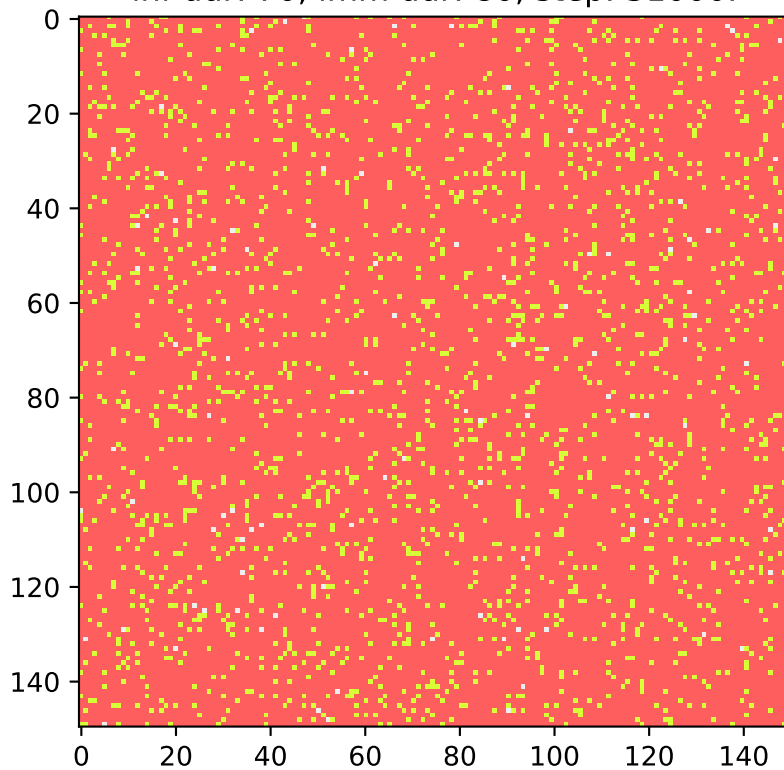
inf dur: 70, imm dur: 80, step: 29000.



inf dur: 70, imm dur: 80, step: 30000.

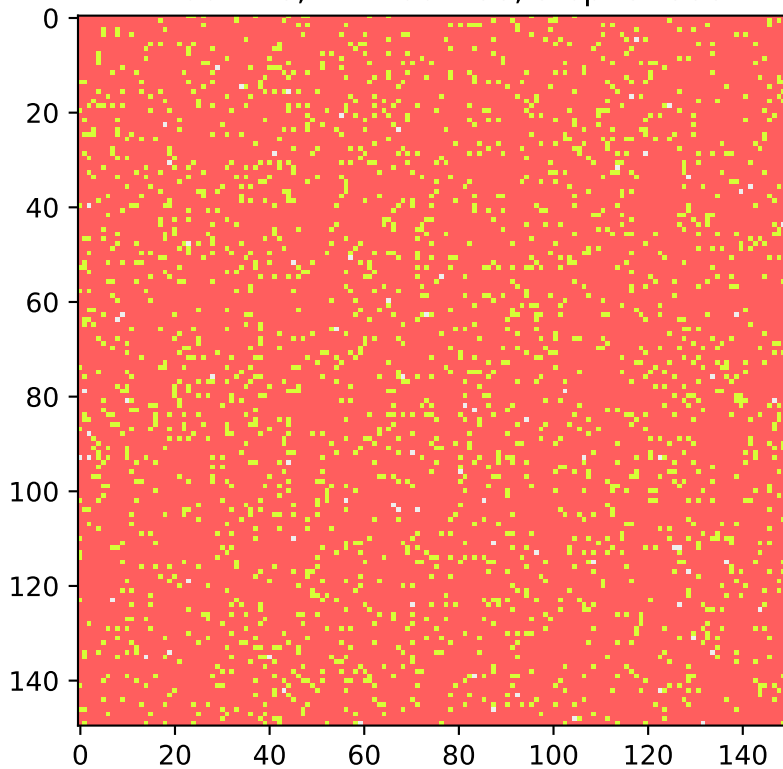


inf dur: 70, imm dur: 80, step: 31000.

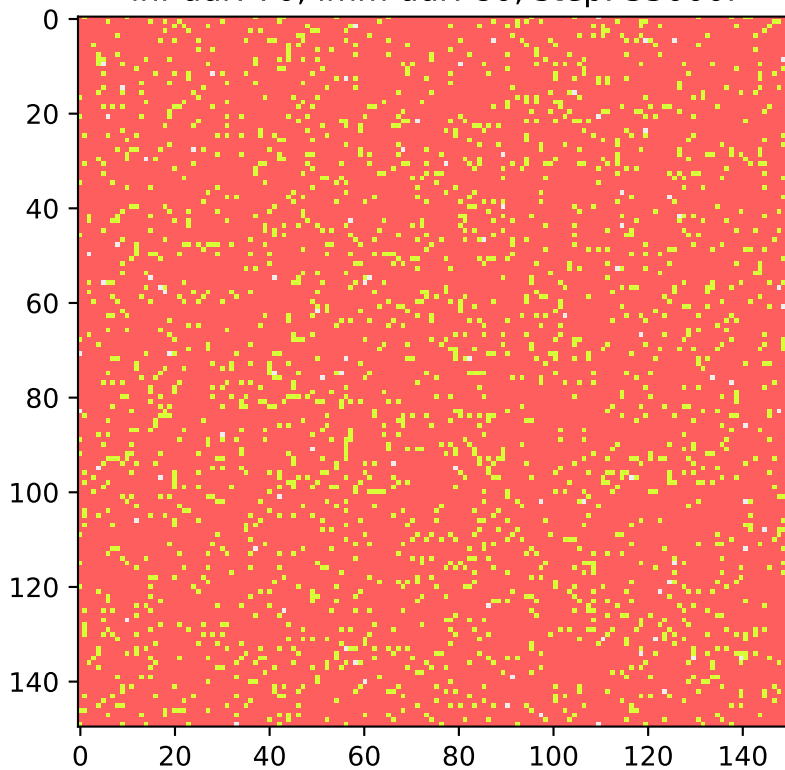




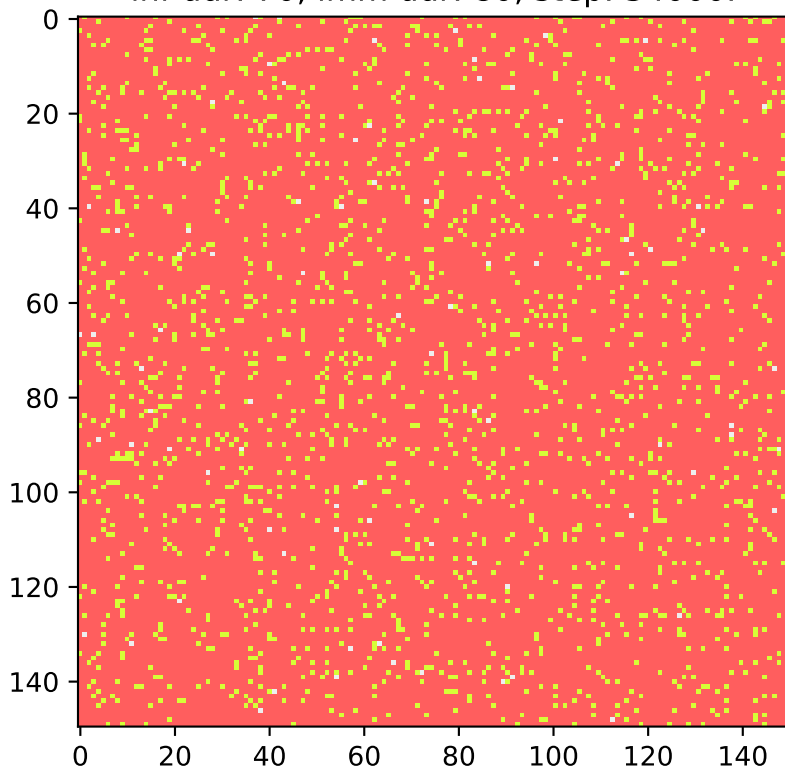
inf dur: 70, imm dur: 80, step: 32000.



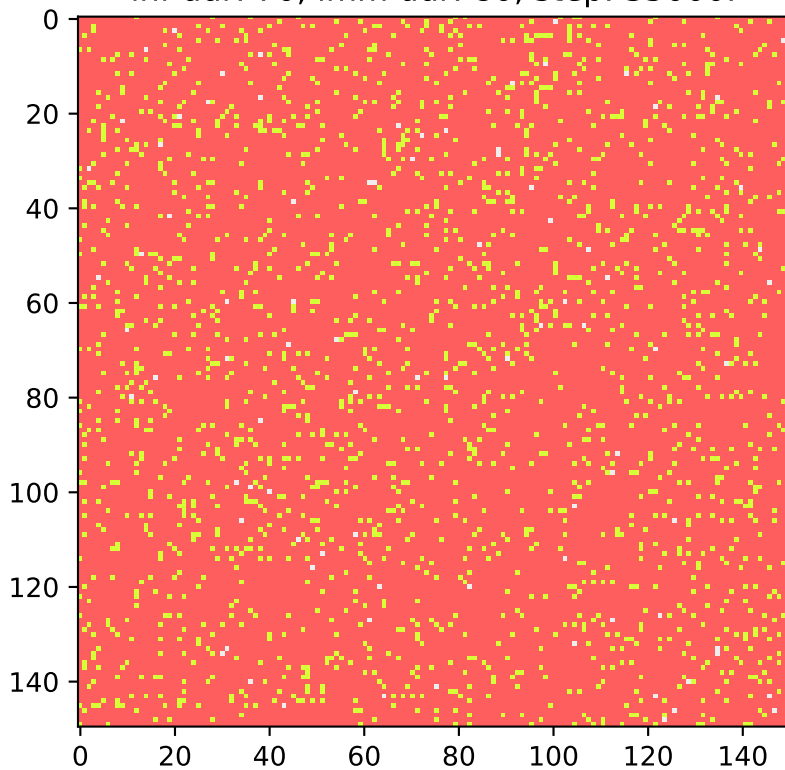
inf dur: 70, imm dur: 80, step: 33000.



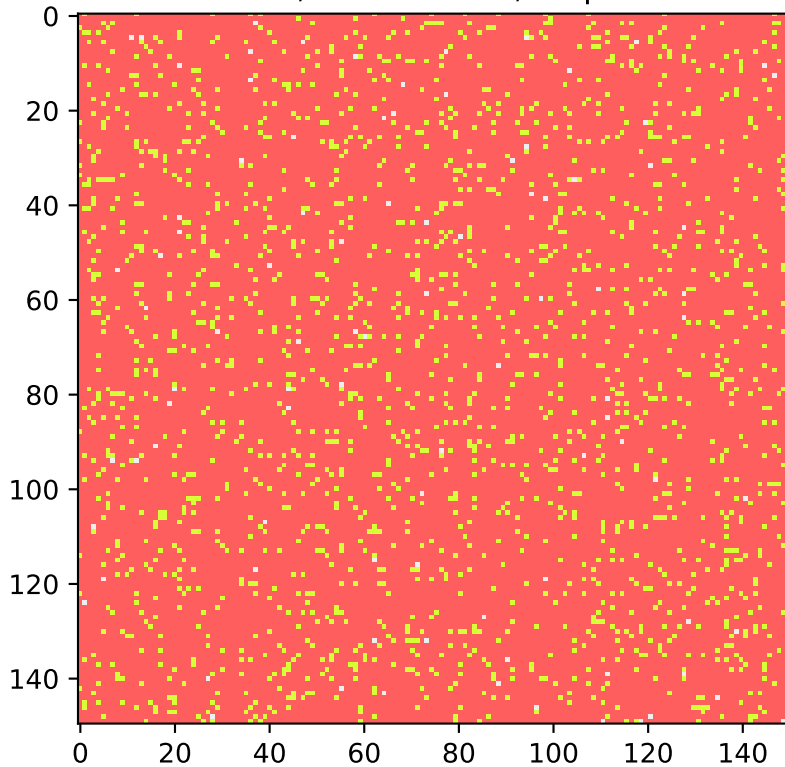
inf dur: 70, imm dur: 80, step: 34000.



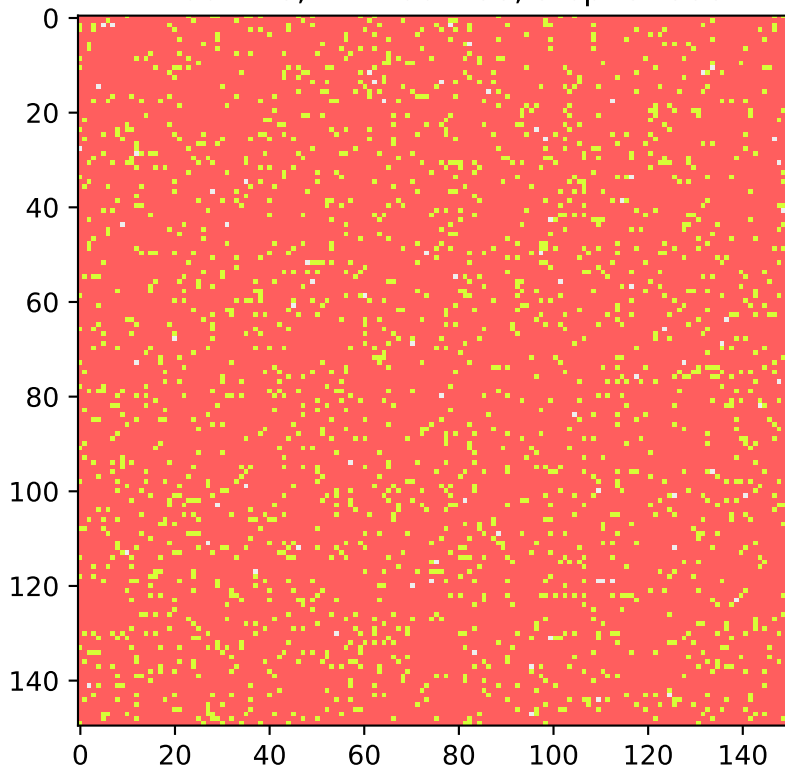
inf dur: 70, imm dur: 80, step: 35000.



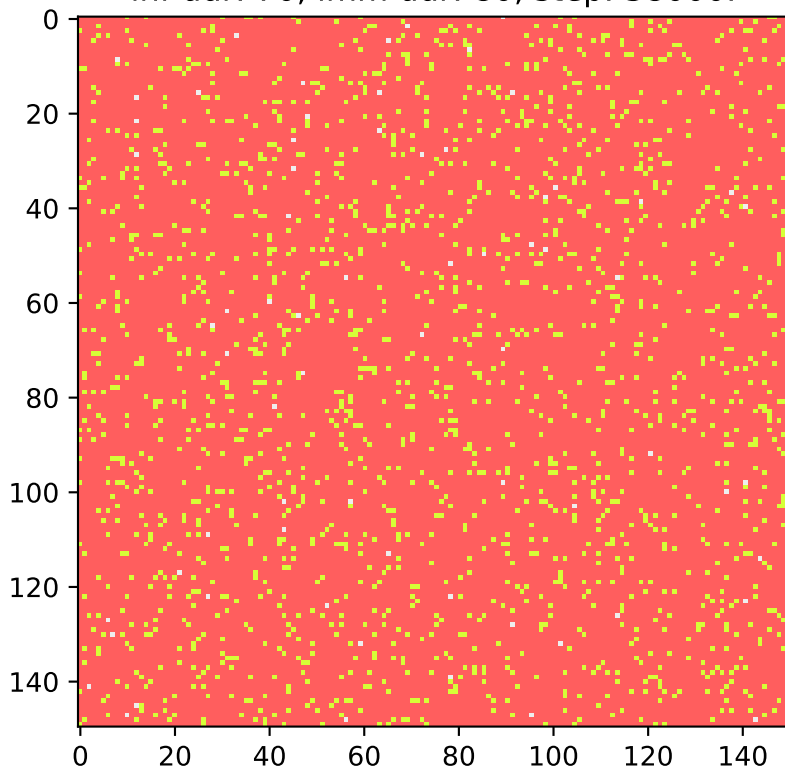
inf dur: 70, imm dur: 80, step: 36000.



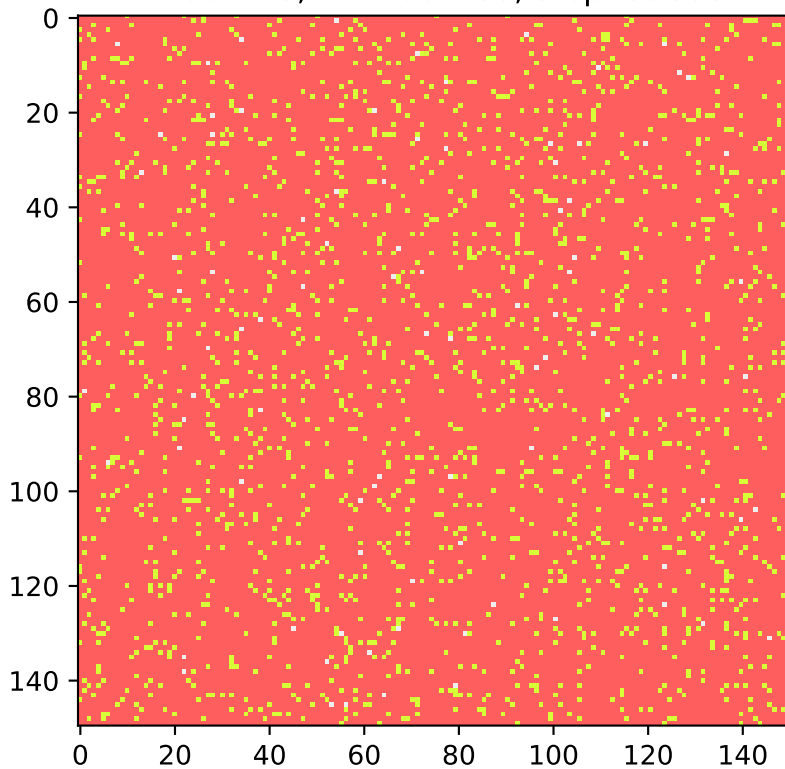
inf dur: 70, imm dur: 80, step: 37000.



inf dur: 70, imm dur: 80, step: 38000.

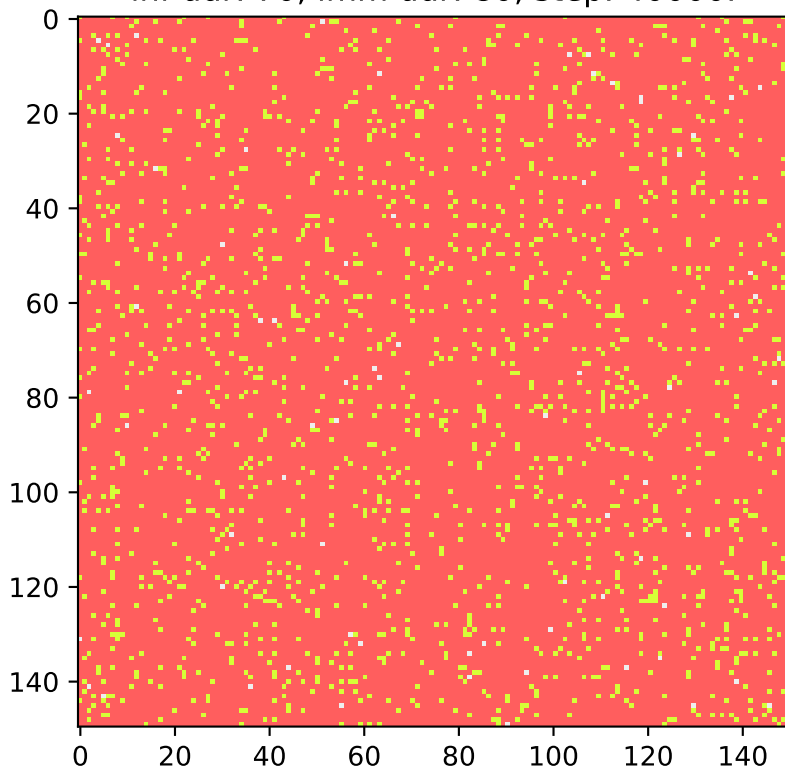


inf dur: 70, imm dur: 80, step: 39000.

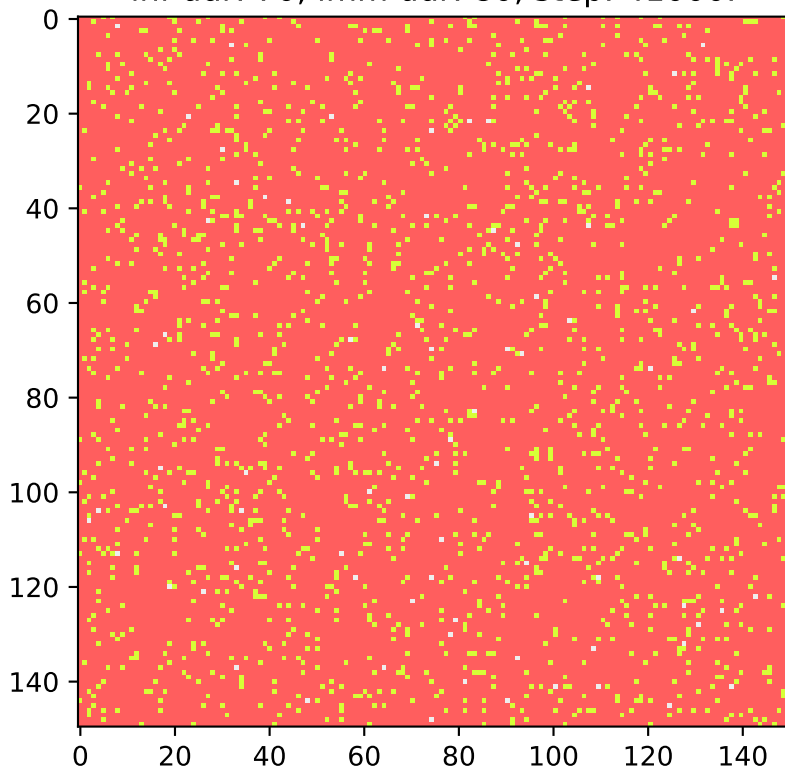




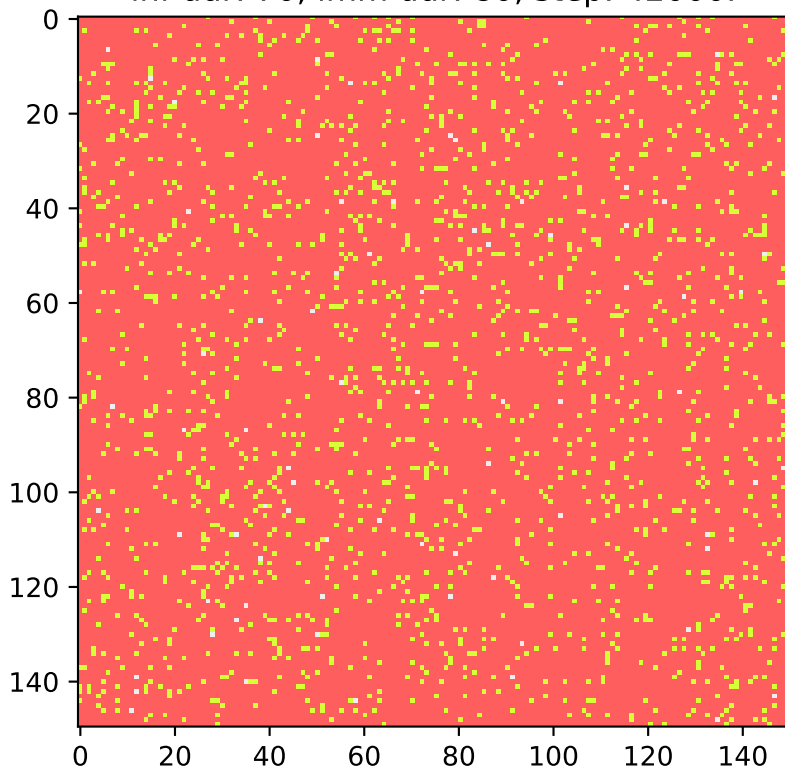
inf dur: 70, imm dur: 80, step: 40000.



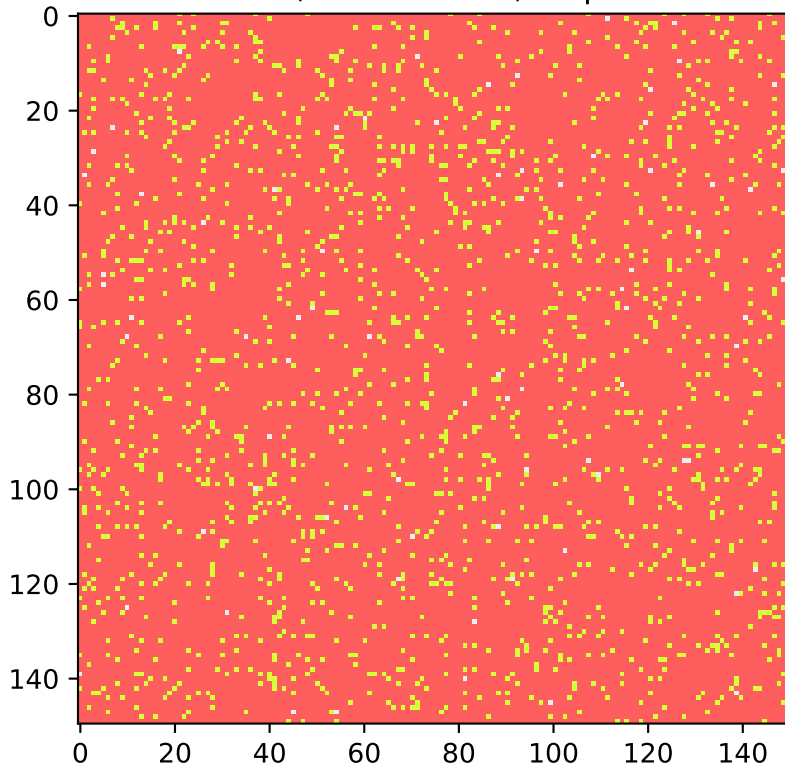
inf dur: 70, imm dur: 80, step: 41000.



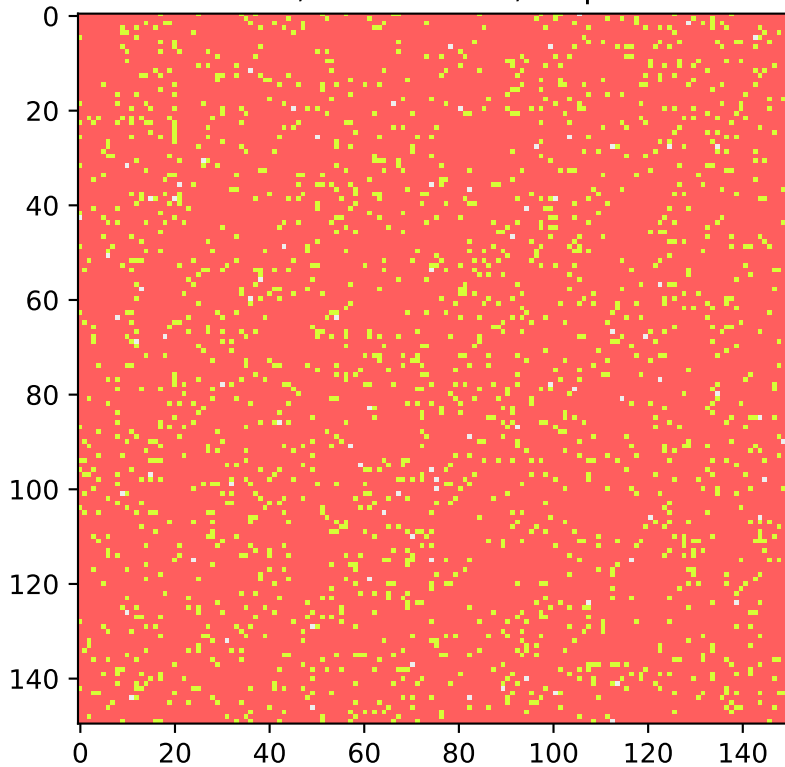
inf dur: 70, imm dur: 80, step: 42000.



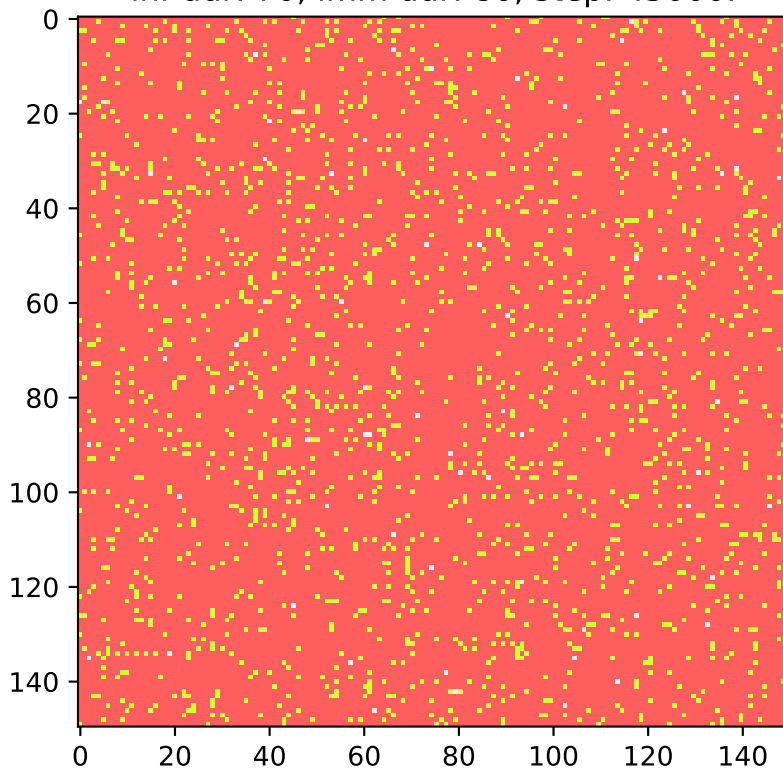
inf dur: 70, imm dur: 80, step: 43000.



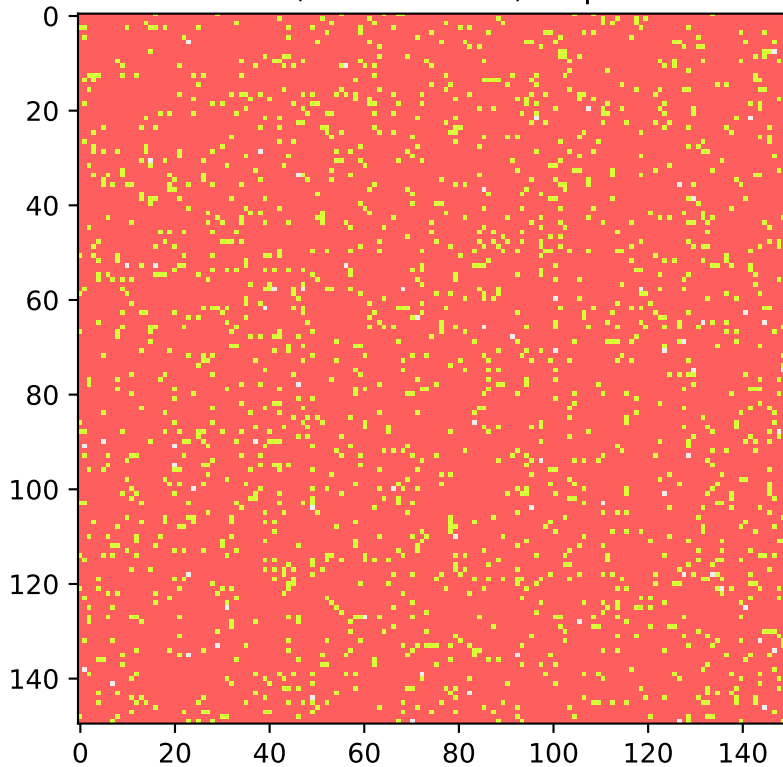
inf dur: 70, imm dur: 80, step: 44000.



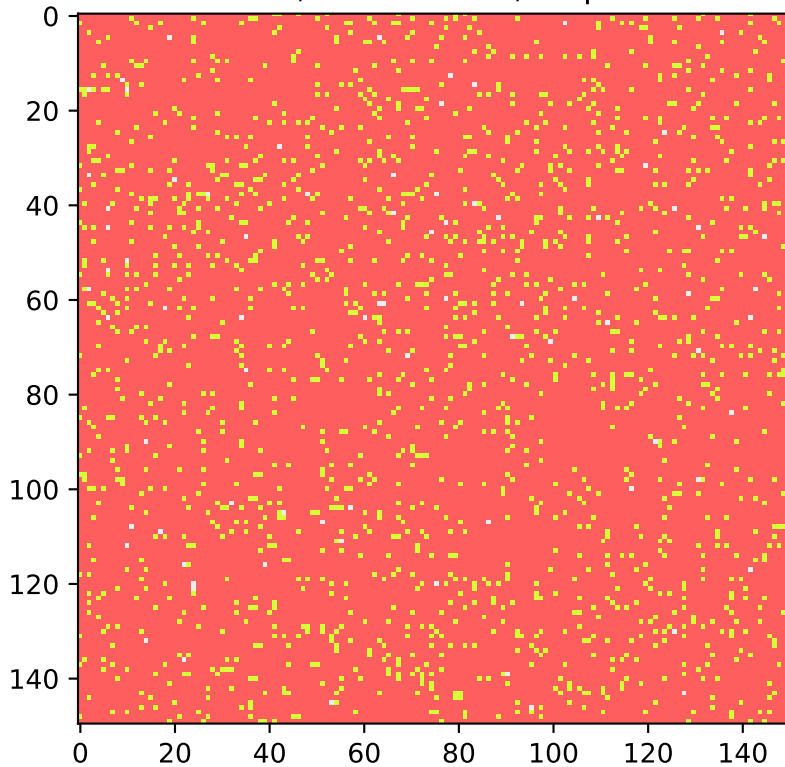
inf dur: 70, imm dur: 80, step: 45000.



inf dur: 70, imm dur: 80, step: 46000.

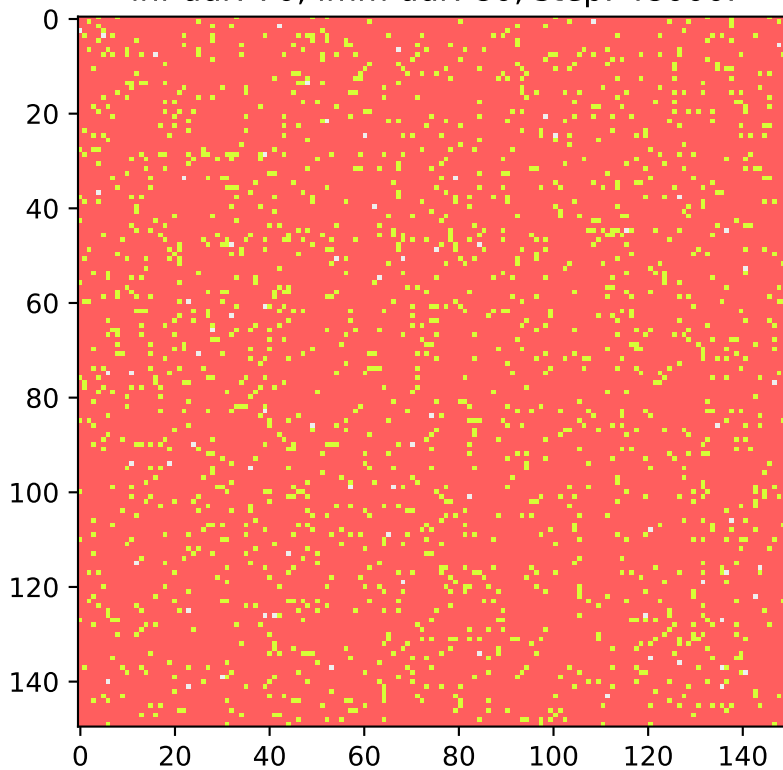


inf dur: 70, imm dur: 80, step: 47000.

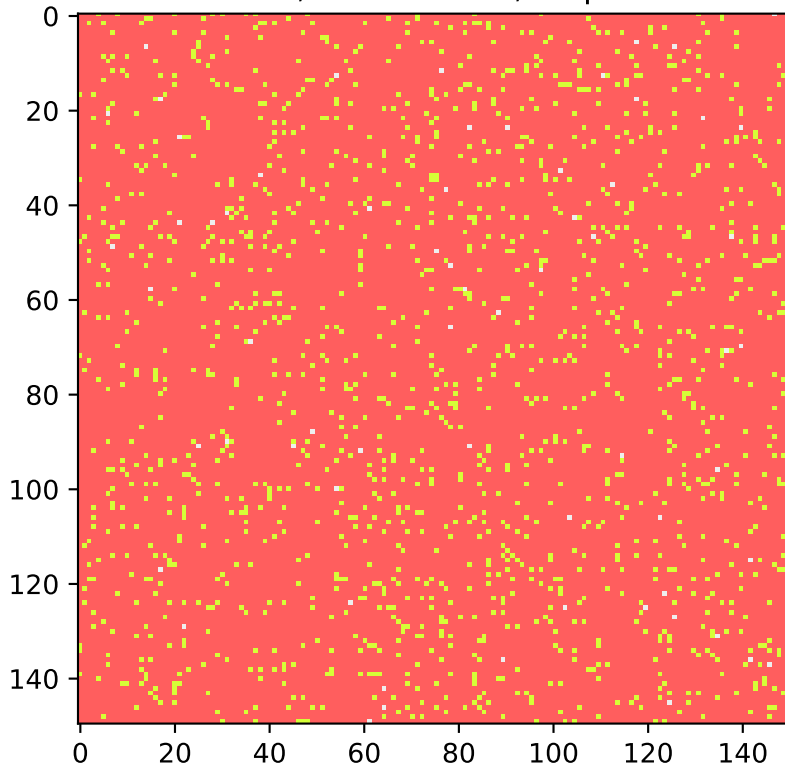




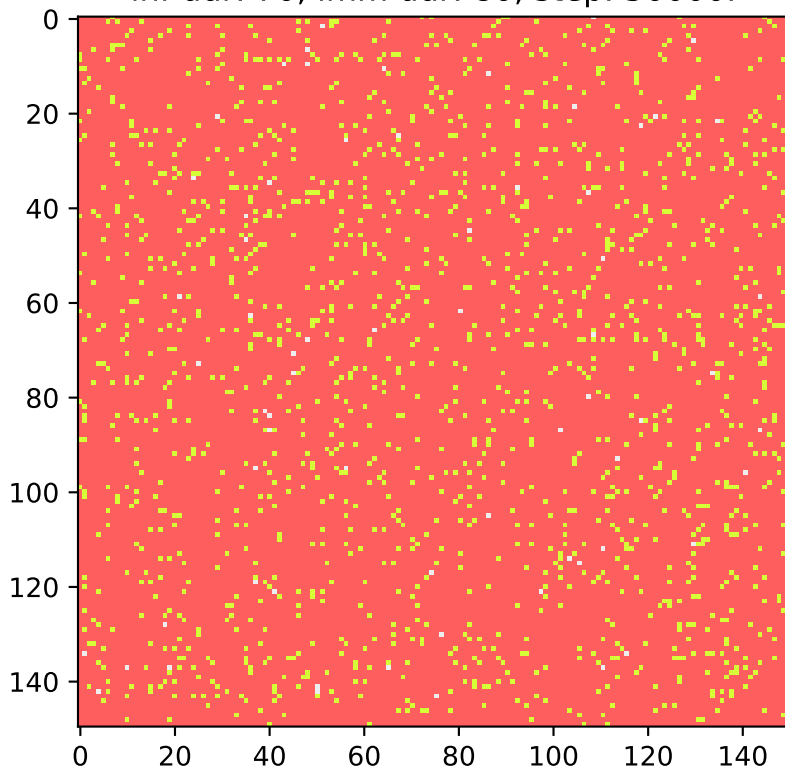
inf dur: 70, imm dur: 80, step: 48000.



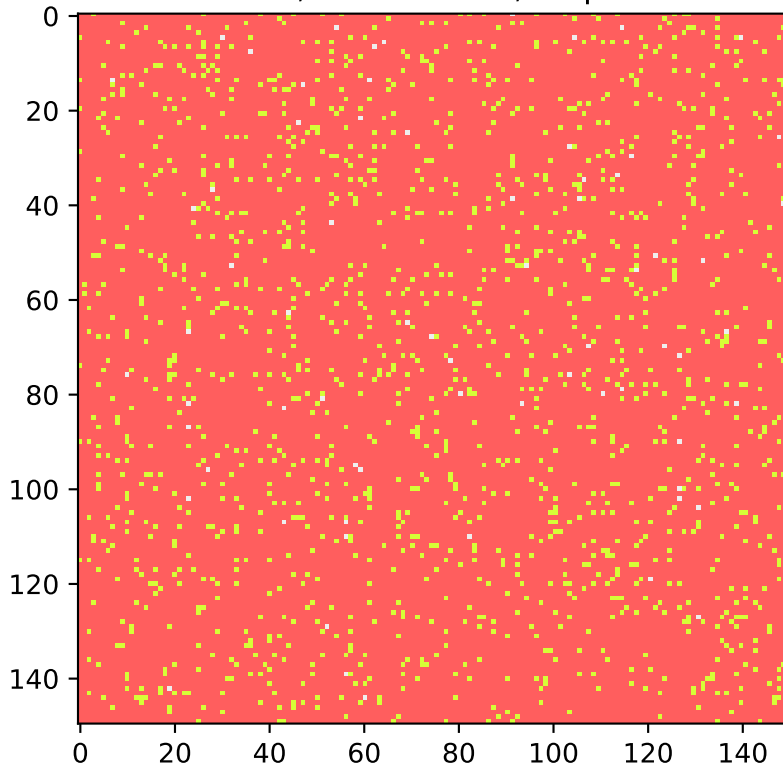
inf dur: 70, imm dur: 80, step: 49000.



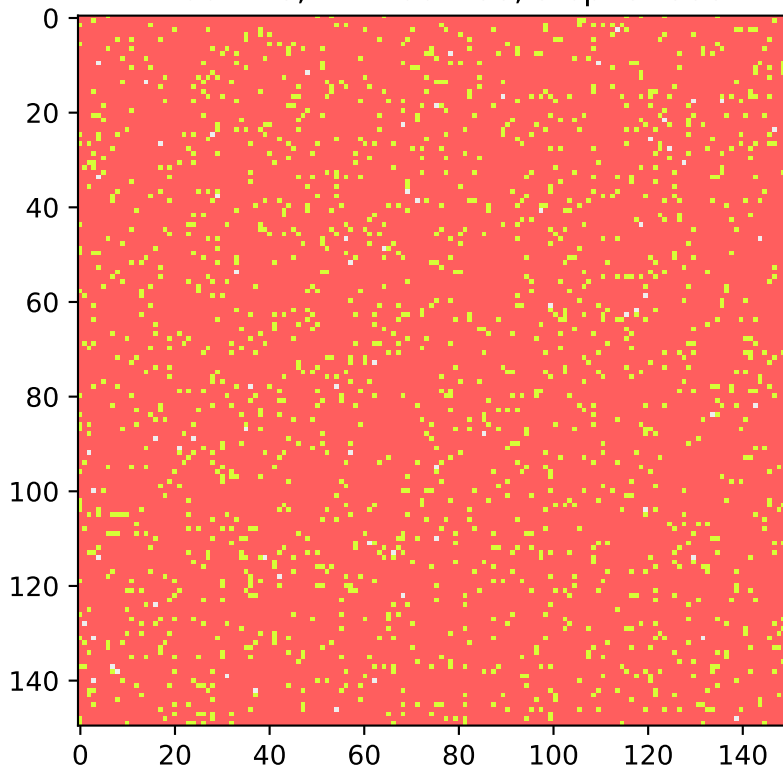
inf dur: 70, imm dur: 80, step: 50000.



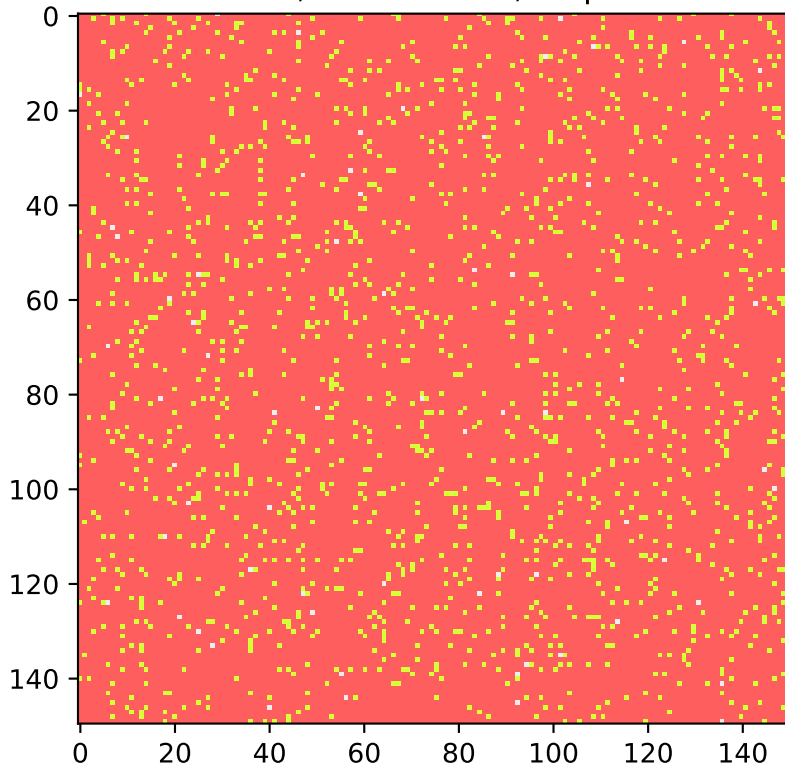
inf dur: 70, imm dur: 80, step: 51000.



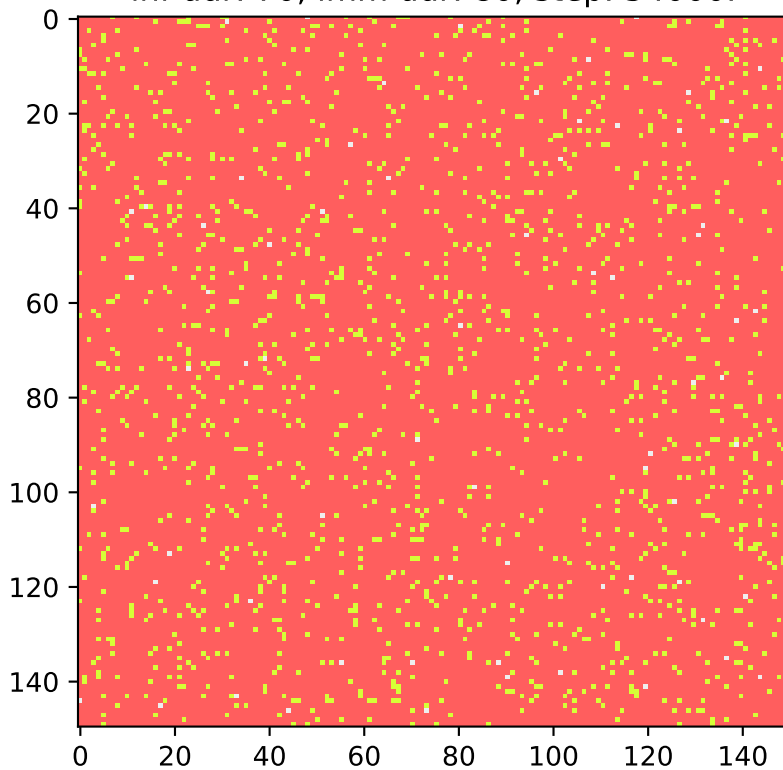
inf dur: 70, imm dur: 80, step: 52000.



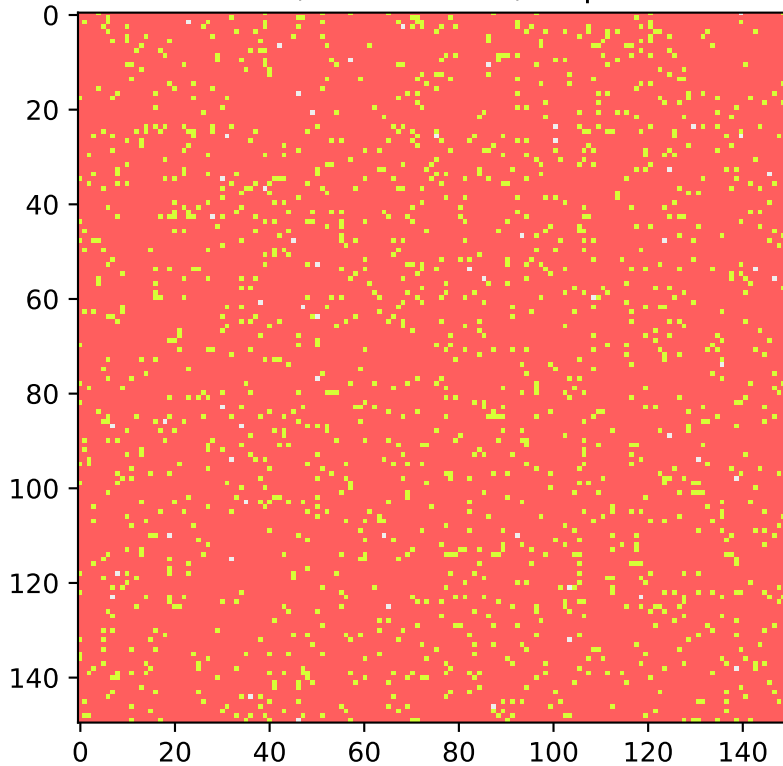
inf dur: 70, imm dur: 80, step: 53000.



inf dur: 70, imm dur: 80, step: 54000.

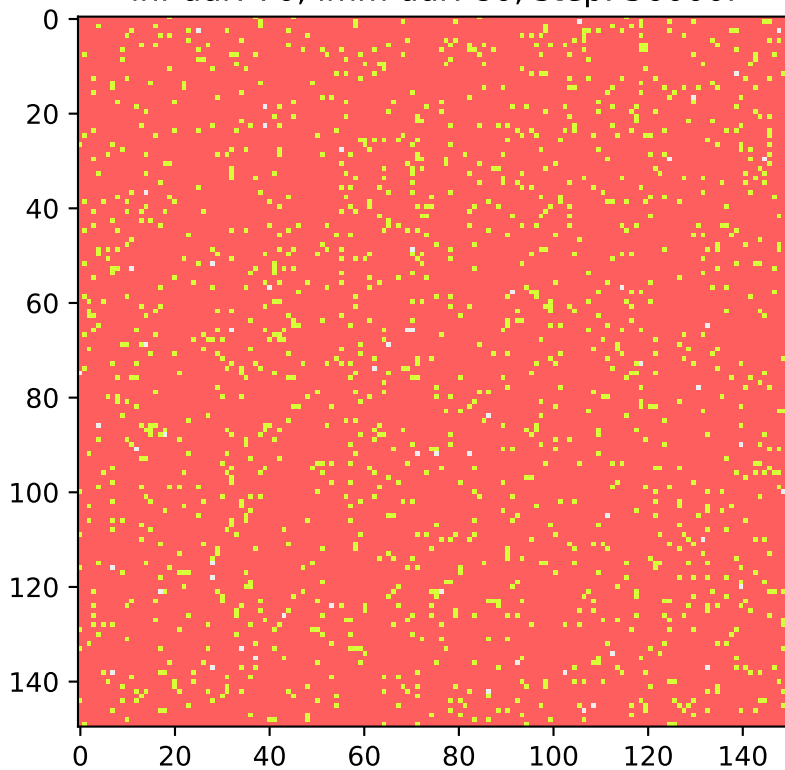


inf dur: 70, imm dur: 80, step: 55000.

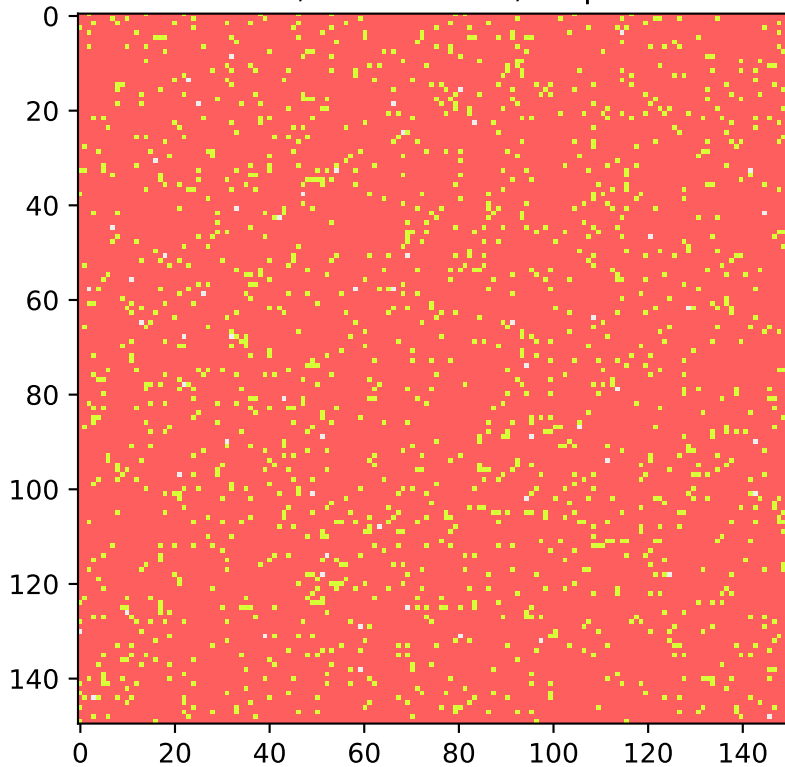




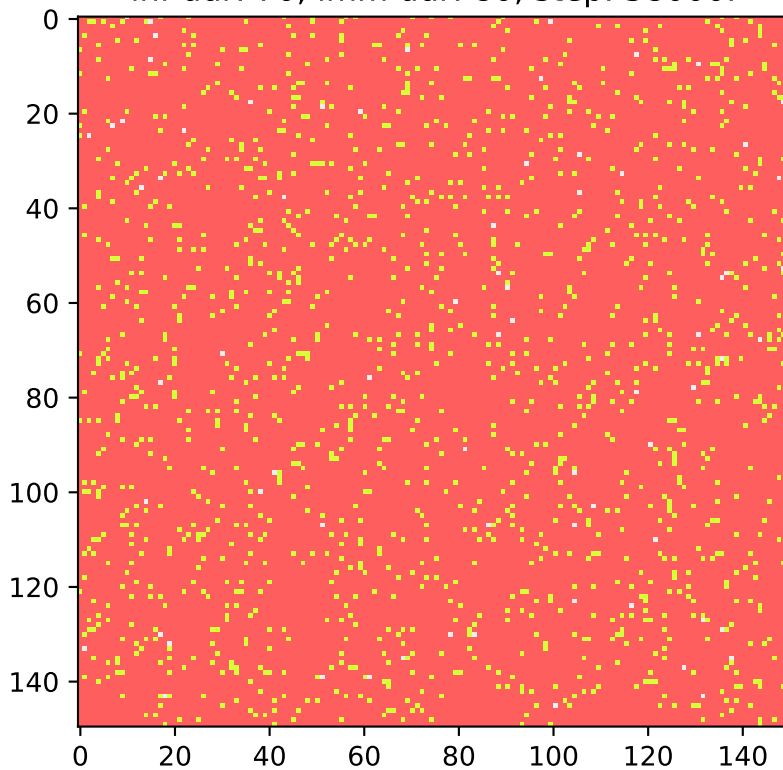
inf dur: 70, imm dur: 80, step: 56000.



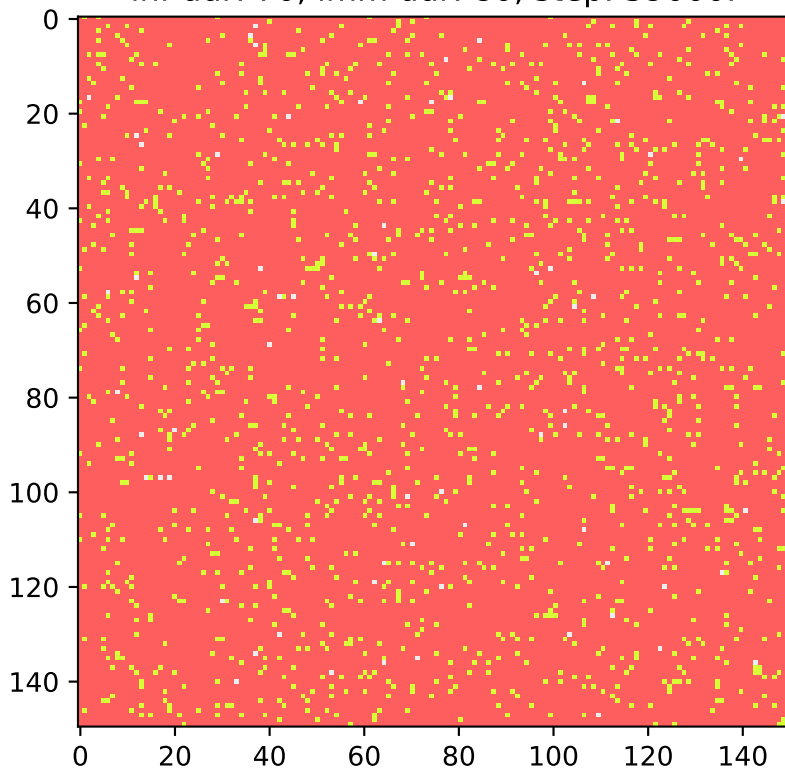
inf dur: 70, imm dur: 80, step: 57000.



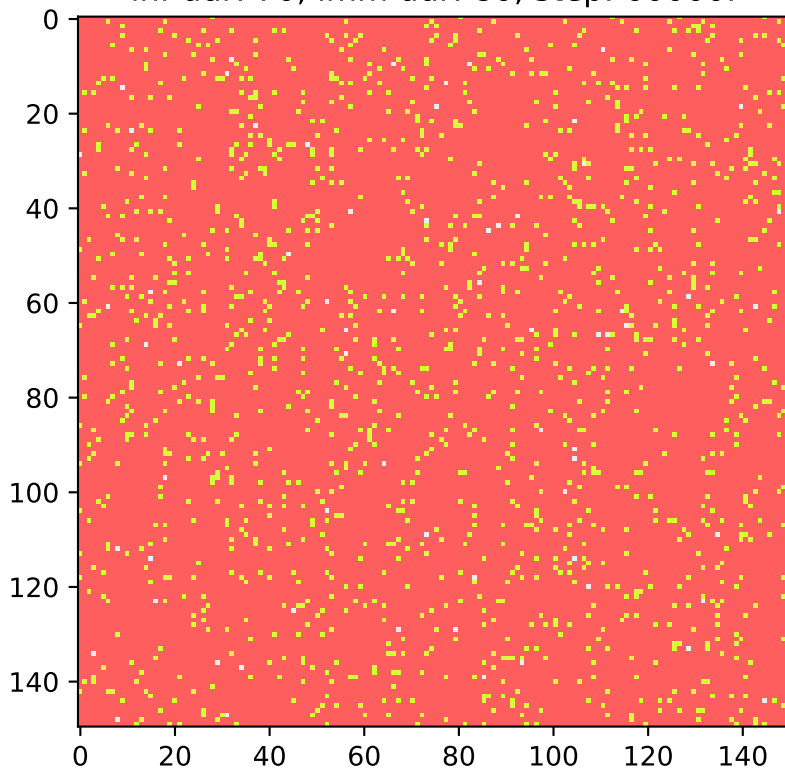
inf dur: 70, imm dur: 80, step: 58000.



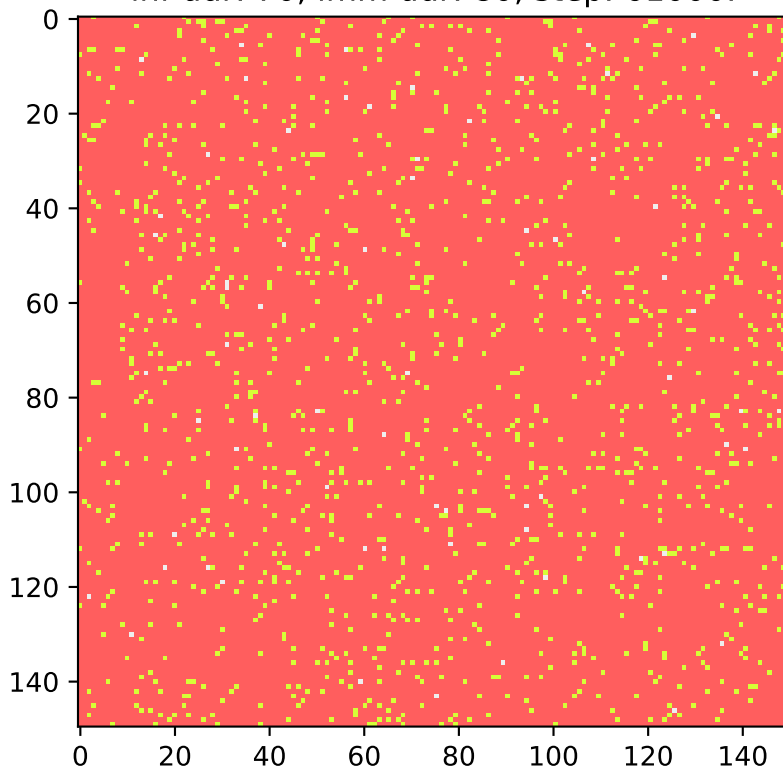
inf dur: 70, imm dur: 80, step: 59000.



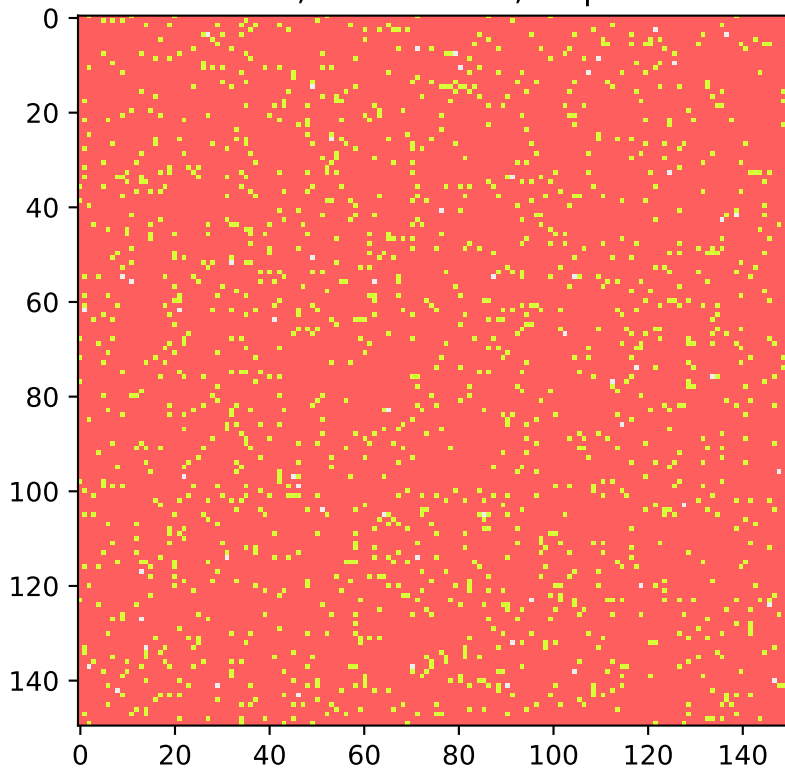
inf dur: 70, imm dur: 80, step: 60000.



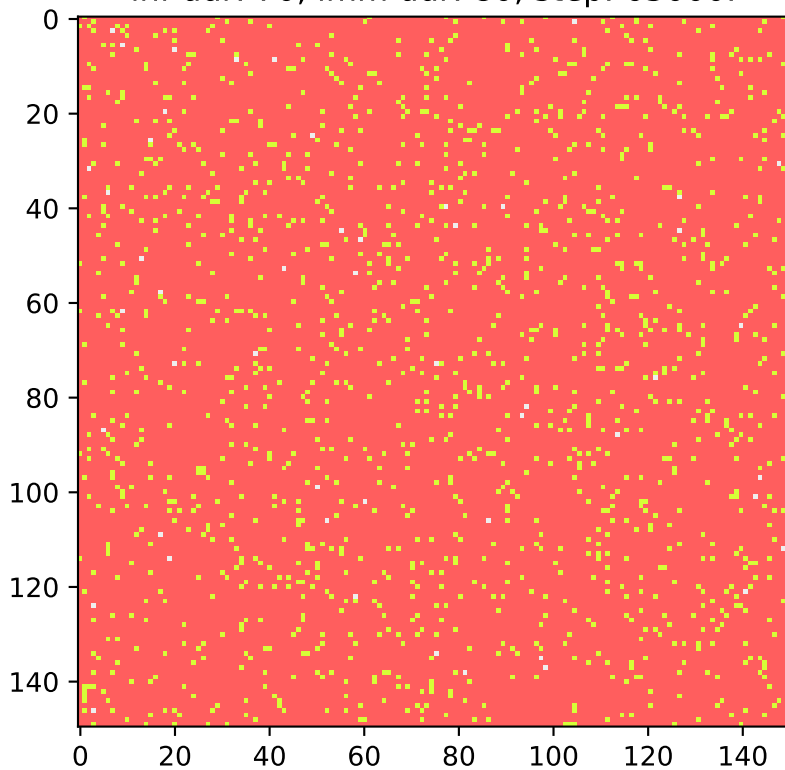
inf dur: 70, imm dur: 80, step: 61000.



inf dur: 70, imm dur: 80, step: 62000.

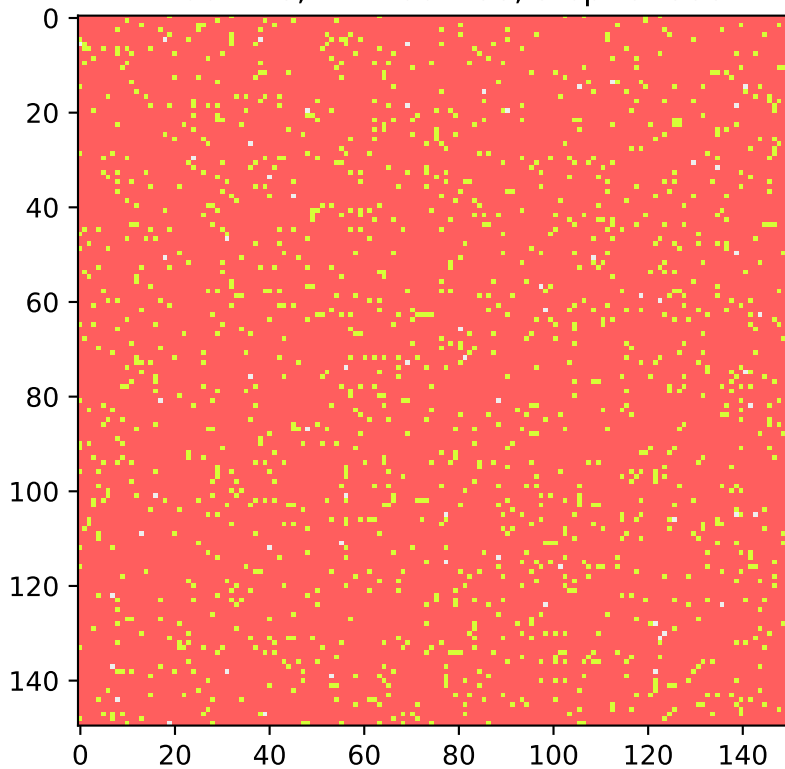


inf dur: 70, imm dur: 80, step: 63000.

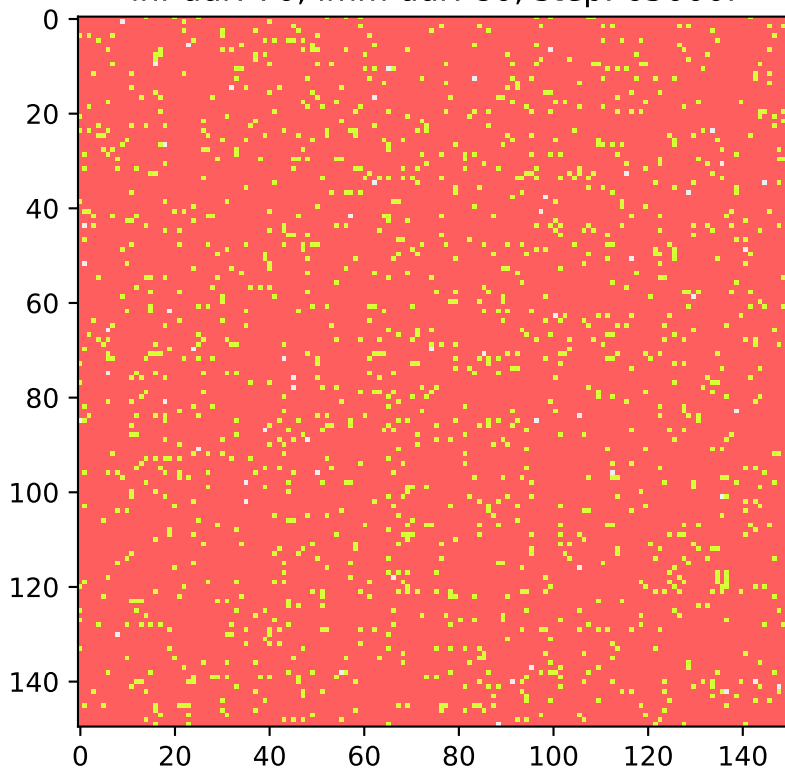




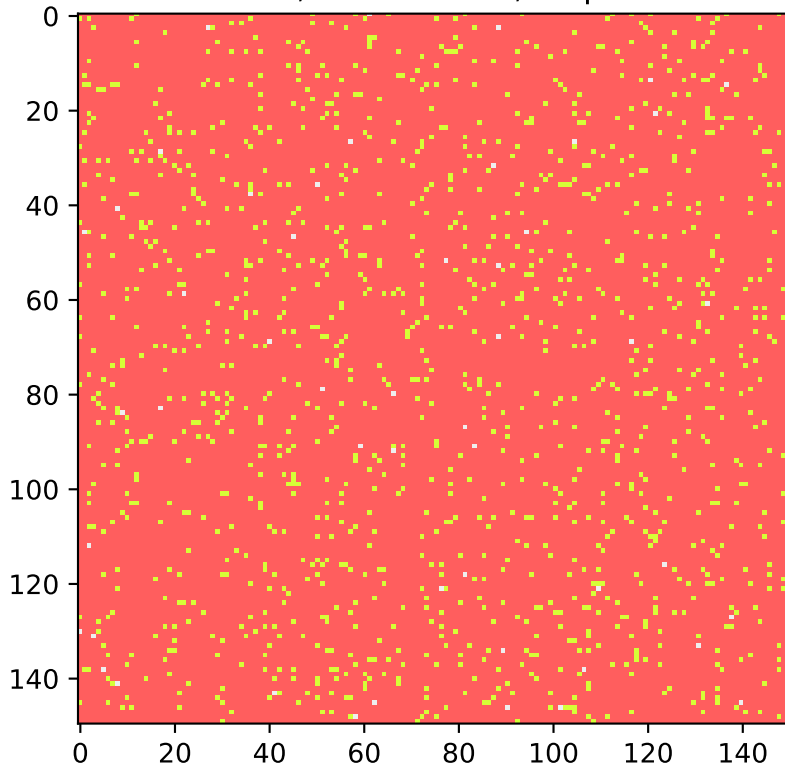
inf dur: 70, imm dur: 80, step: 64000.



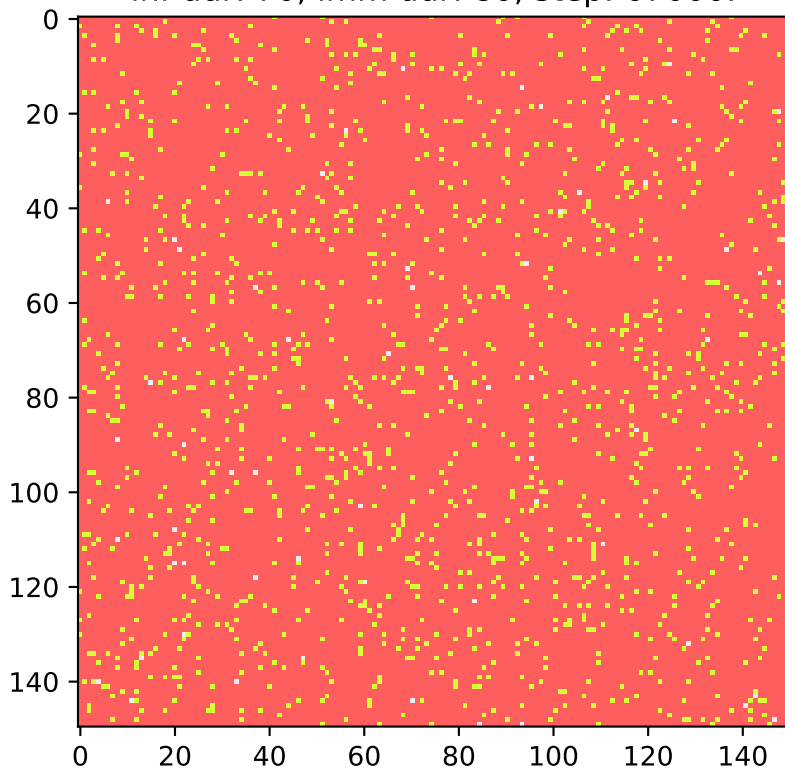
inf dur: 70, imm dur: 80, step: 65000.



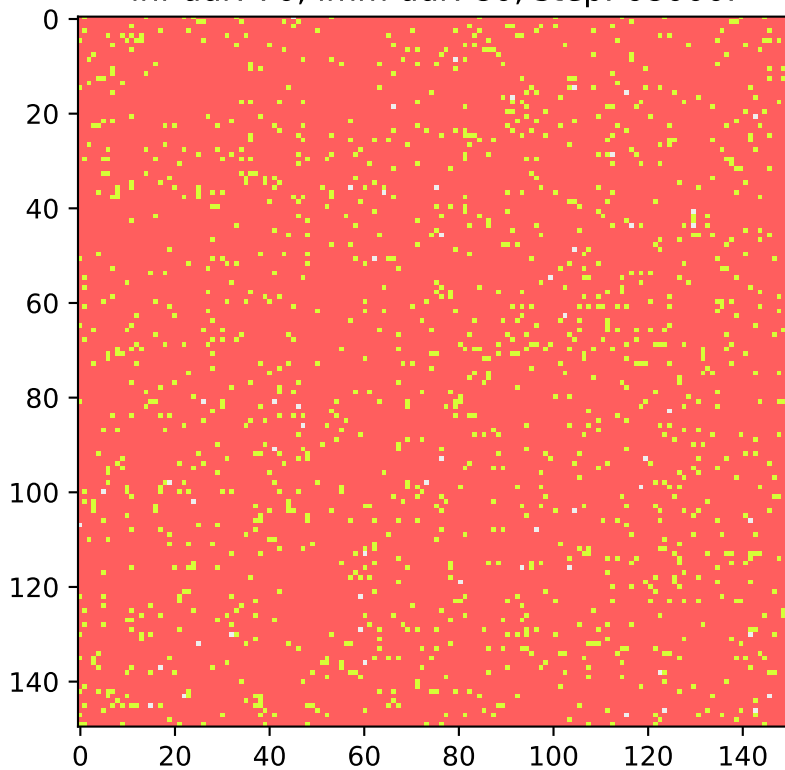
inf dur: 70, imm dur: 80, step: 66000.



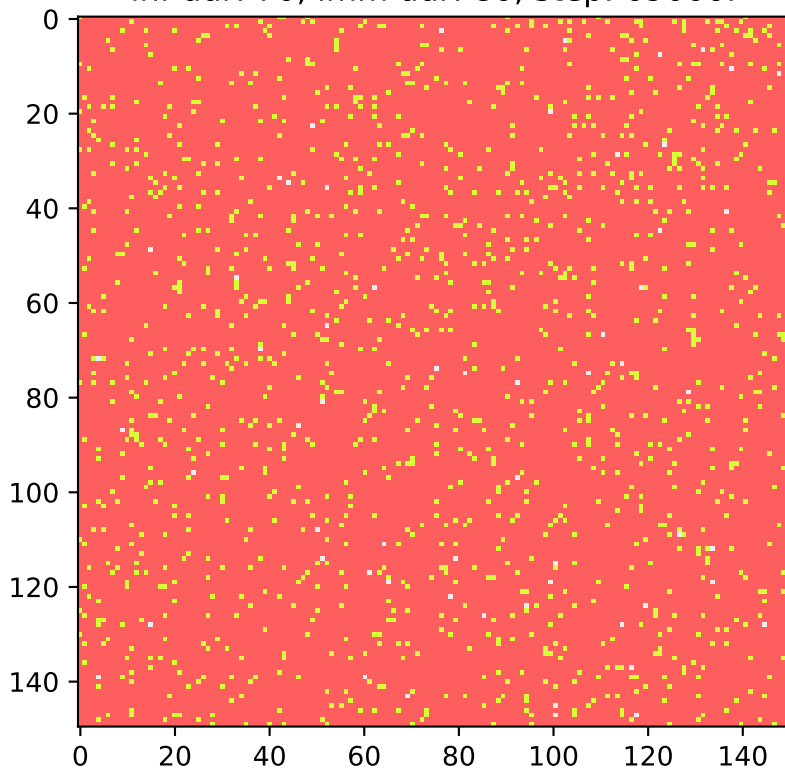
inf dur: 70, imm dur: 80, step: 67000.



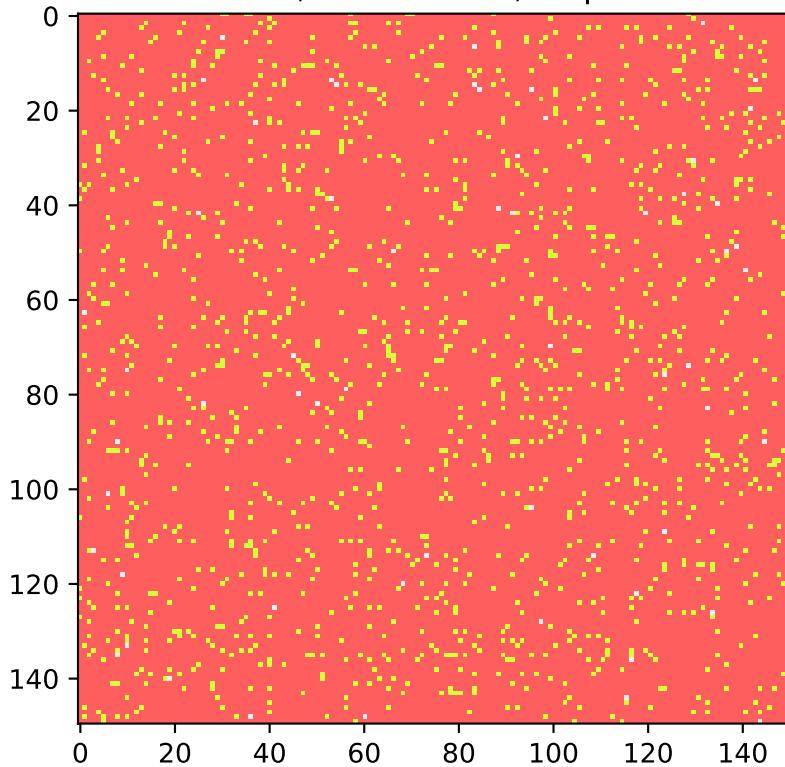
inf dur: 70, imm dur: 80, step: 68000.



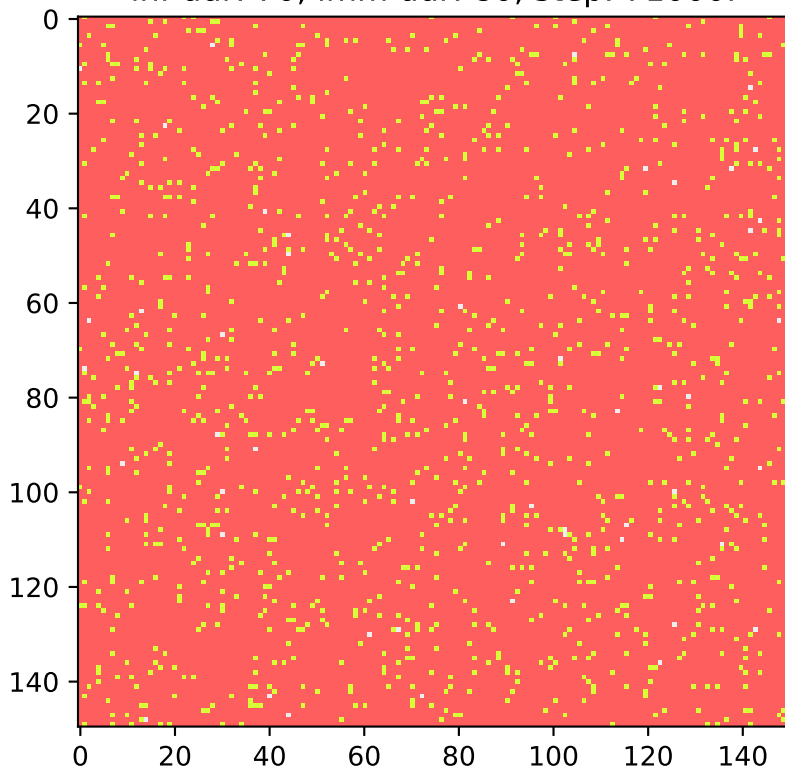
inf dur: 70, imm dur: 80, step: 69000.



inf dur: 70, imm dur: 80, step: 70000.

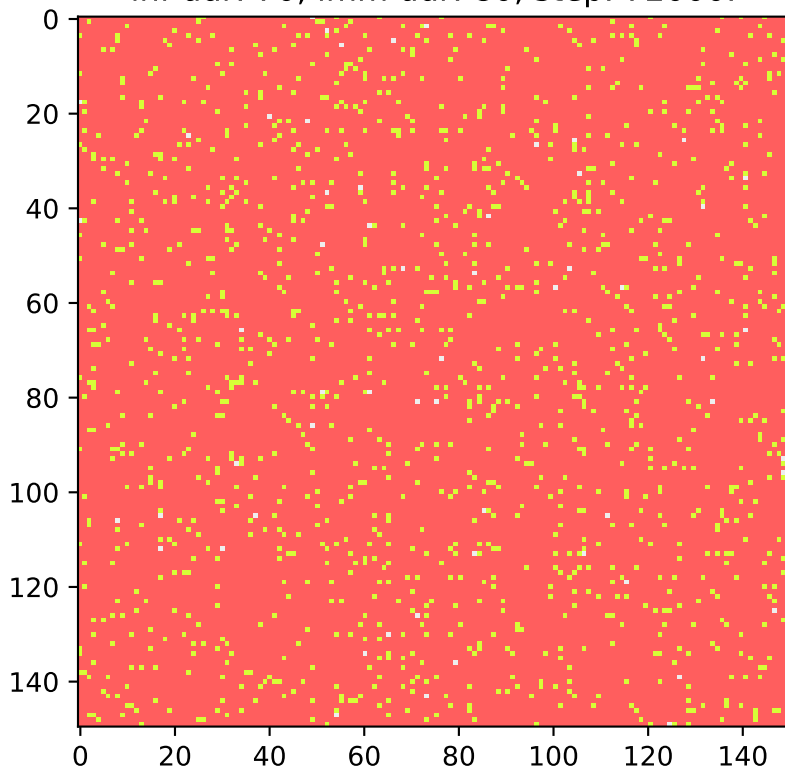


inf dur: 70, imm dur: 80, step: 71000.

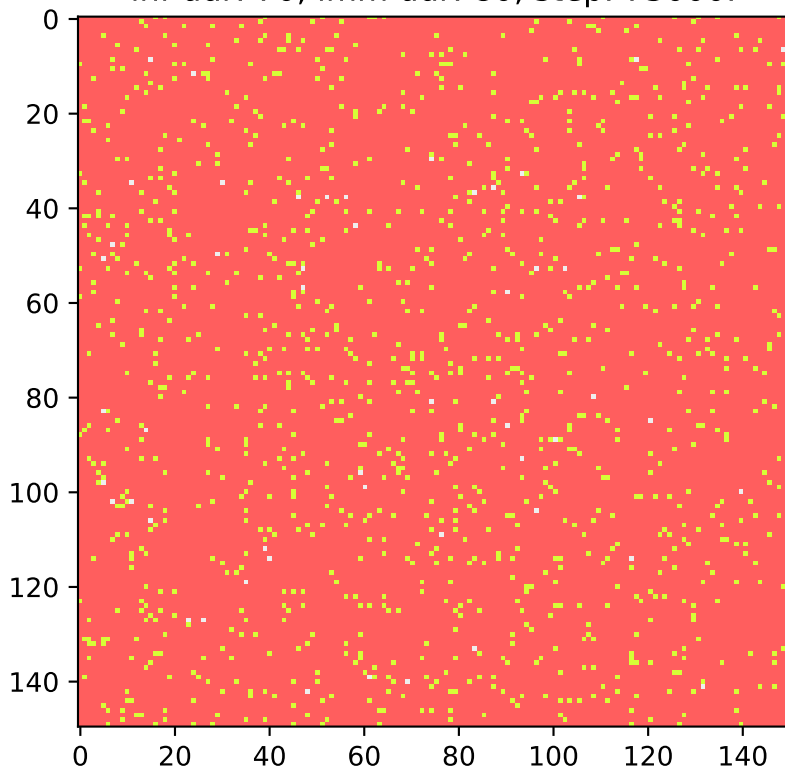




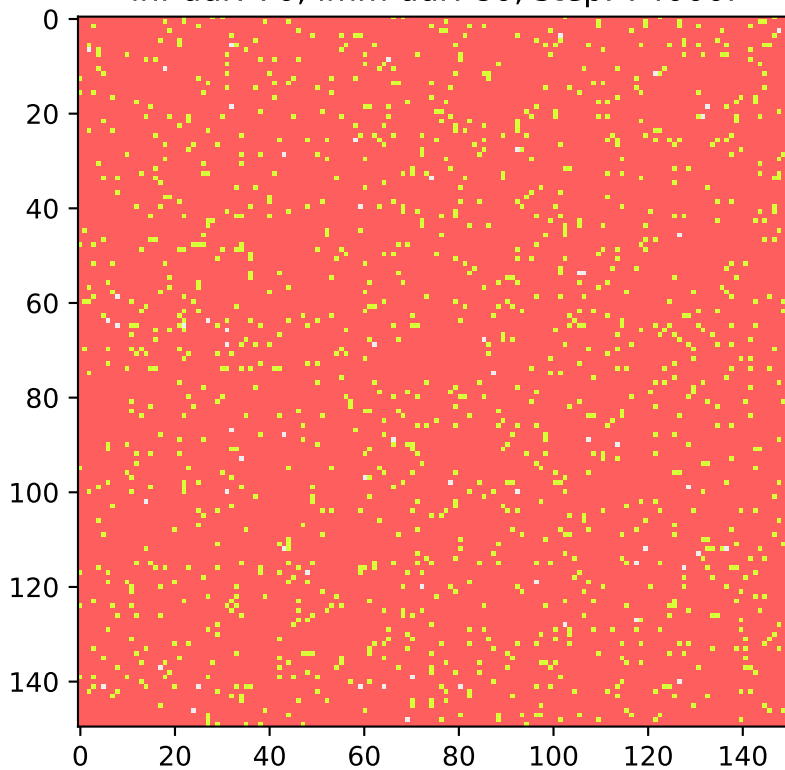
inf dur: 70, imm dur: 80, step: 72000.



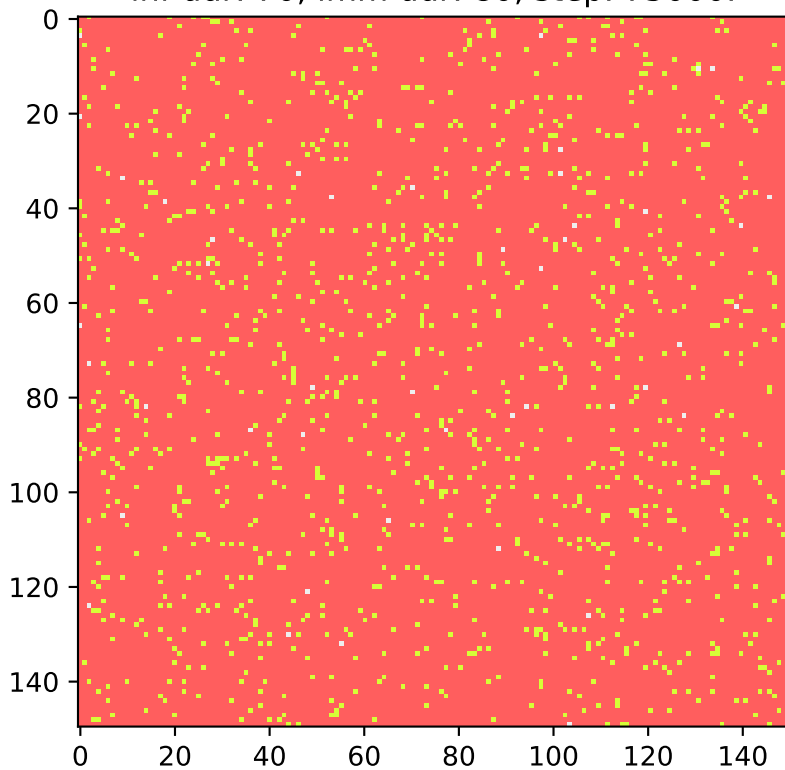
inf dur: 70, imm dur: 80, step: 73000.



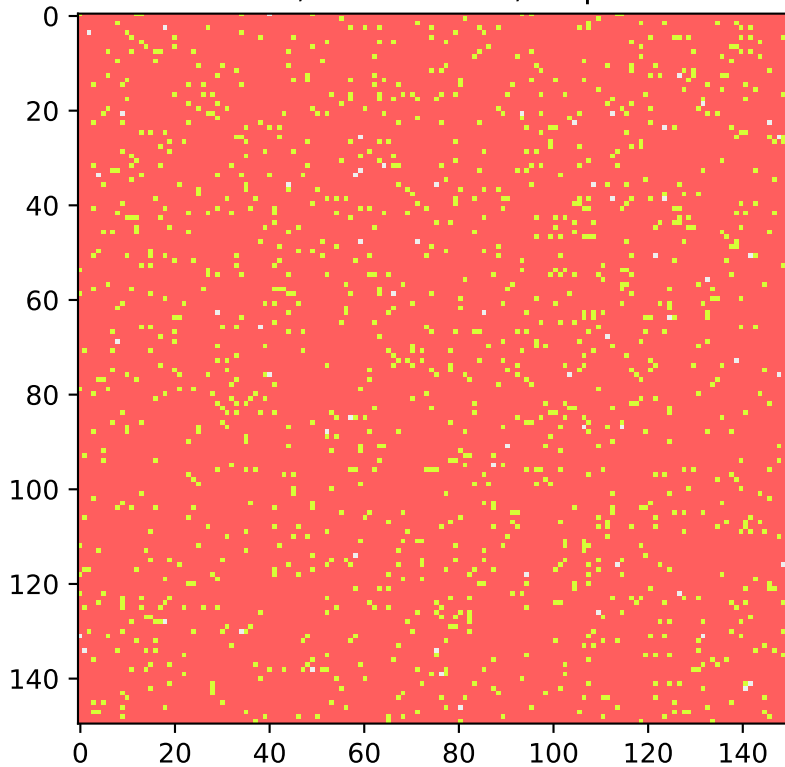
inf dur: 70, imm dur: 80, step: 74000.



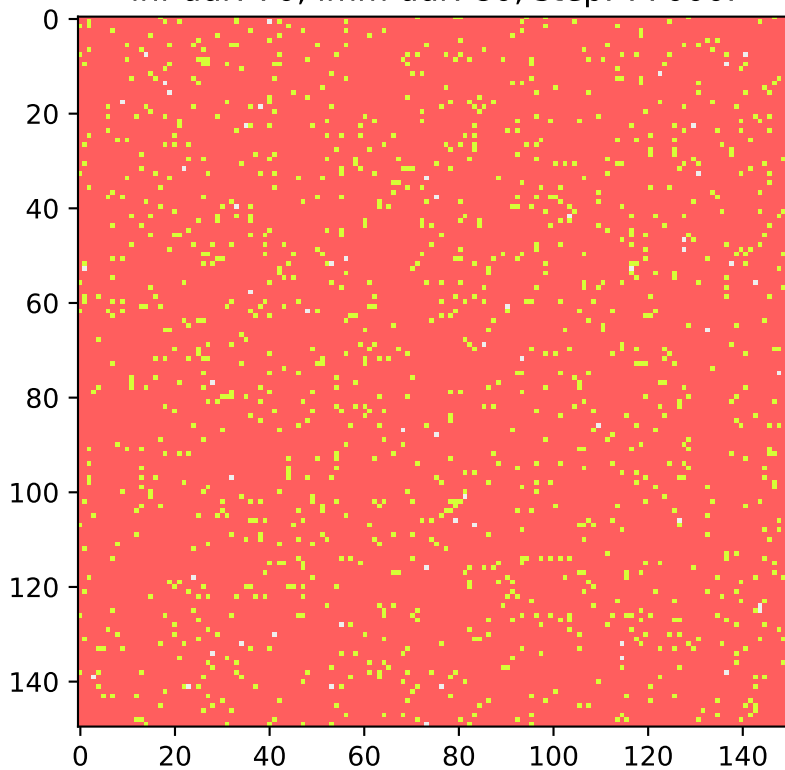
inf dur: 70, imm dur: 80, step: 75000.



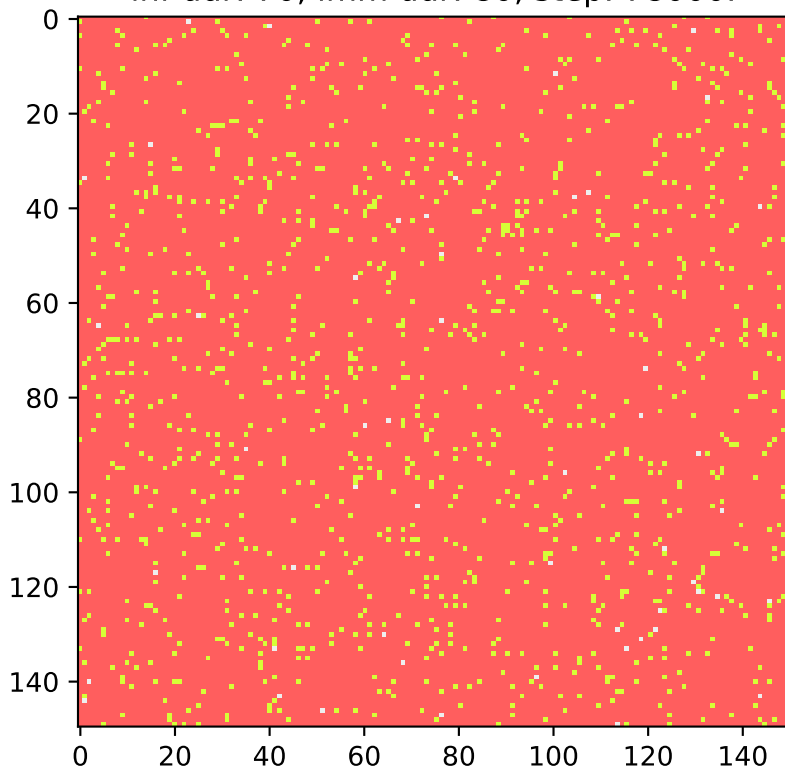
inf dur: 70, imm dur: 80, step: 76000.



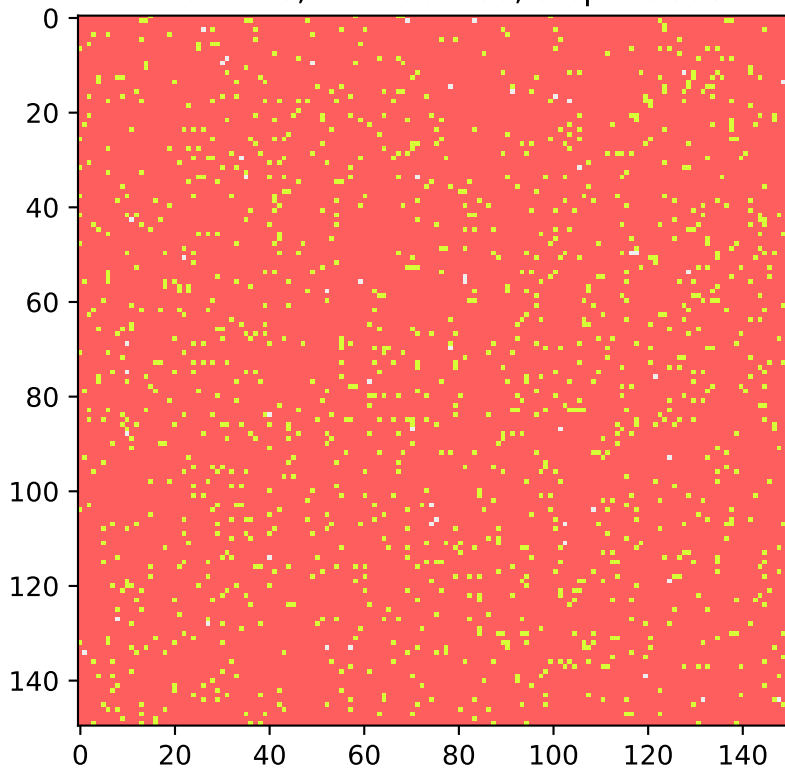
inf dur: 70, imm dur: 80, step: 77000.



inf dur: 70, imm dur: 80, step: 78000.

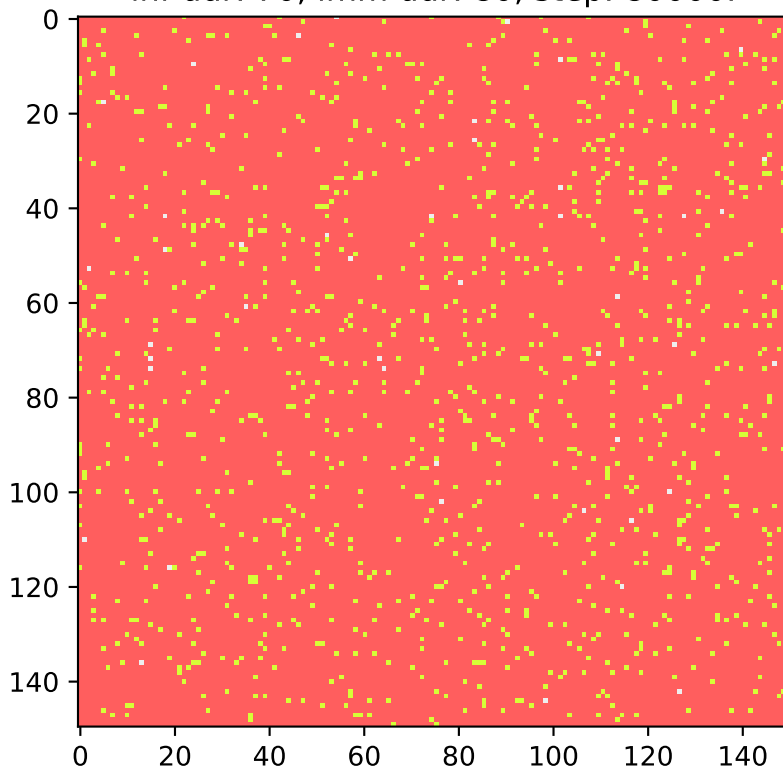


inf dur: 70, imm dur: 80, step: 79000.

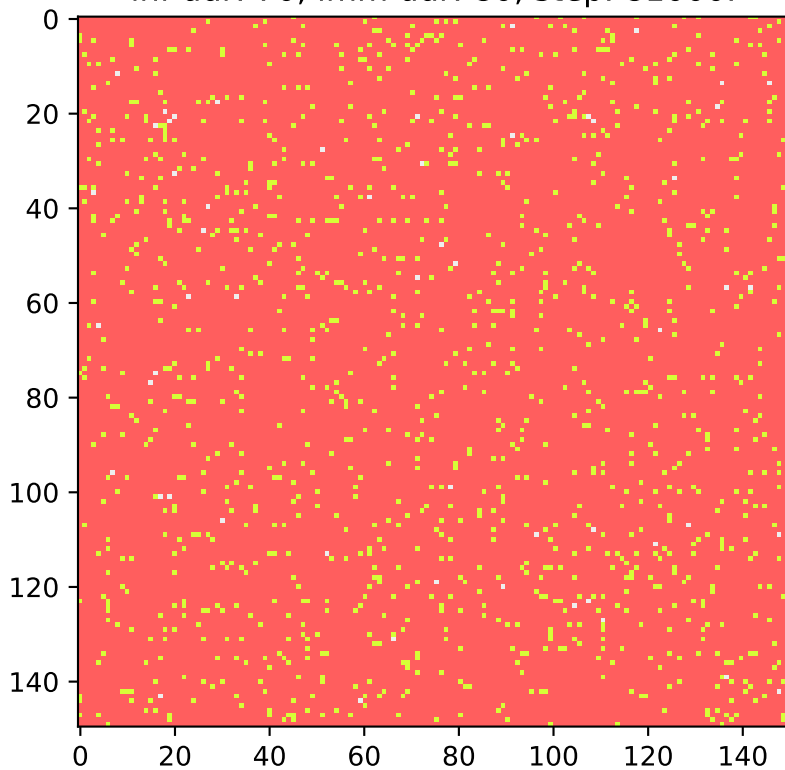




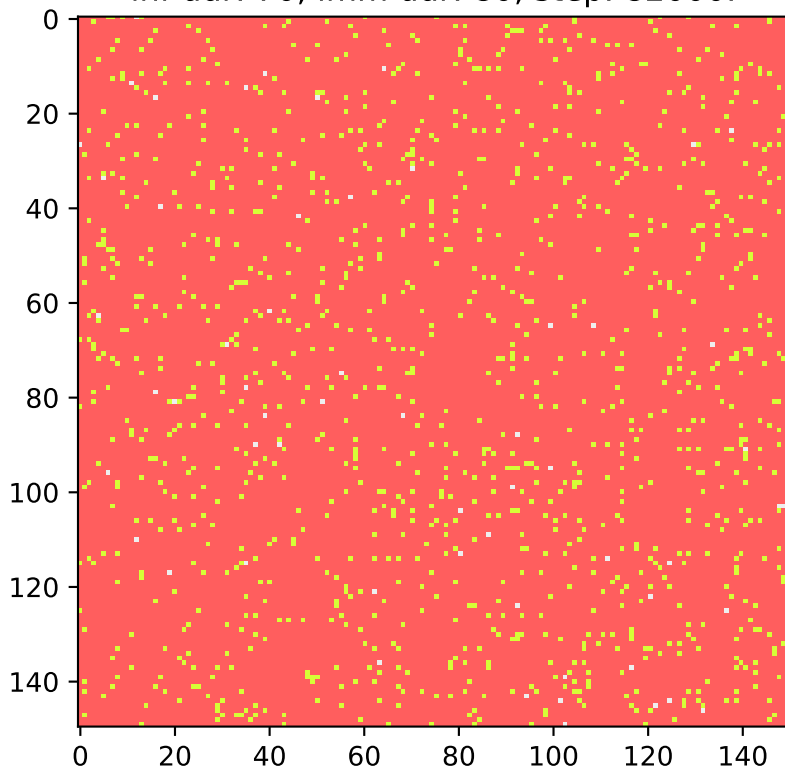
inf dur: 70, imm dur: 80, step: 80000.



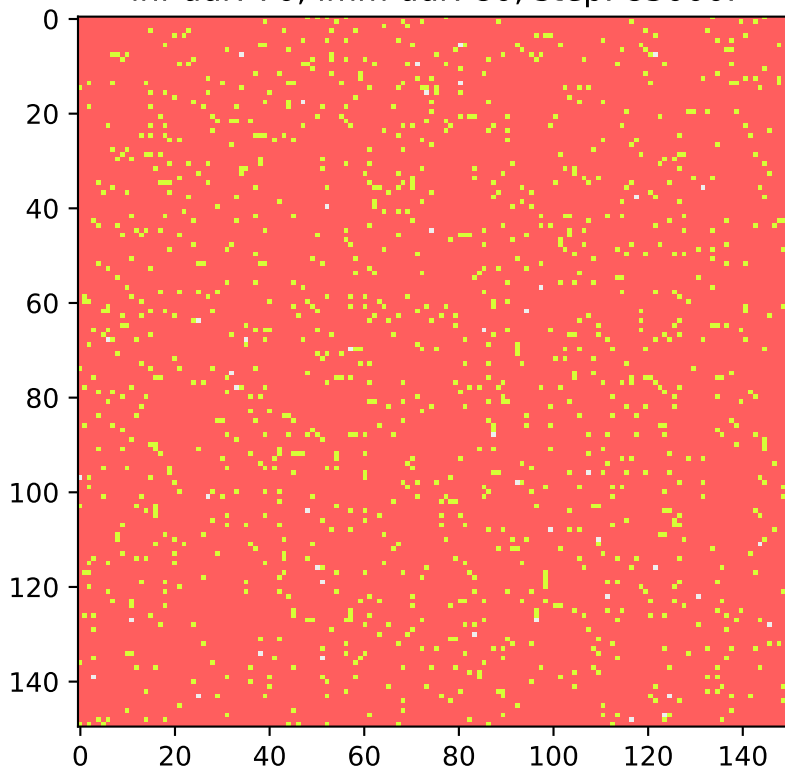
inf dur: 70, imm dur: 80, step: 81000.



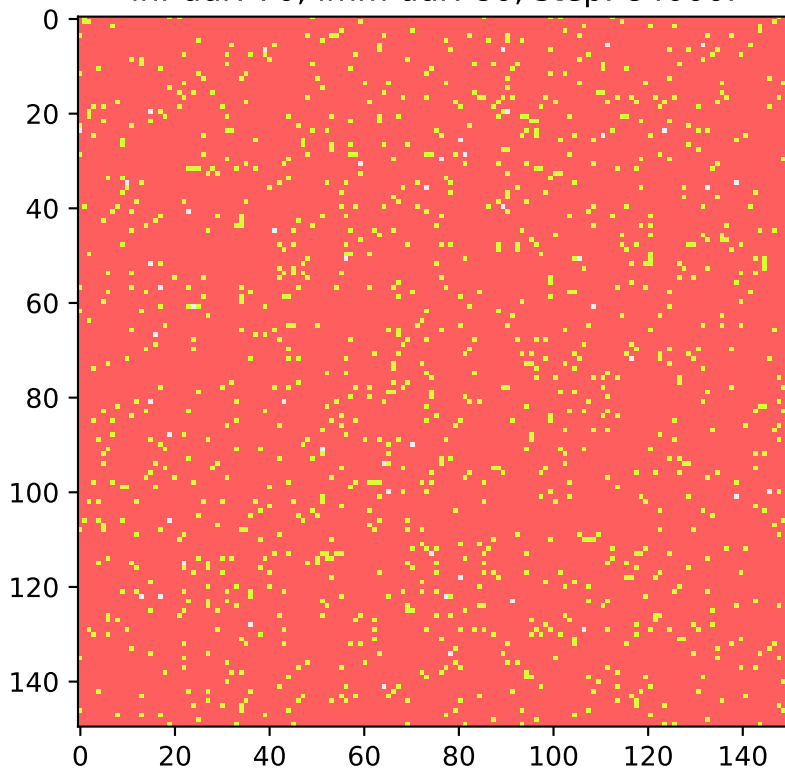
inf dur: 70, imm dur: 80, step: 82000.



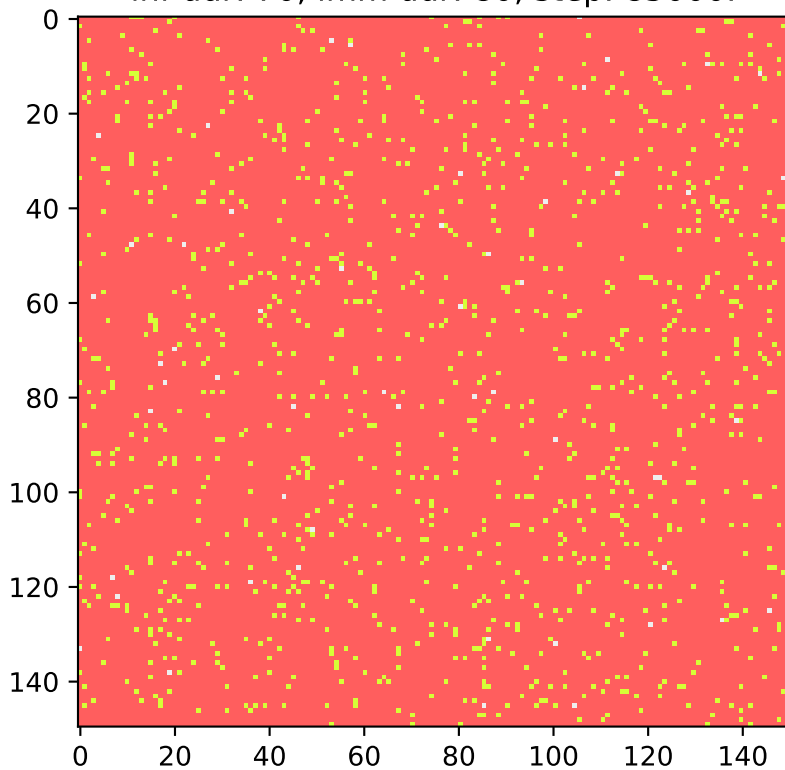
inf dur: 70, imm dur: 80, step: 83000.



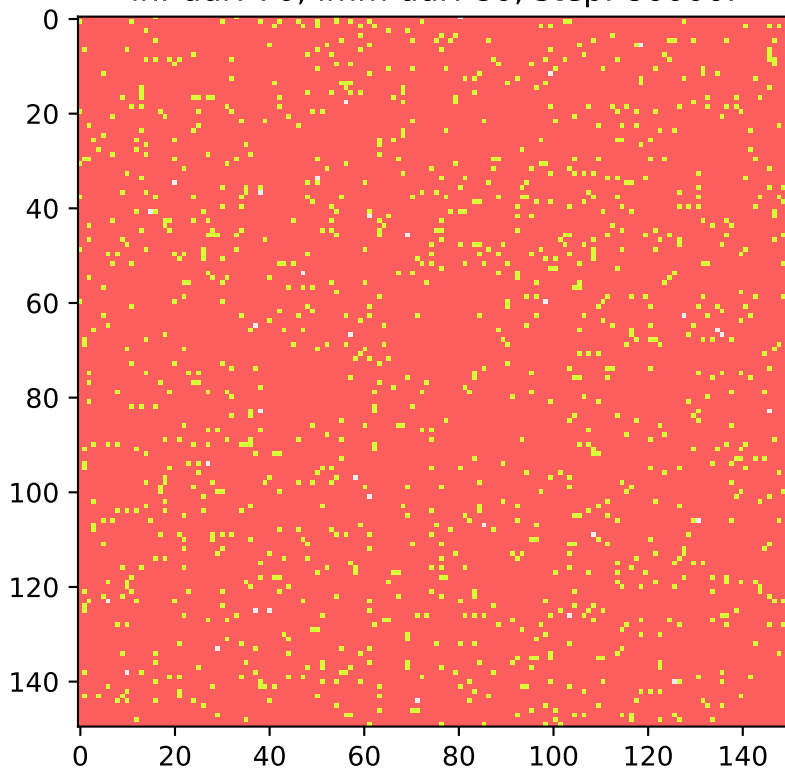
inf dur: 70, imm dur: 80, step: 84000.



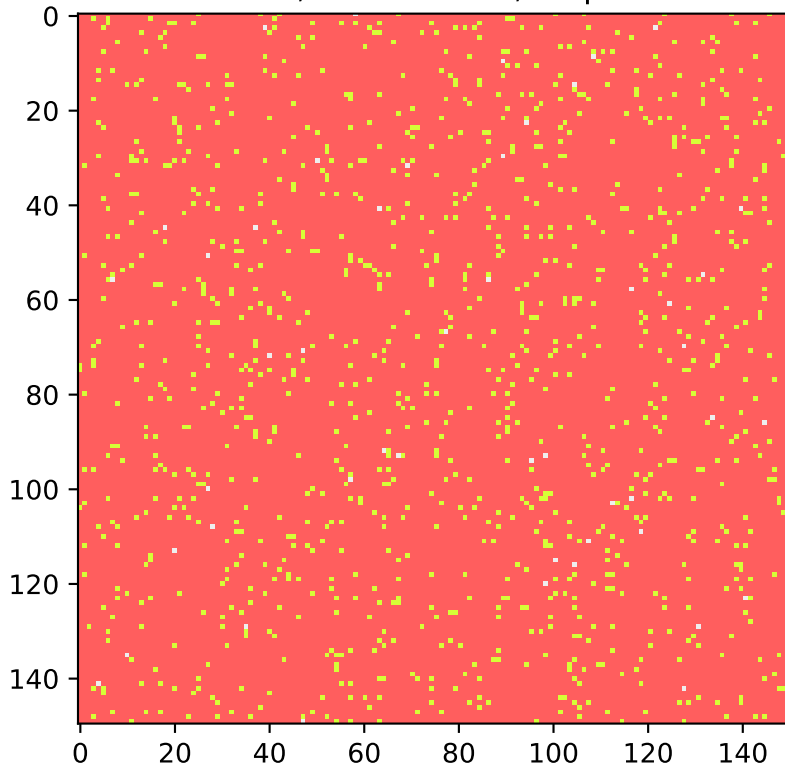
inf dur: 70, imm dur: 80, step: 85000.



inf dur: 70, imm dur: 80, step: 86000.

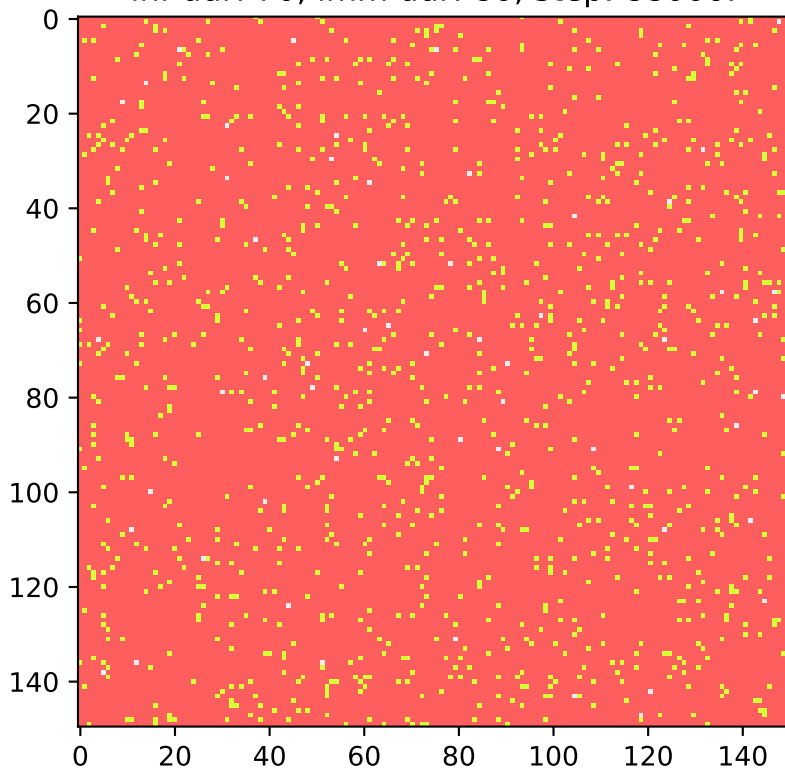


inf dur: 70, imm dur: 80, step: 87000.

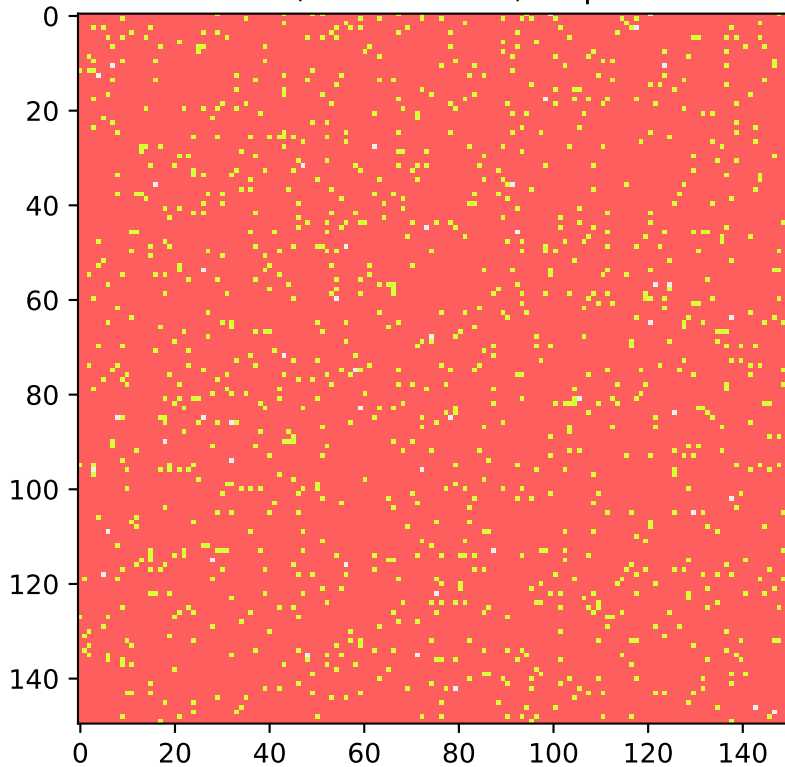




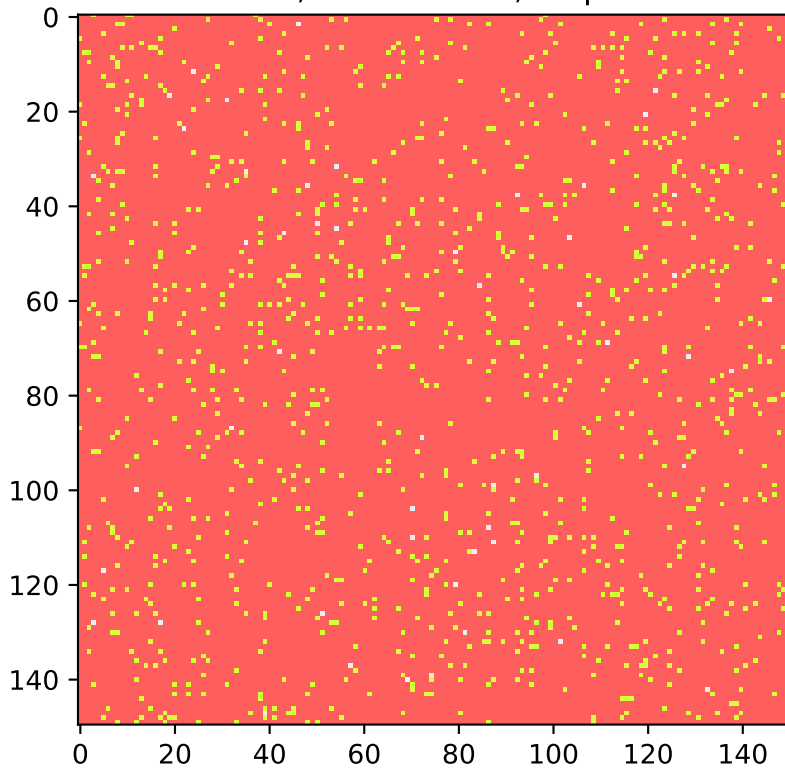
inf dur: 70, imm dur: 80, step: 88000.



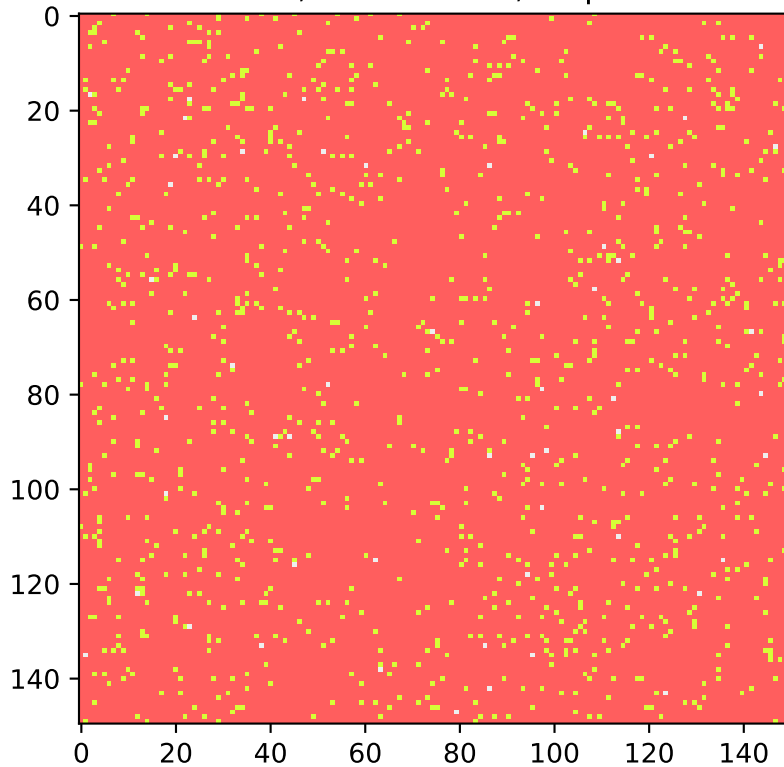
inf dur: 70, imm dur: 80, step: 89000.



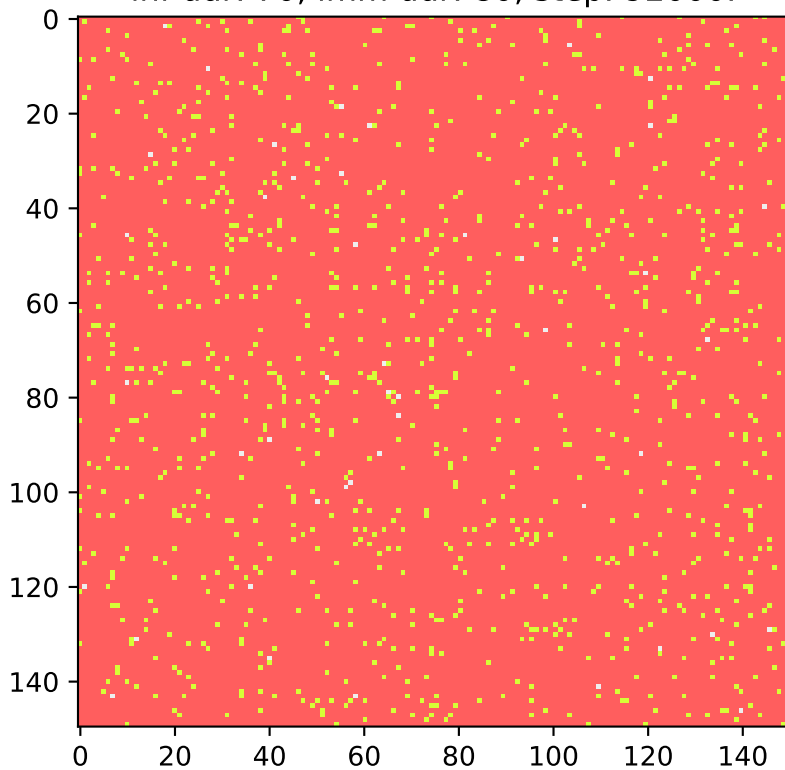
inf dur: 70, imm dur: 80, step: 90000.



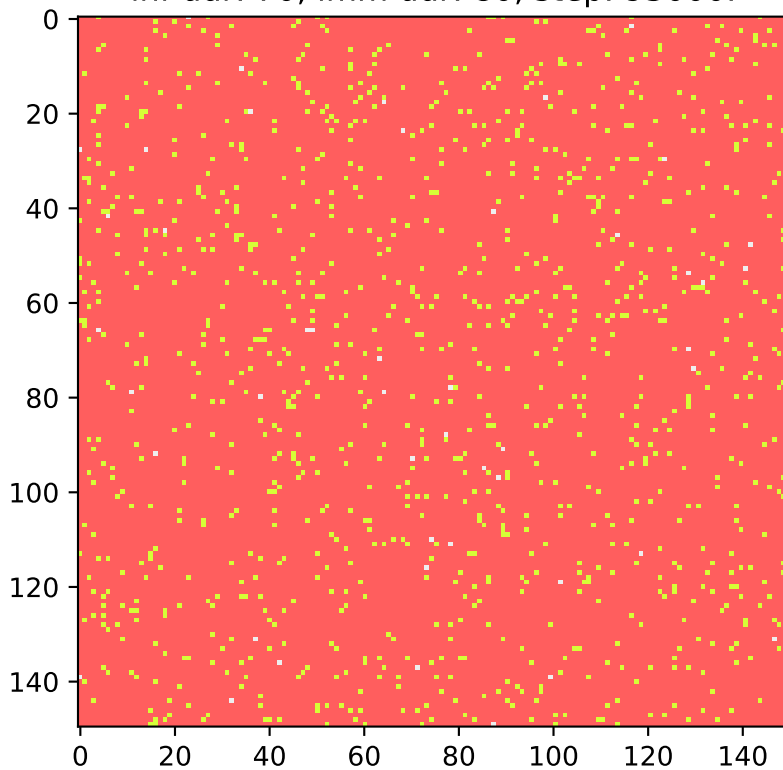
inf dur: 70, imm dur: 80, step: 91000.



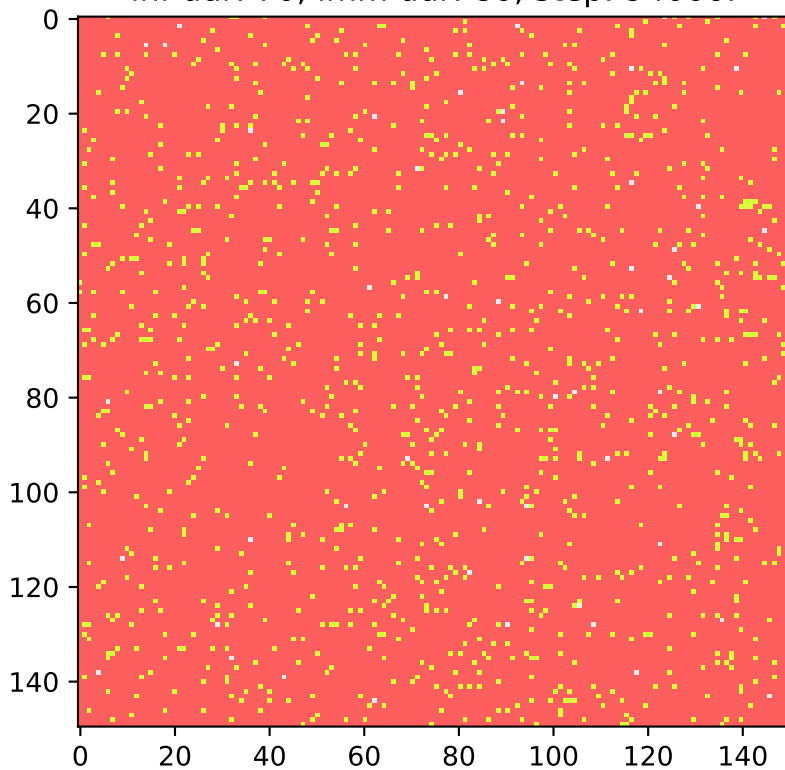
inf dur: 70, imm dur: 80, step: 92000.



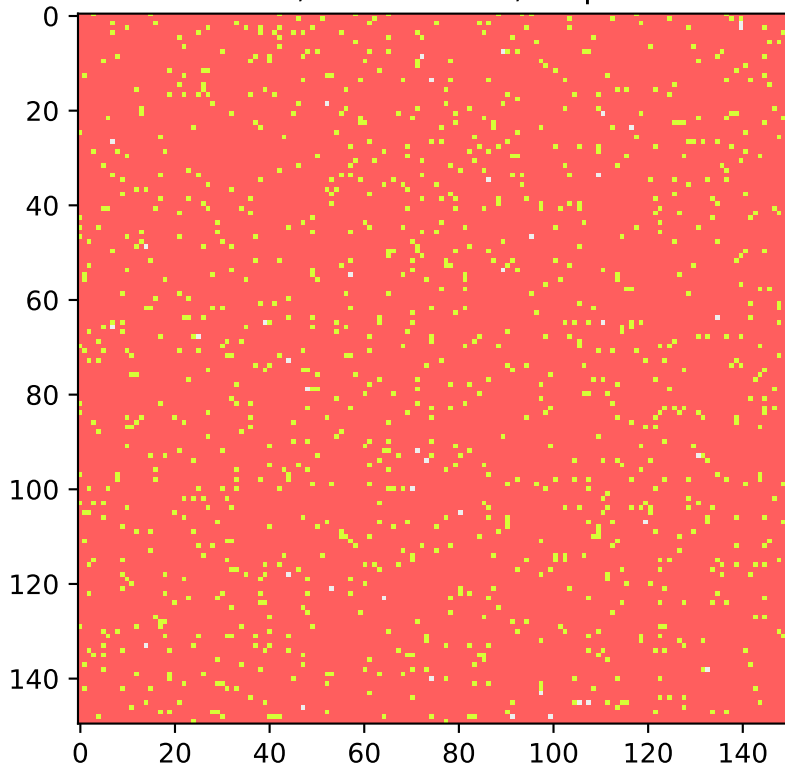
inf dur: 70, imm dur: 80, step: 93000.



inf dur: 70, imm dur: 80, step: 94000.

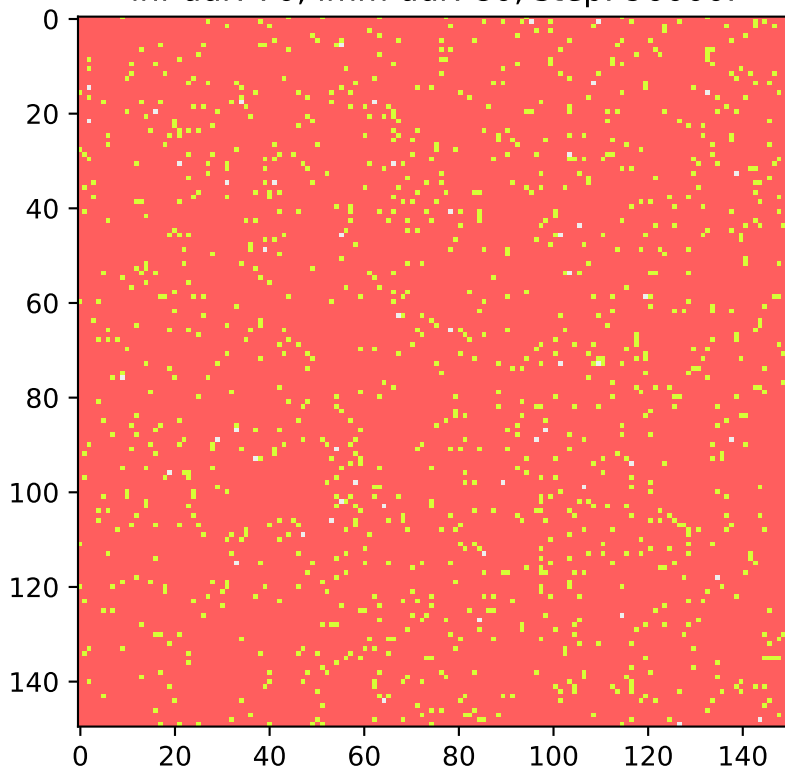


inf dur: 70, imm dur: 80, step: 95000.

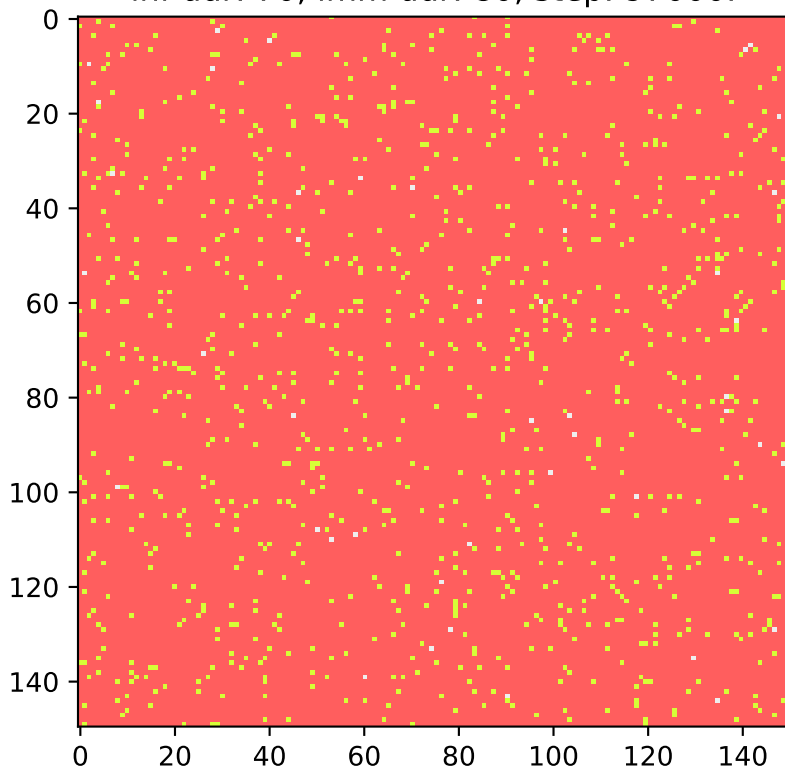




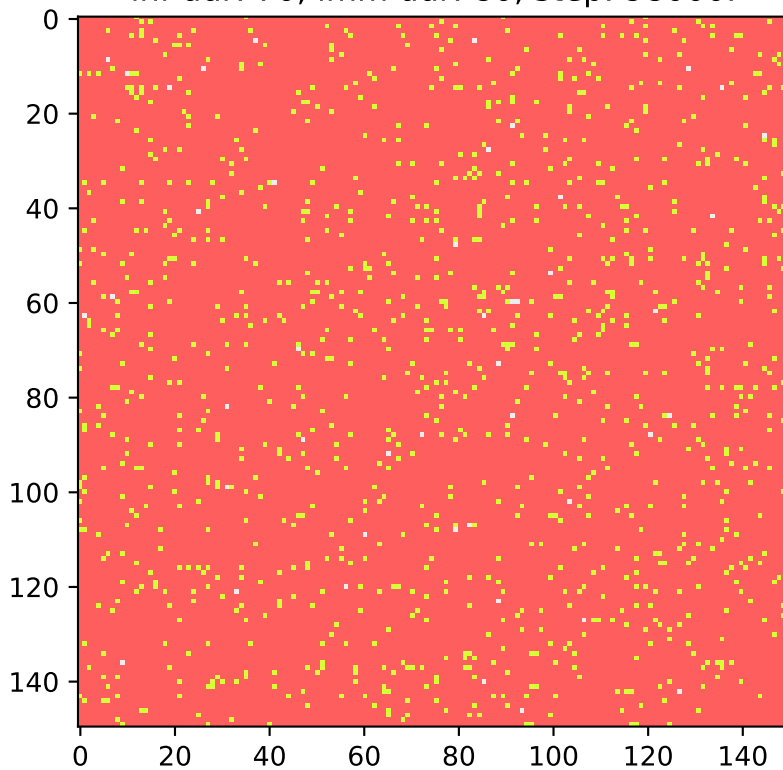
inf dur: 70, imm dur: 80, step: 96000.



inf dur: 70, imm dur: 80, step: 97000.



inf dur: 70, imm dur: 80, step: 98000.



inf dur: 70, imm dur: 80, step: 99000.

