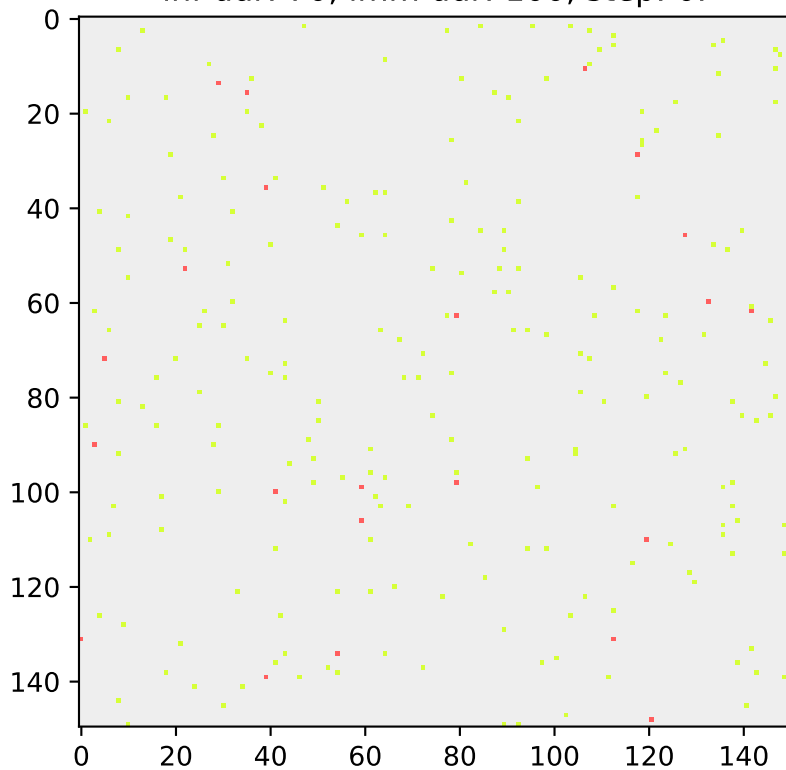
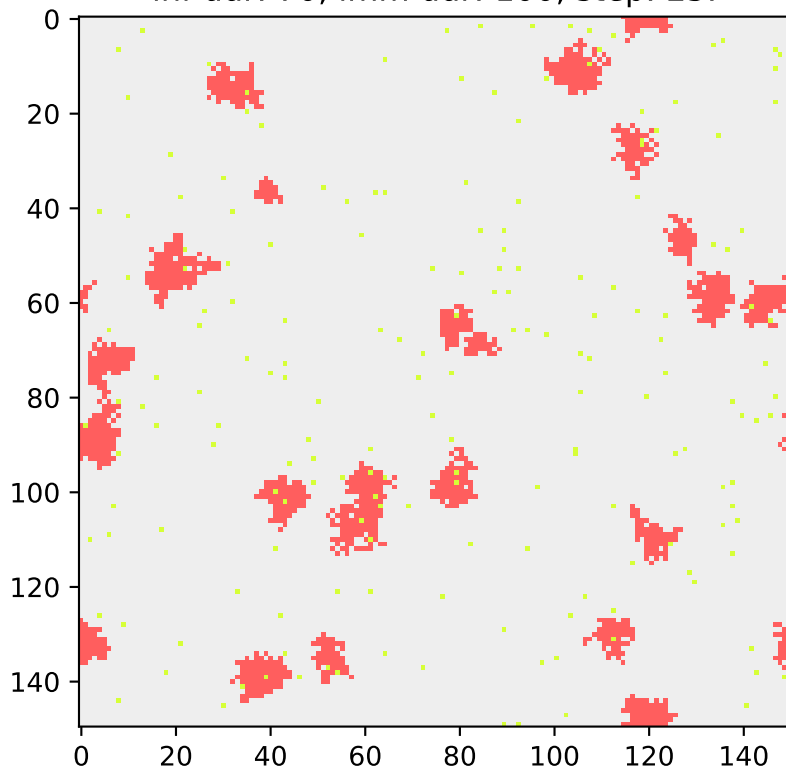


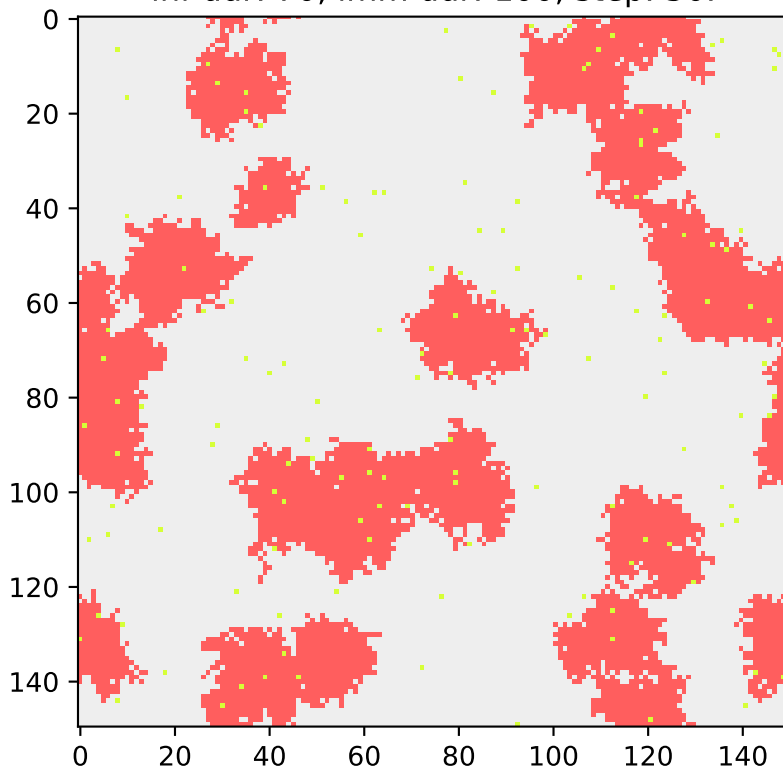
inf dur: 70, imm dur: 100, step: 0.



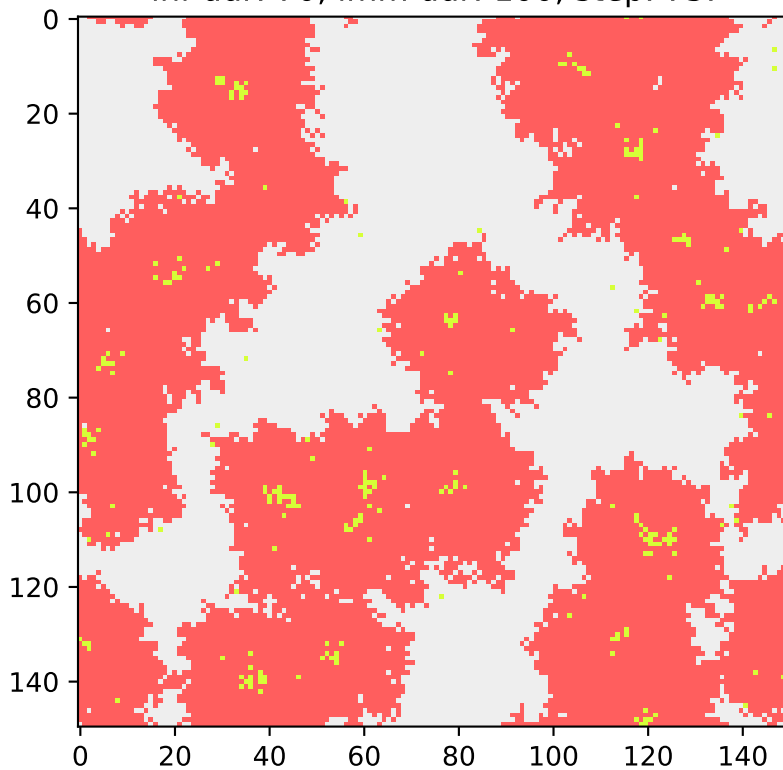
inf dur: 70, imm dur: 100, step: 25.



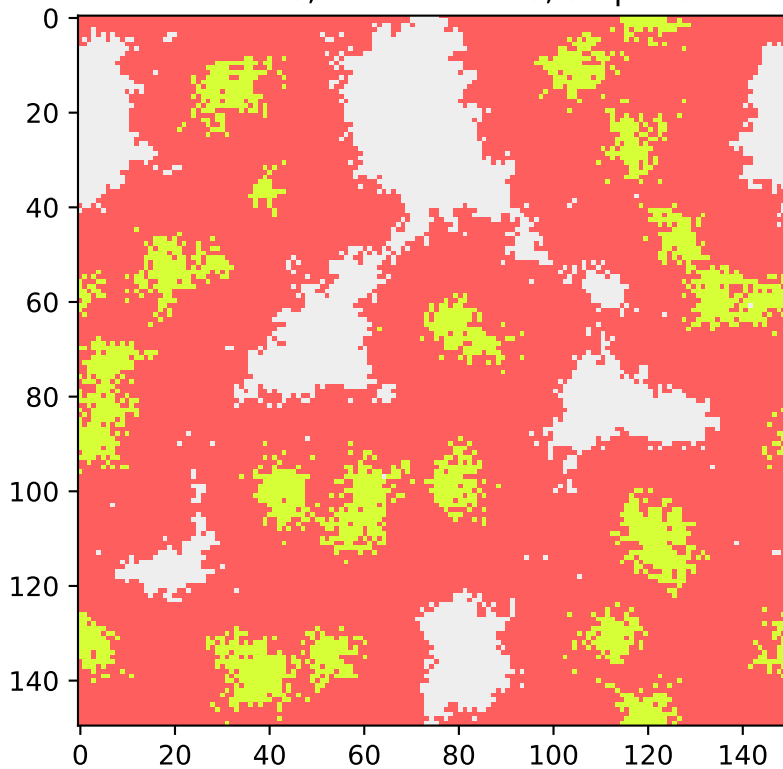
inf dur: 70, imm dur: 100, step: 50.



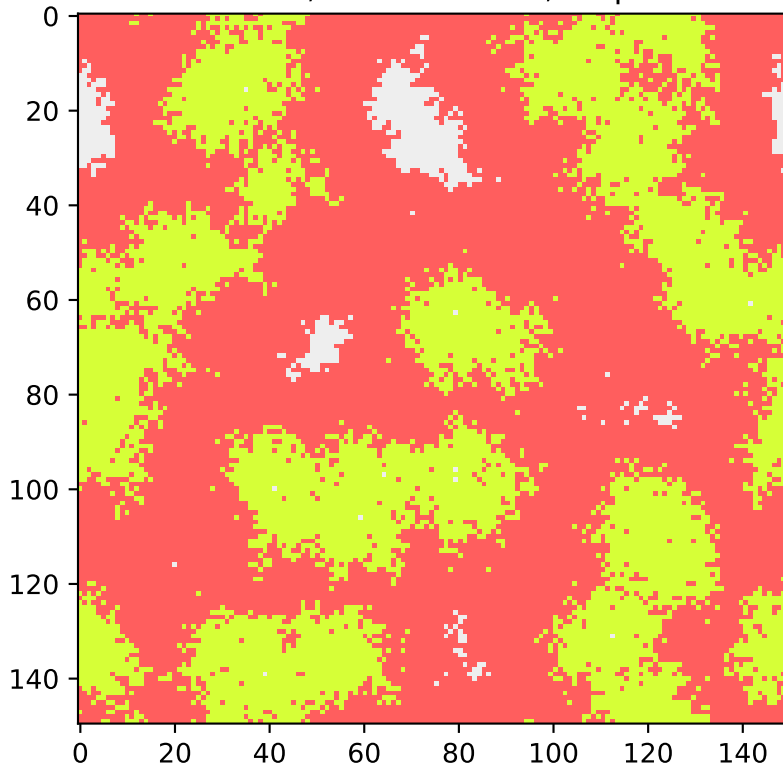
inf dur: 70, imm dur: 100, step: 75.



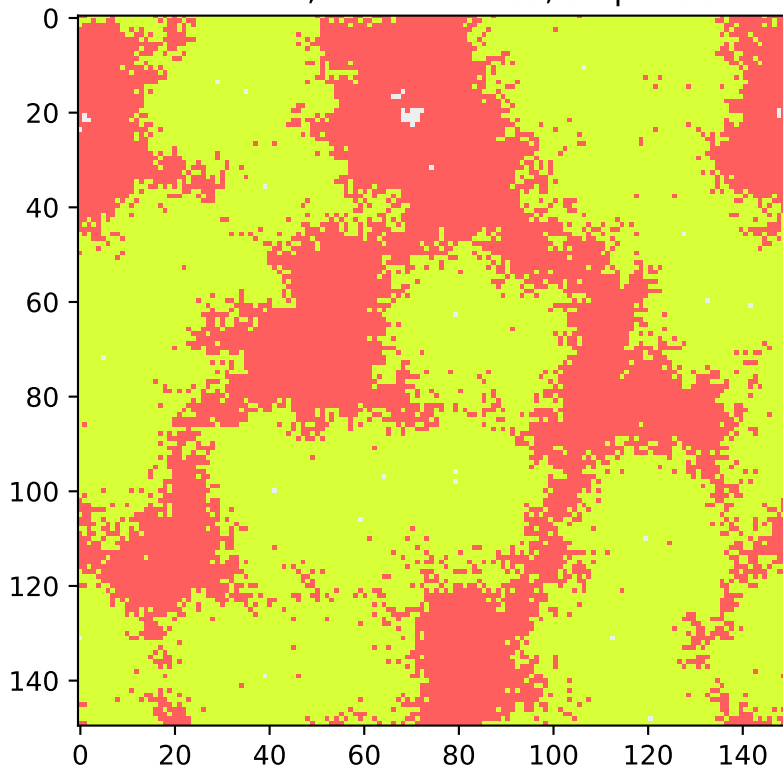
inf dur: 70, imm dur: 100, step: 100.



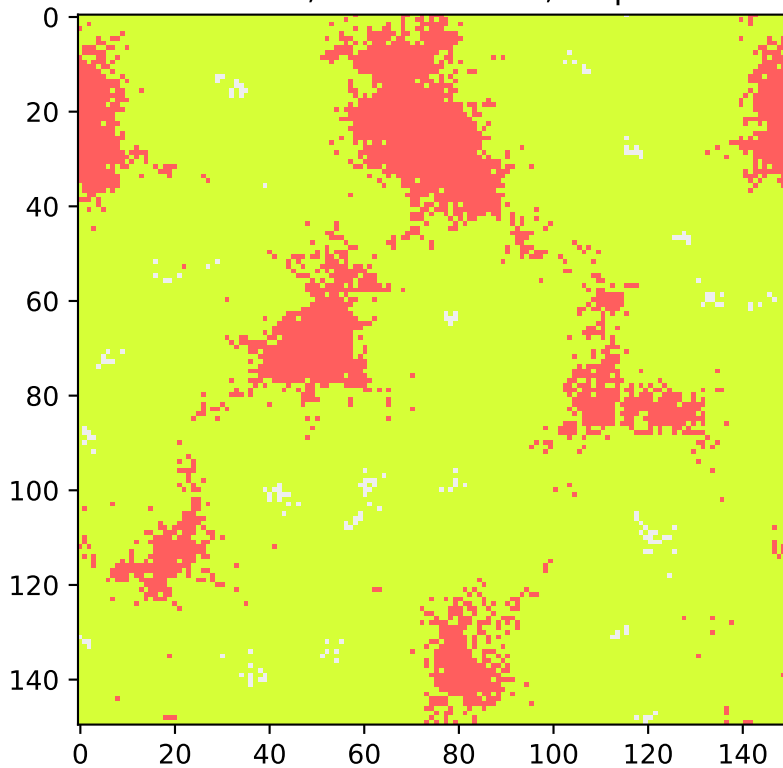
inf dur: 70, imm dur: 100, step: 125.



inf dur: 70, imm dur: 100, step: 150.

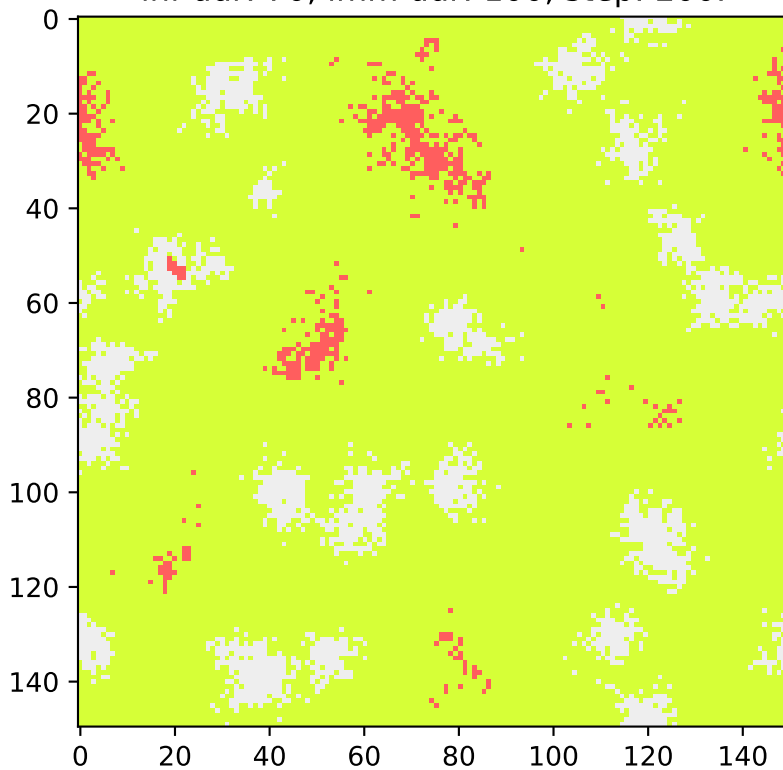


inf dur: 70, imm dur: 100, step: 175.

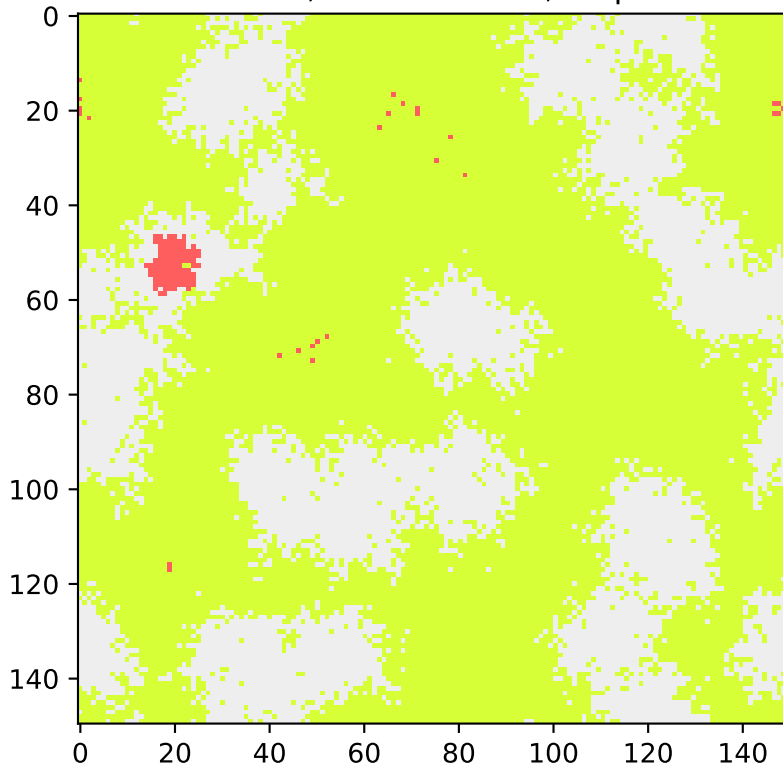




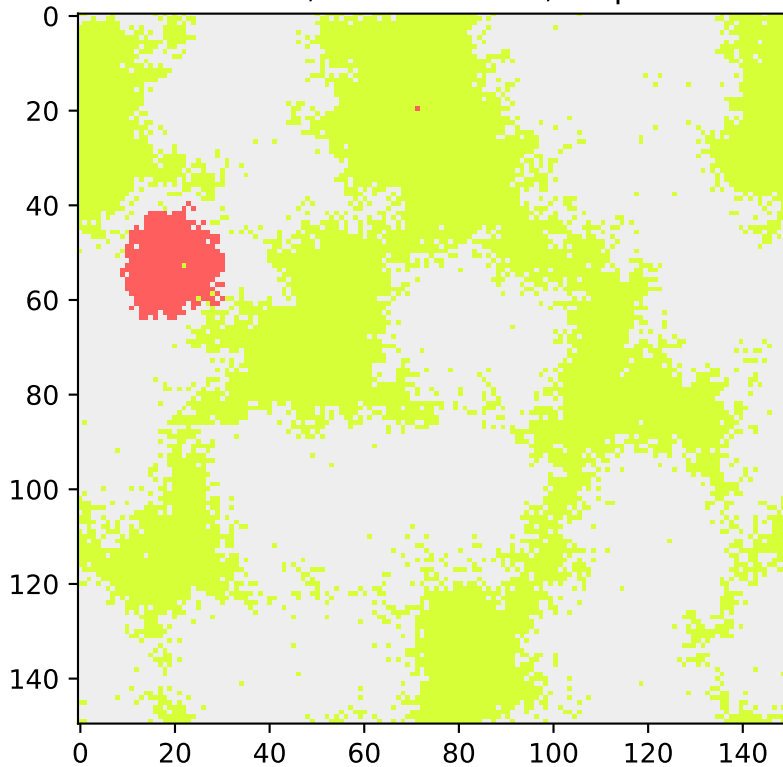
inf dur: 70, imm dur: 100, step: 200.



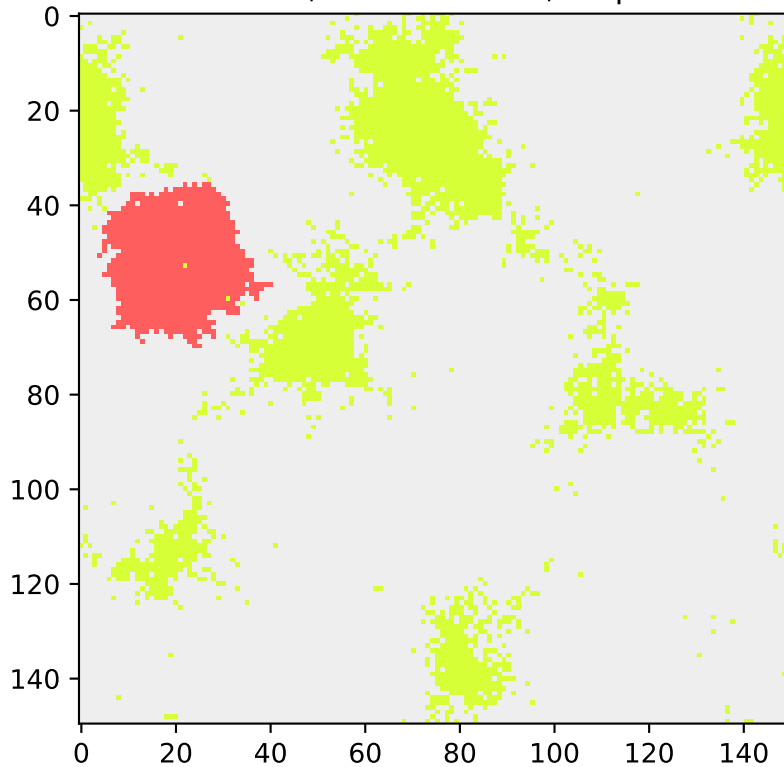
inf dur: 70, imm dur: 100, step: 225.



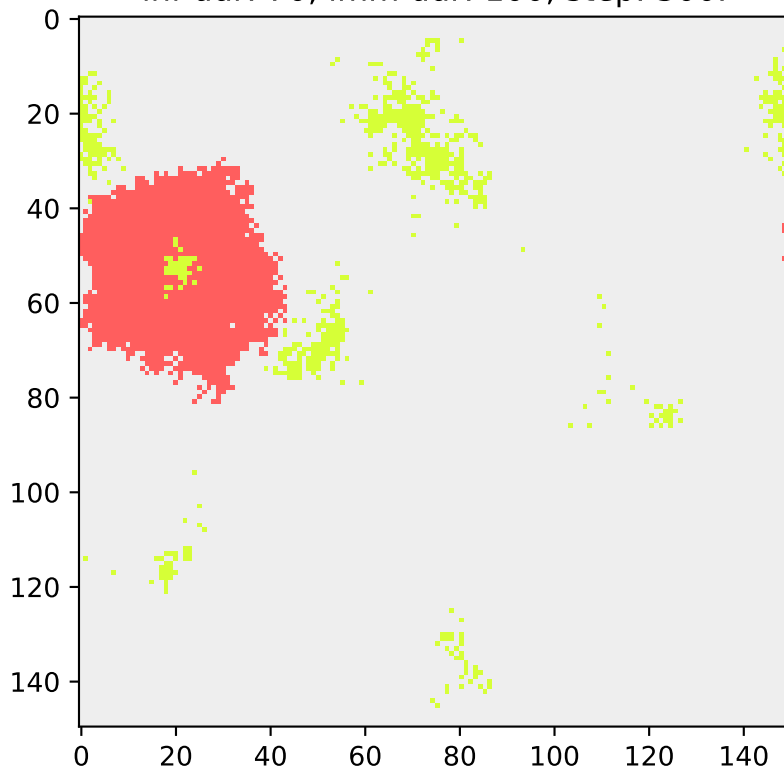
inf dur: 70, imm dur: 100, step: 250.



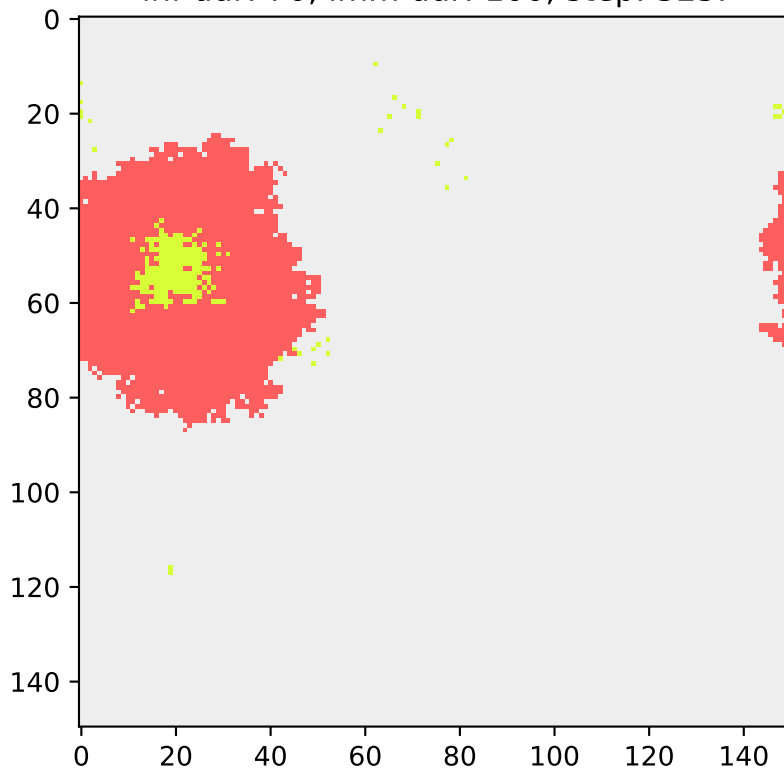
inf dur: 70, imm dur: 100, step: 275.



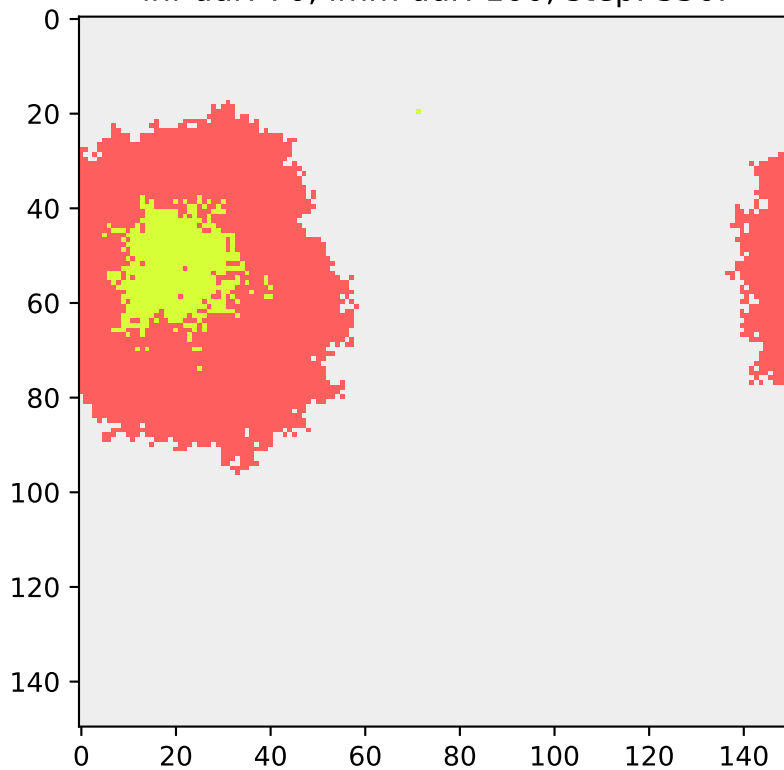
inf dur: 70, imm dur: 100, step: 300.



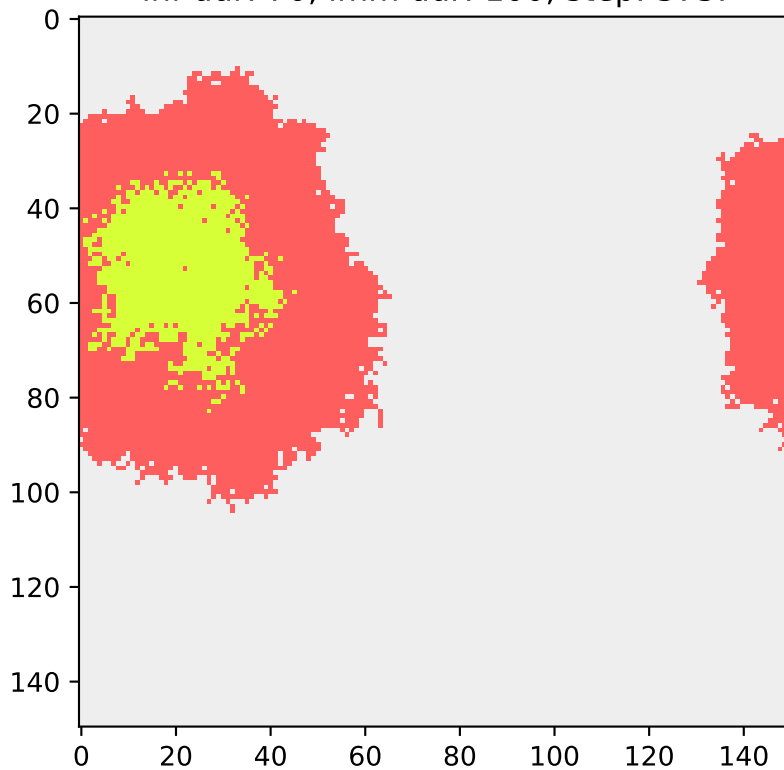
inf dur: 70, imm dur: 100, step: 325.



inf dur: 70, imm dur: 100, step: 350.

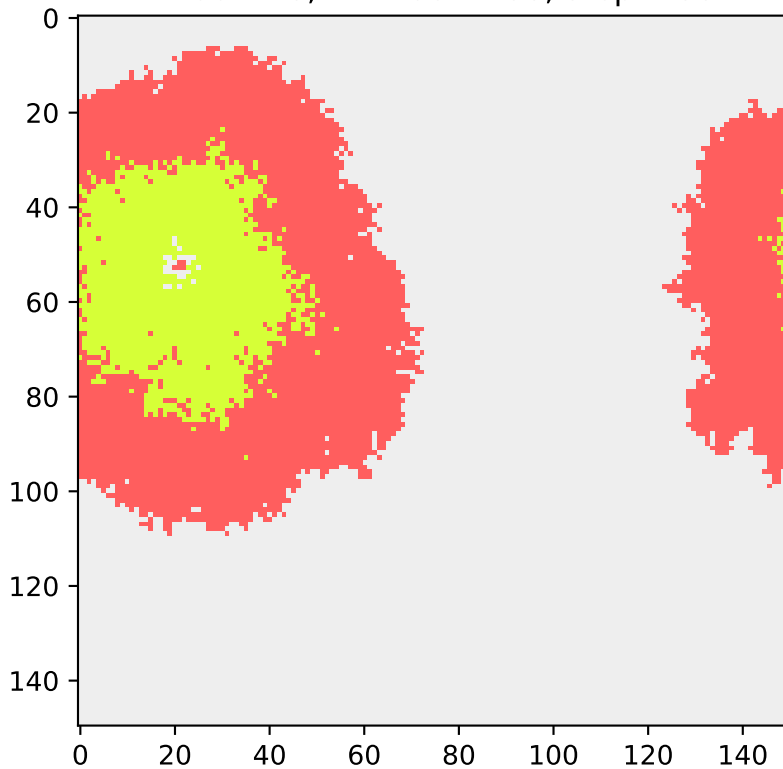


inf dur: 70, imm dur: 100, step: 375.

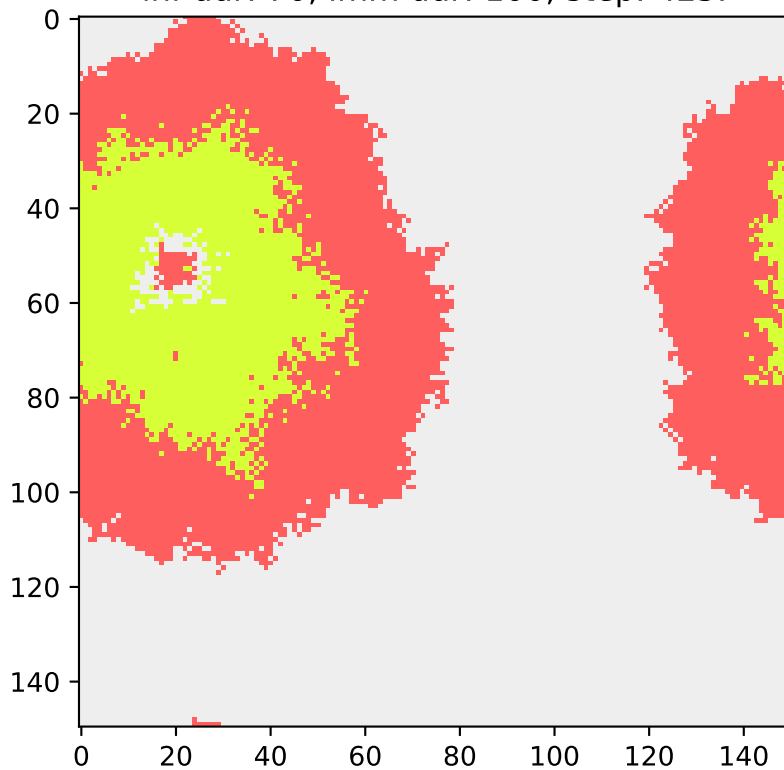




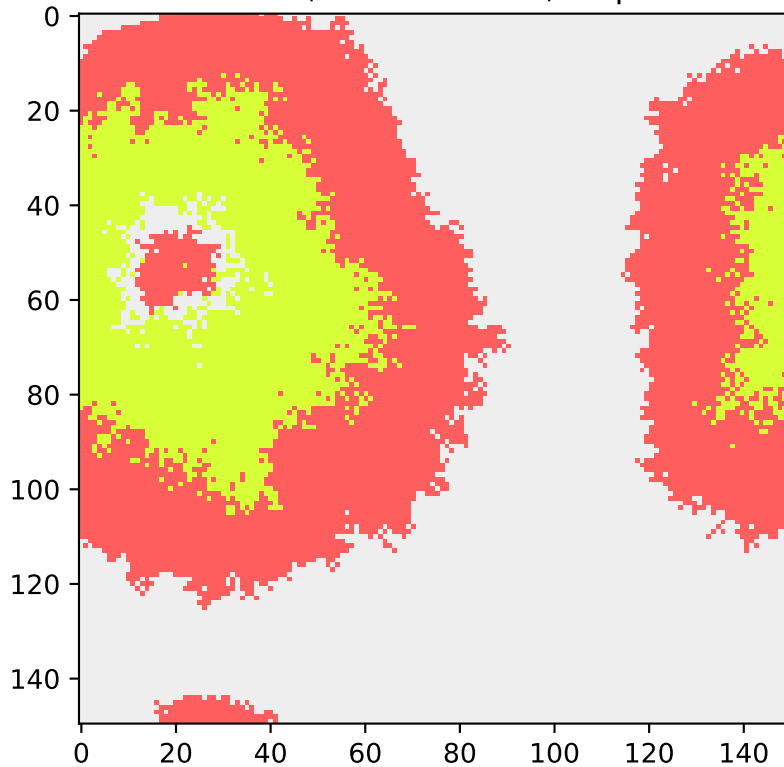
inf dur: 70, imm dur: 100, step: 400.



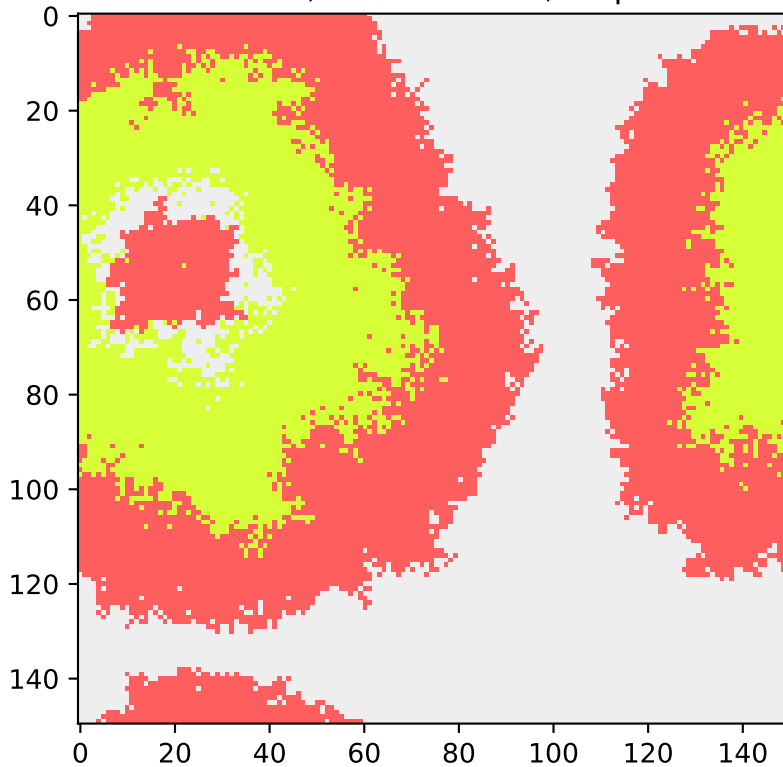
inf dur: 70, imm dur: 100, step: 425.



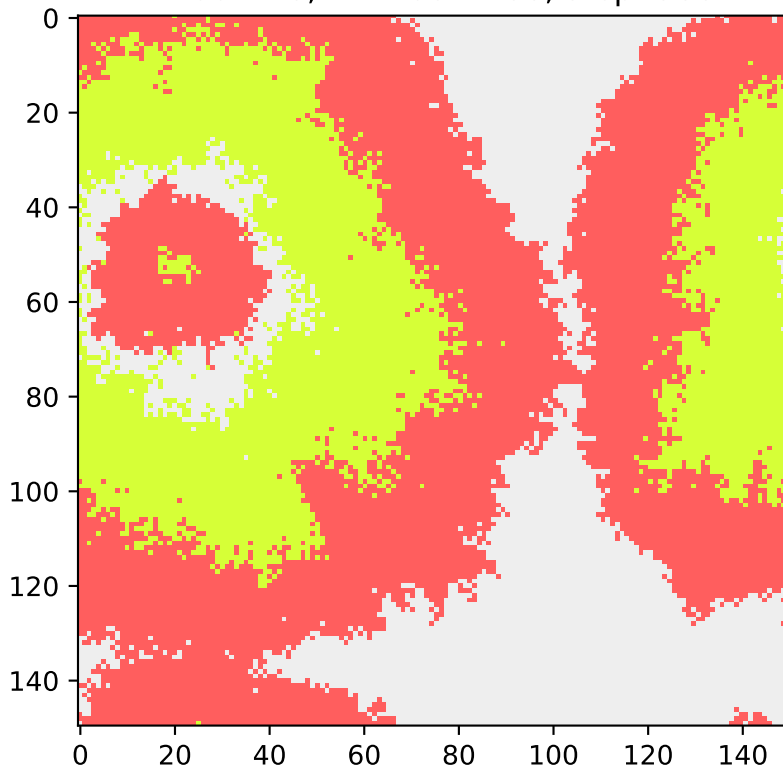
inf dur: 70, imm dur: 100, step: 450.



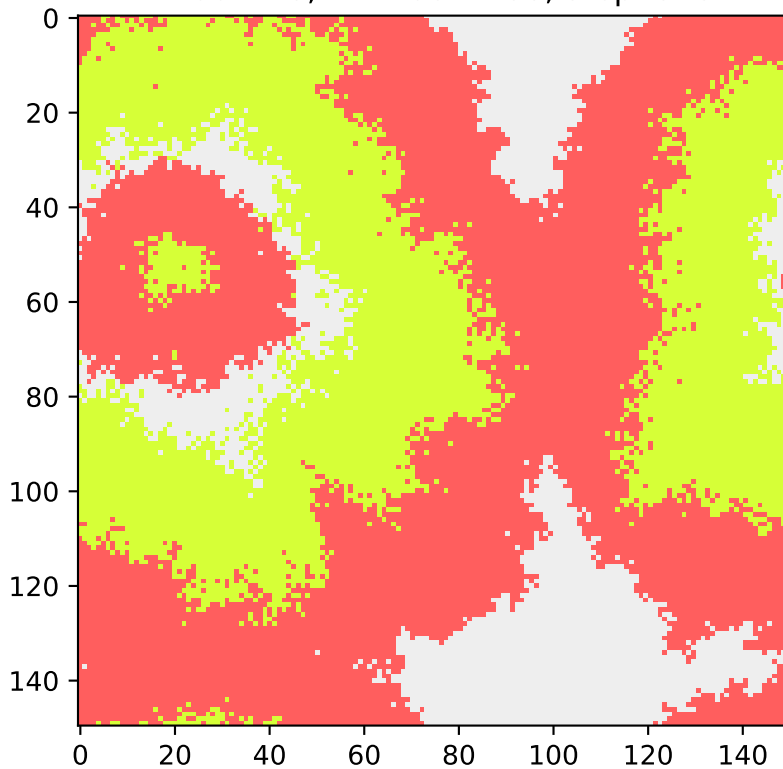
inf dur: 70, imm dur: 100, step: 475.



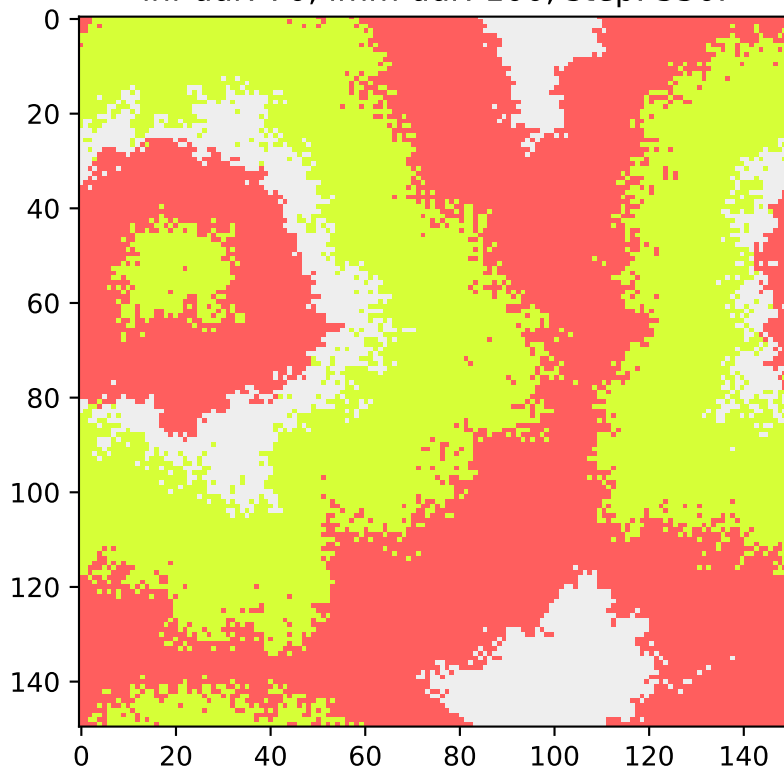
inf dur: 70, imm dur: 100, step: 500.



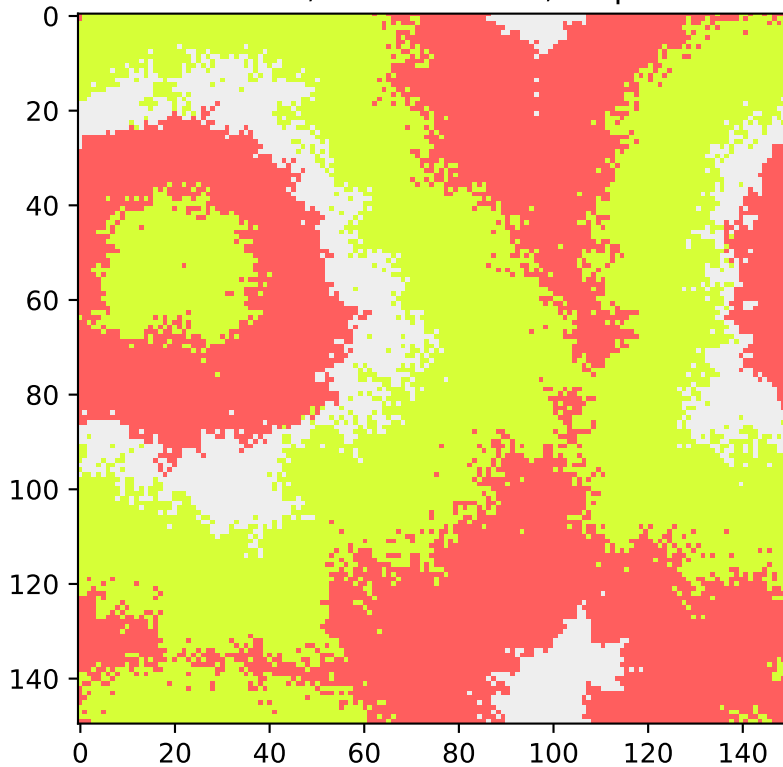
inf dur: 70, imm dur: 100, step: 525.



inf dur: 70, imm dur: 100, step: 550.

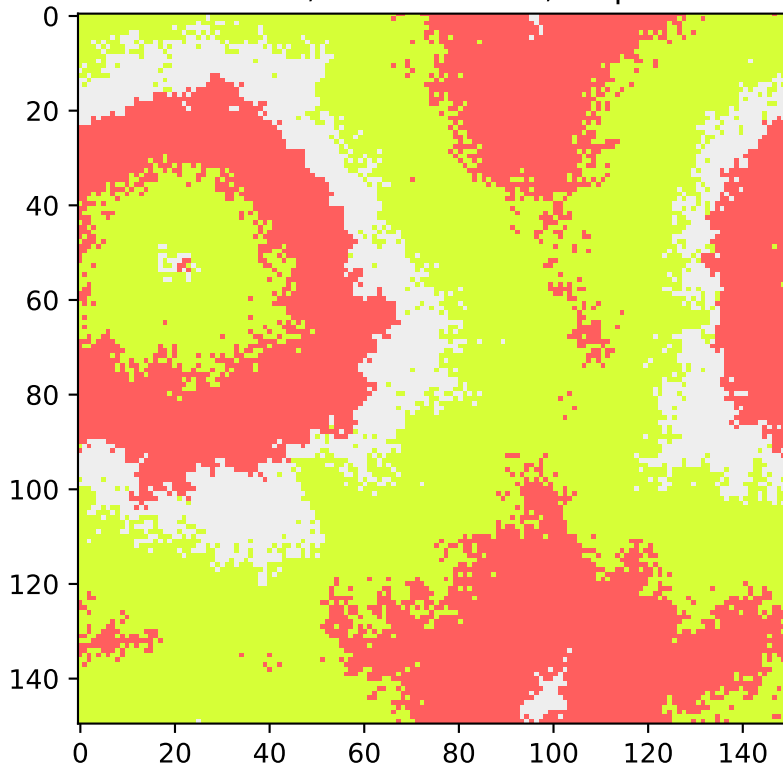


inf dur: 70, imm dur: 100, step: 575.

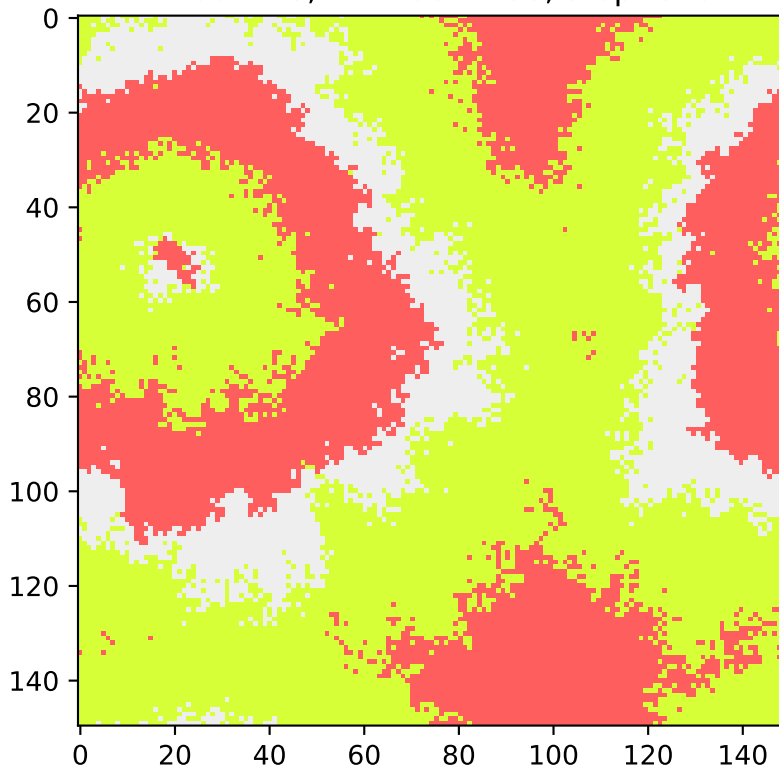




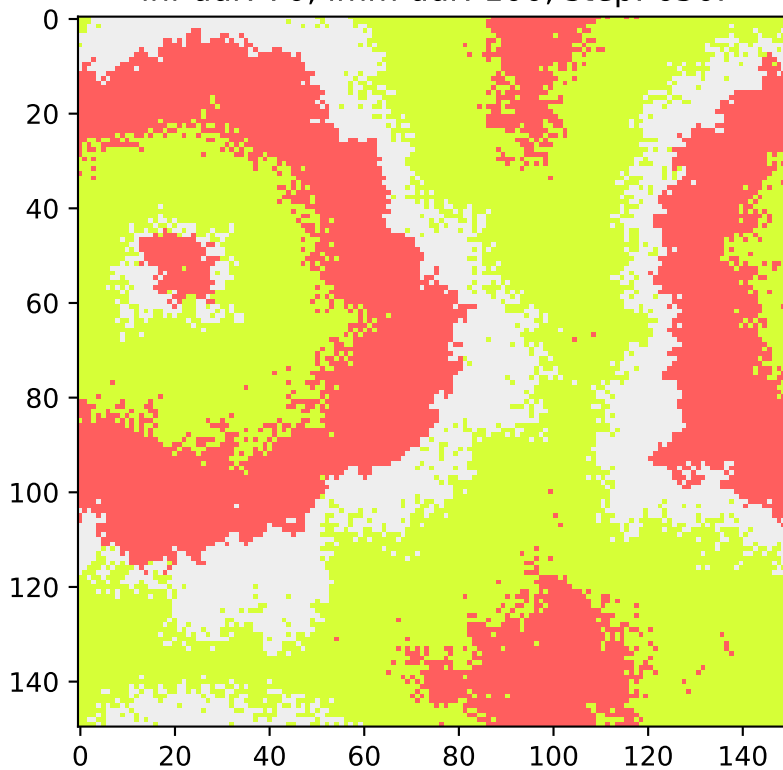
inf dur: 70, imm dur: 100, step: 600.



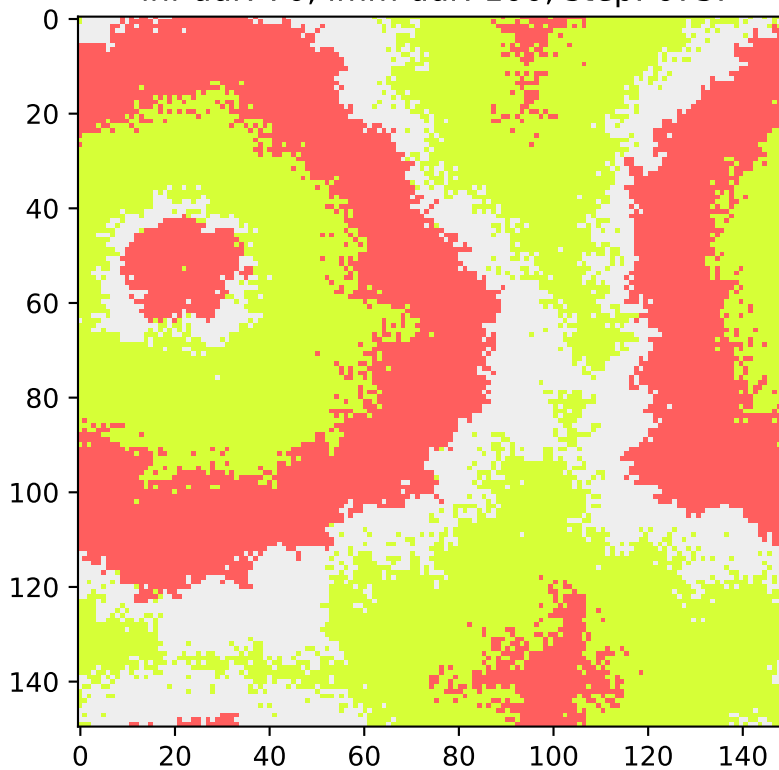
inf dur: 70, imm dur: 100, step: 625.



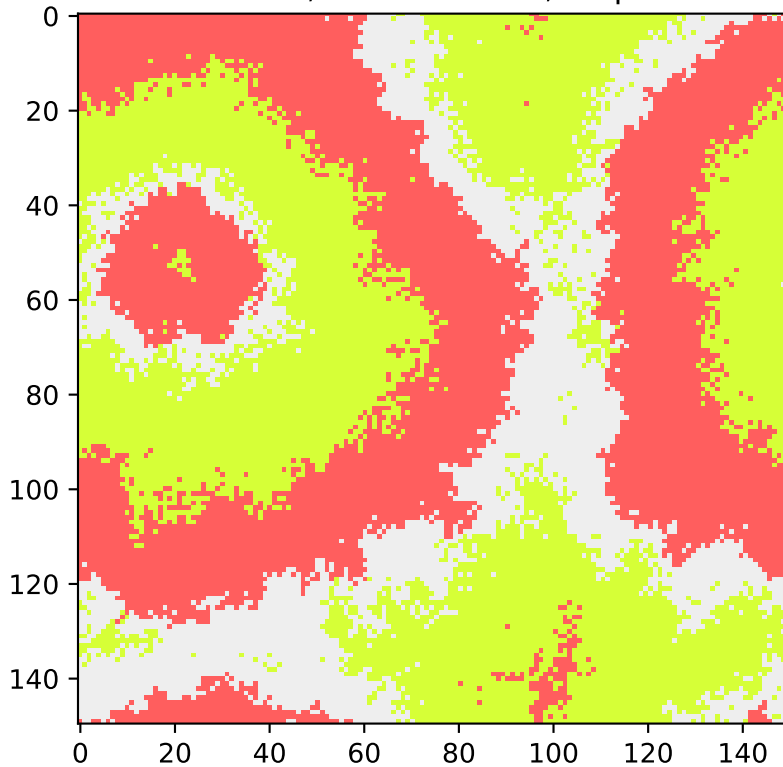
inf dur: 70, imm dur: 100, step: 650.



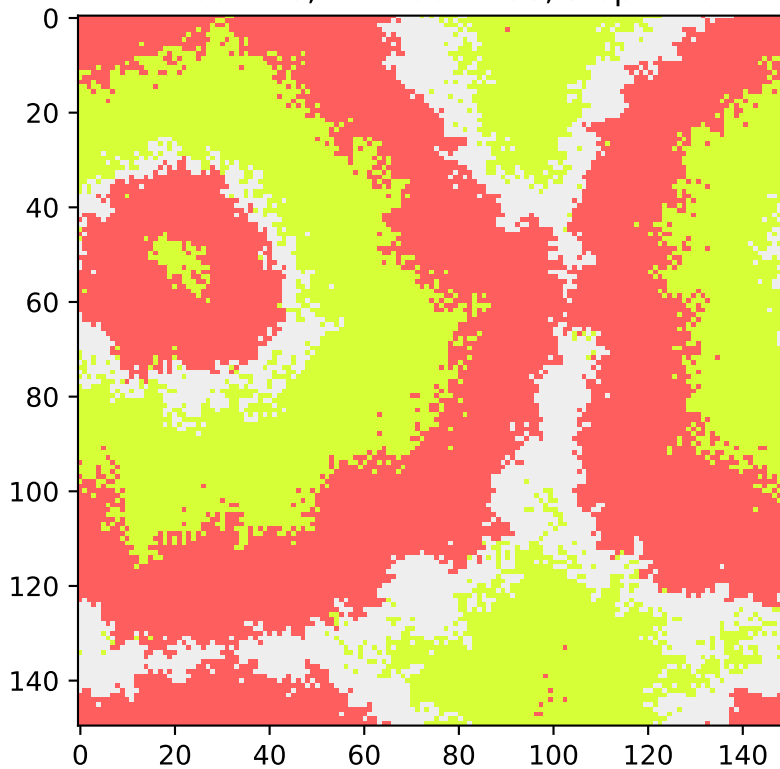
inf dur: 70, imm dur: 100, step: 675.



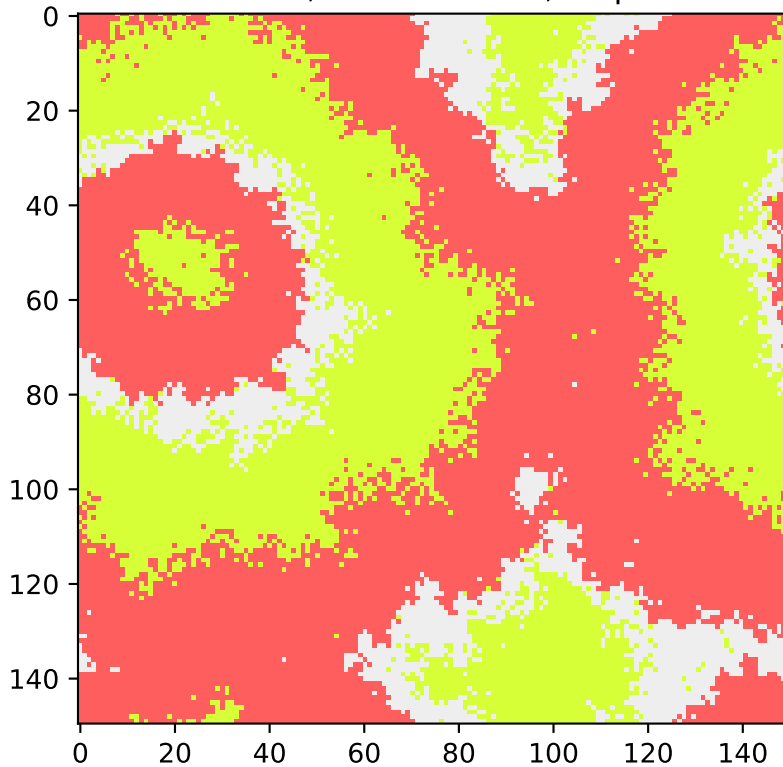
inf dur: 70, imm dur: 100, step: 700.



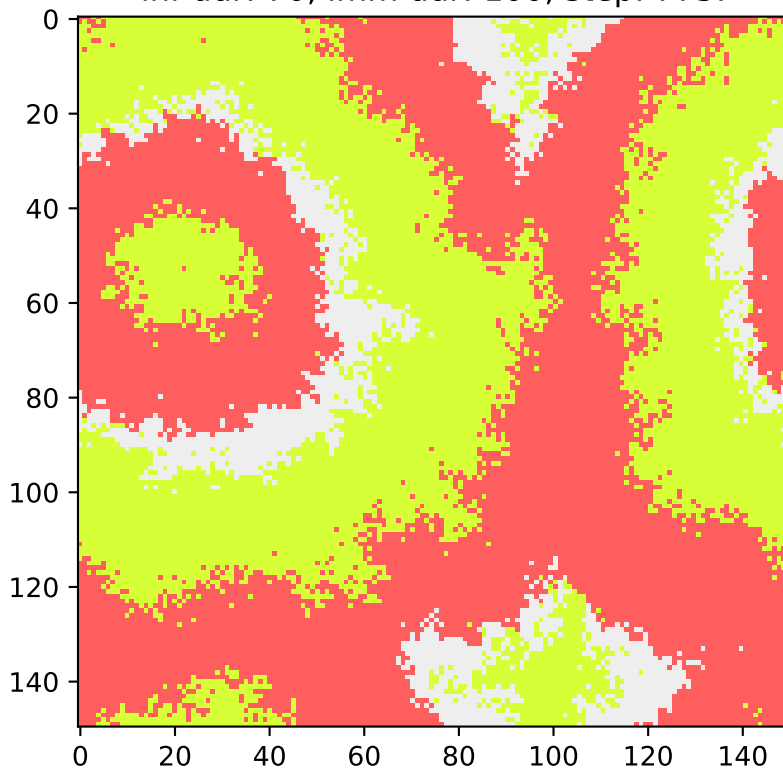
inf dur: 70, imm dur: 100, step: 725.



inf dur: 70, imm dur: 100, step: 750.

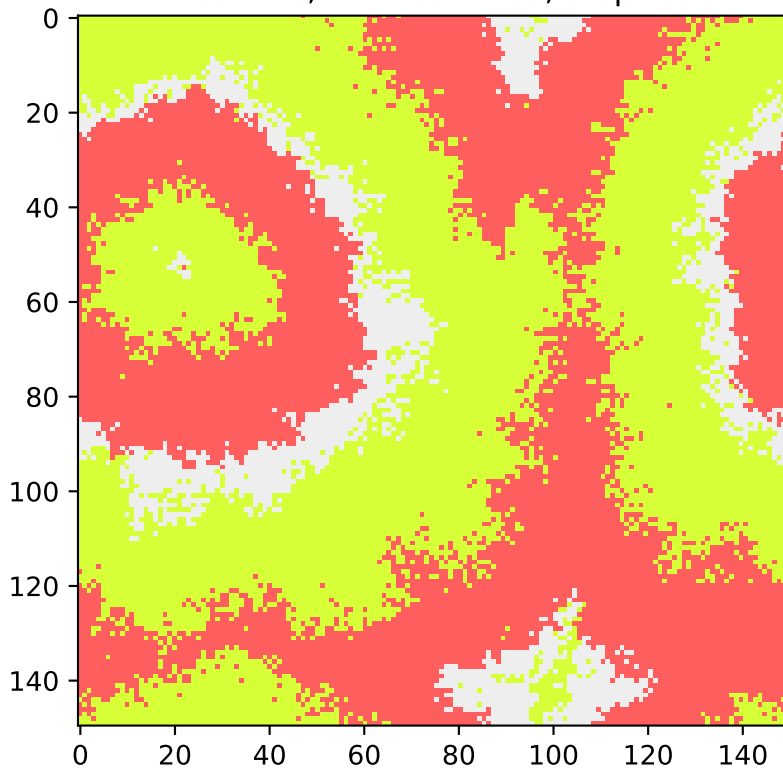


inf dur: 70, imm dur: 100, step: 775.

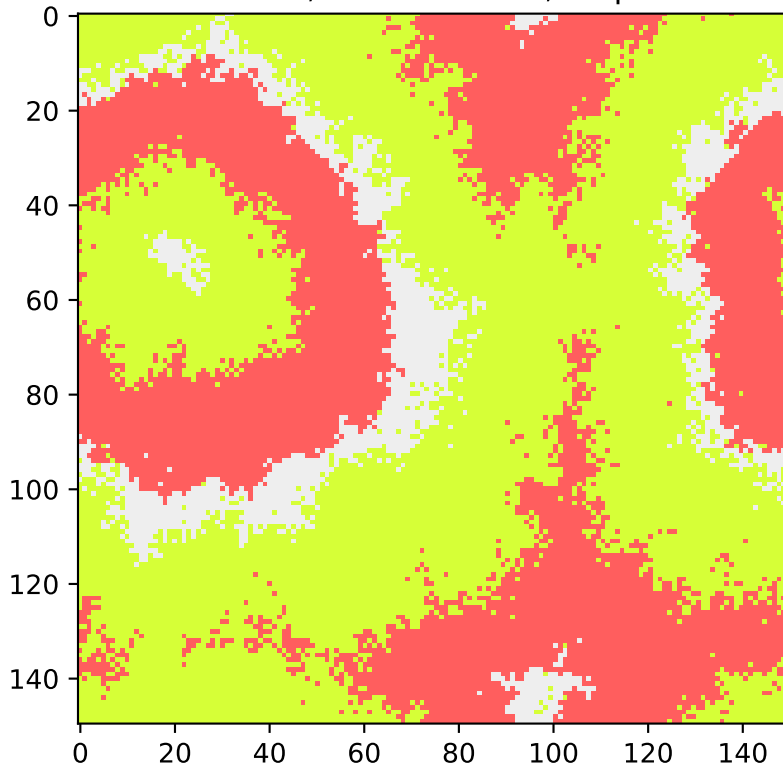




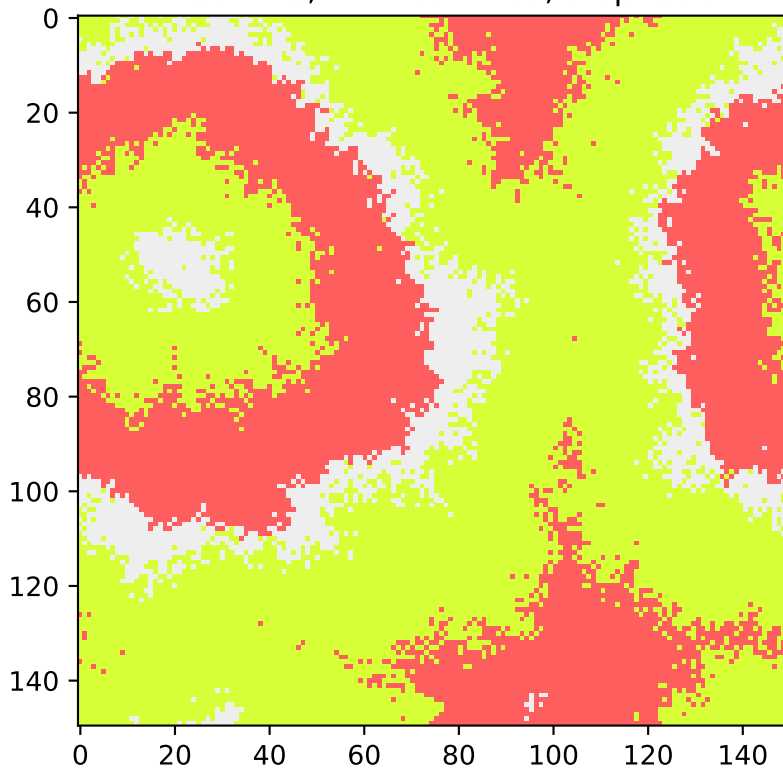
inf dur: 70, imm dur: 100, step: 800.



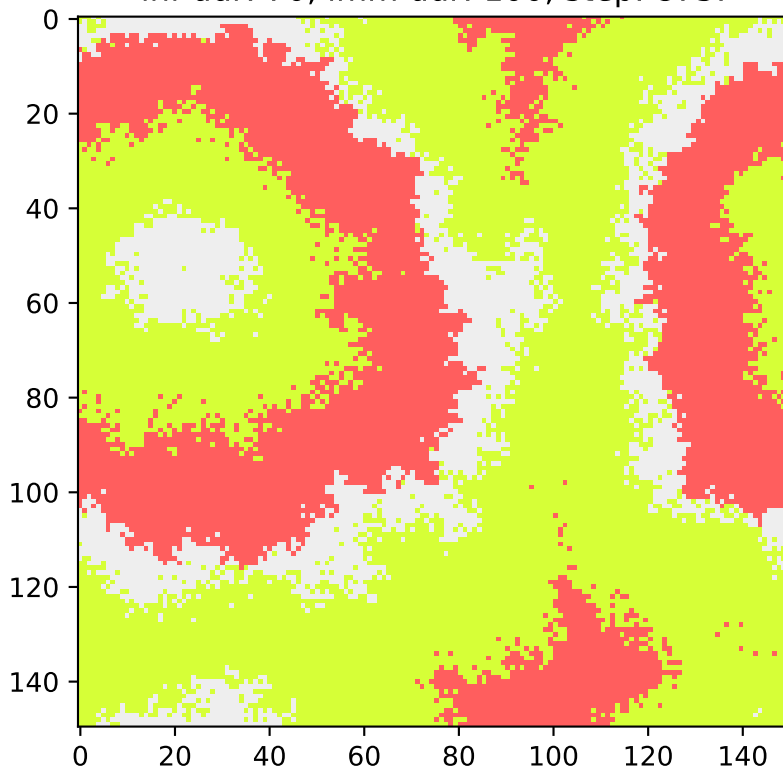
inf dur: 70, imm dur: 100, step: 825.



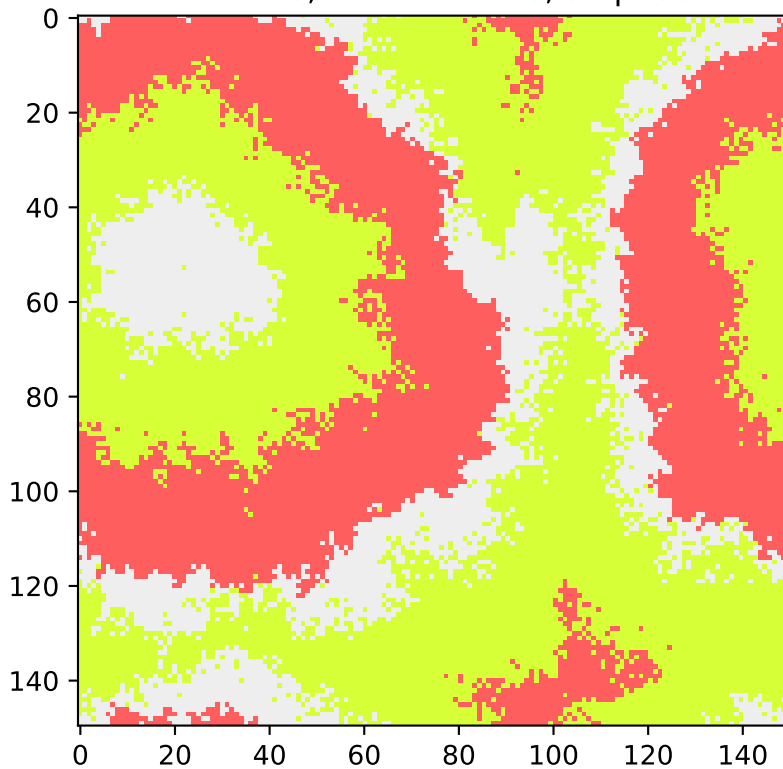
inf dur: 70, imm dur: 100, step: 850.



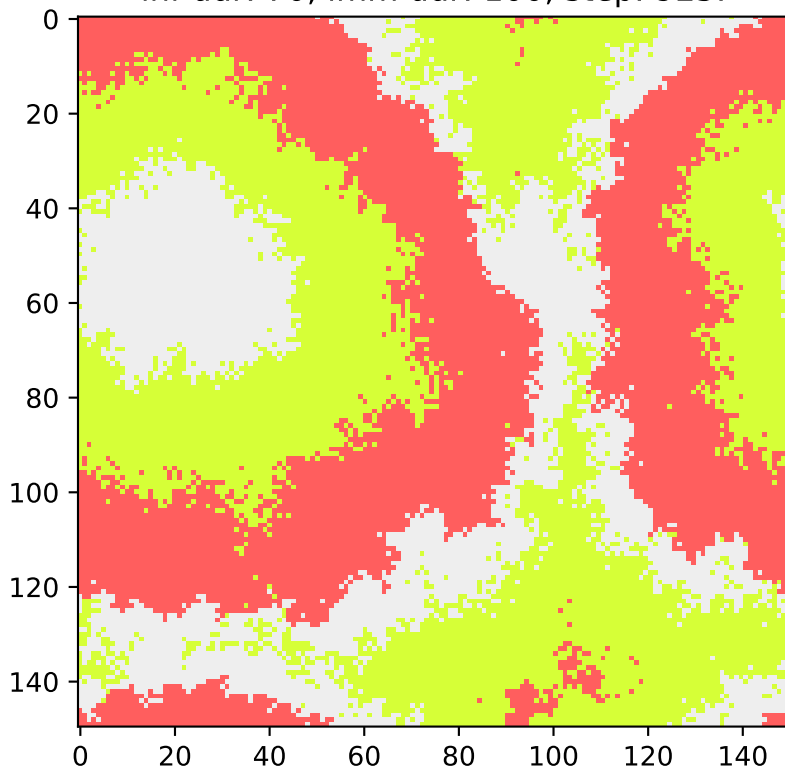
inf dur: 70, imm dur: 100, step: 875.



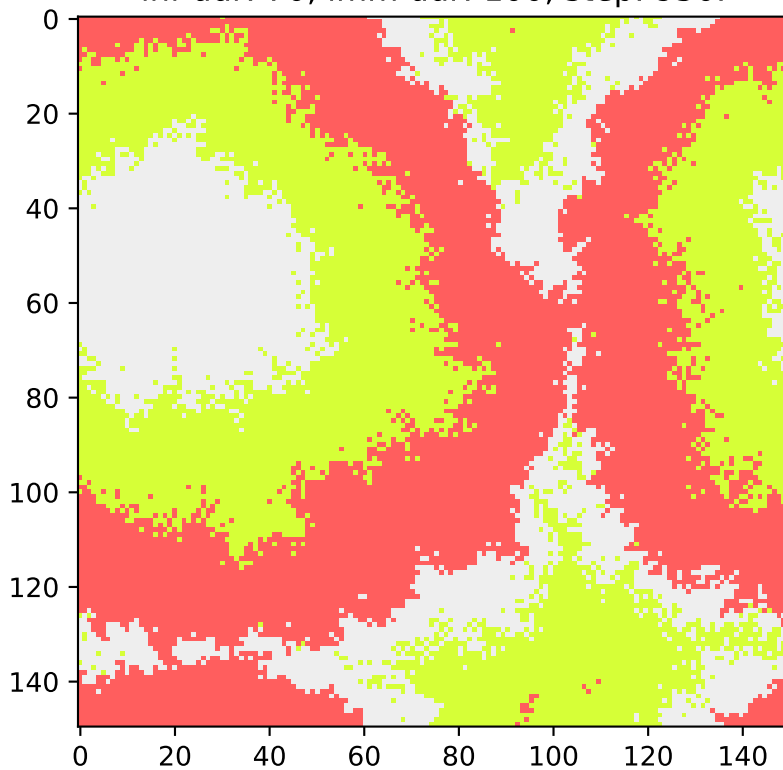
inf dur: 70, imm dur: 100, step: 900.



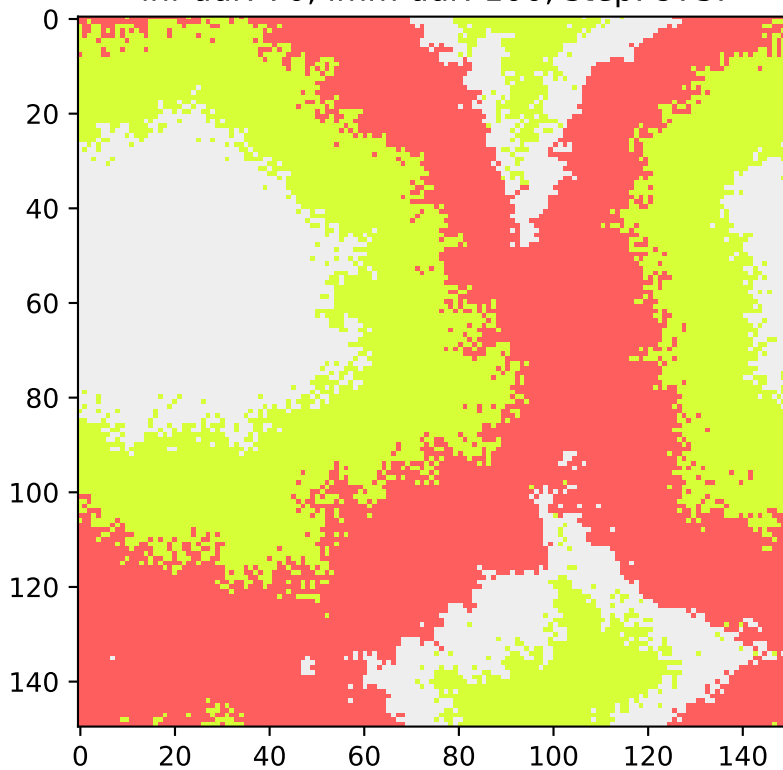
inf dur: 70, imm dur: 100, step: 925.



inf dur: 70, imm dur: 100, step: 950.

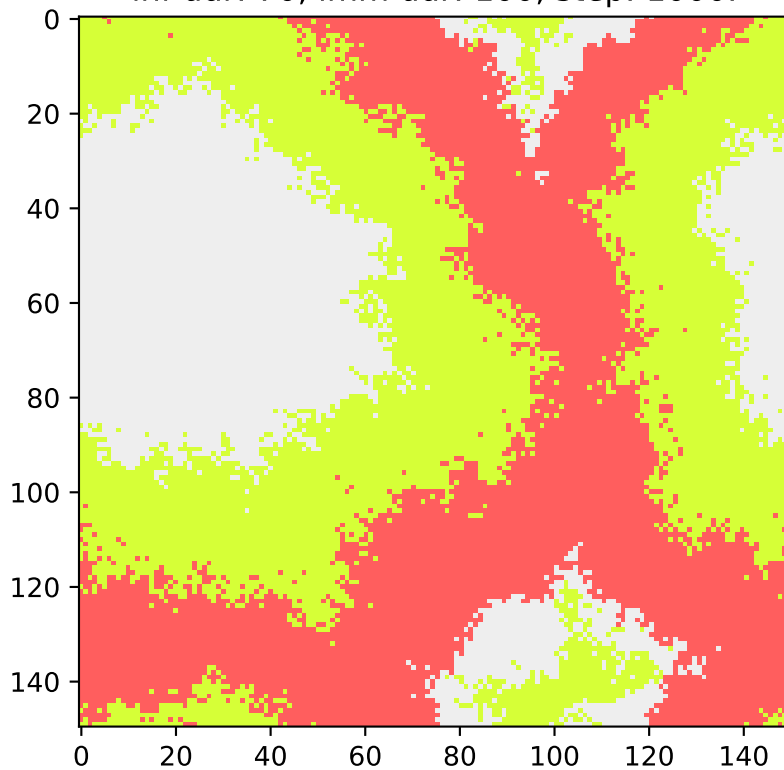


inf dur: 70, imm dur: 100, step: 975.

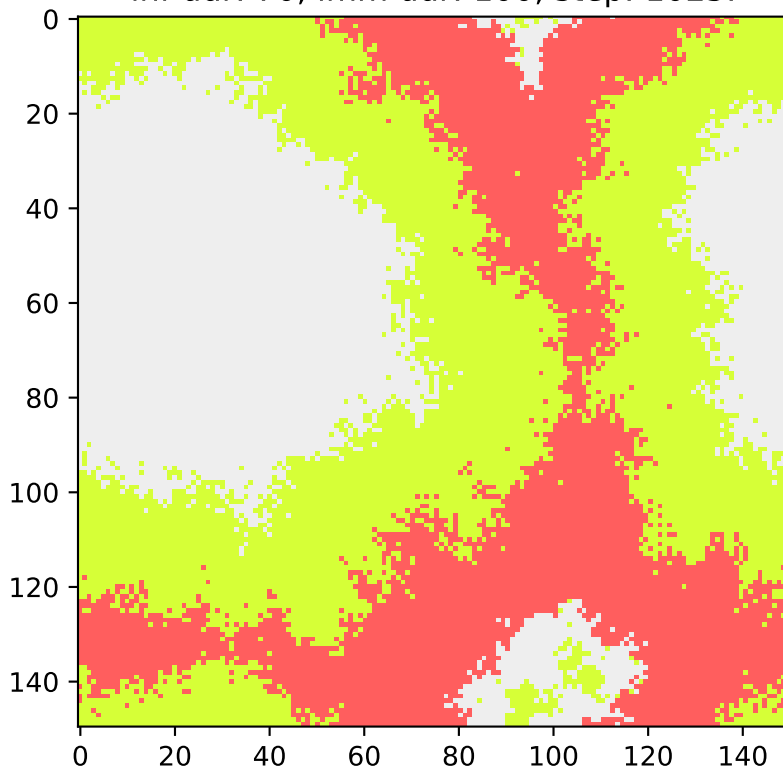




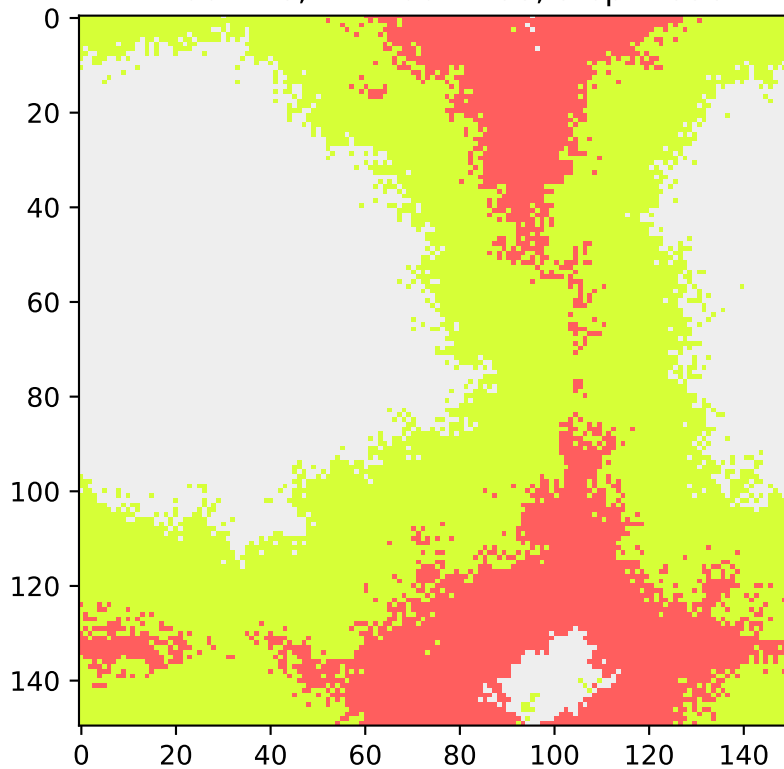
inf dur: 70, imm dur: 100, step: 1000.



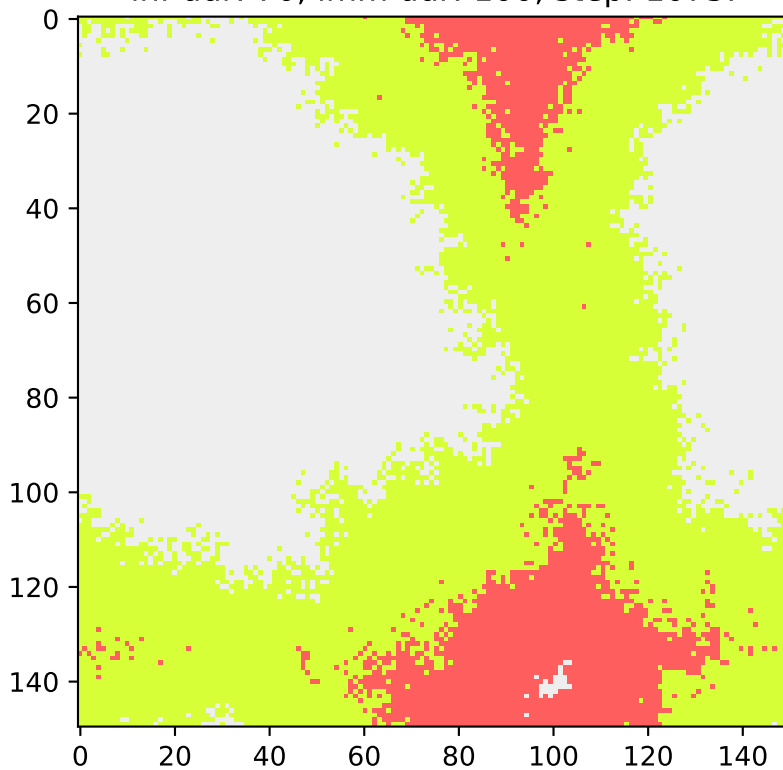
inf dur: 70, imm dur: 100, step: 1025.



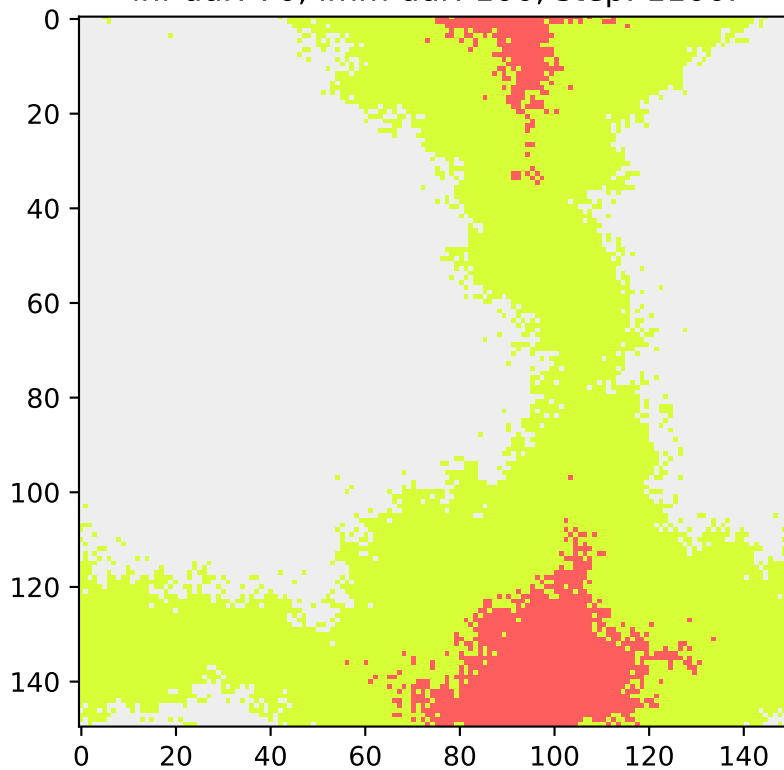
inf dur: 70, imm dur: 100, step: 1050.



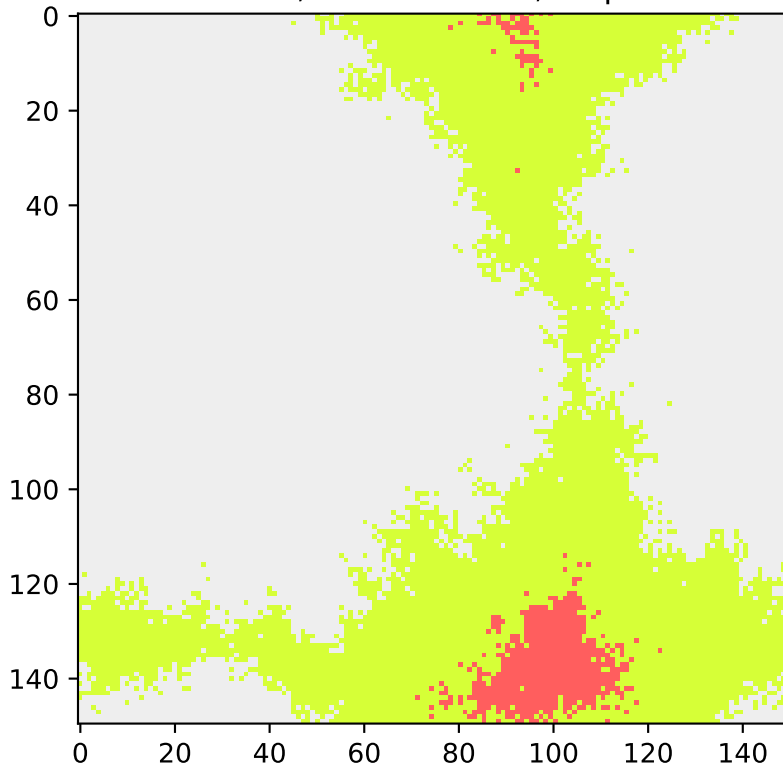
inf dur: 70, imm dur: 100, step: 1075.



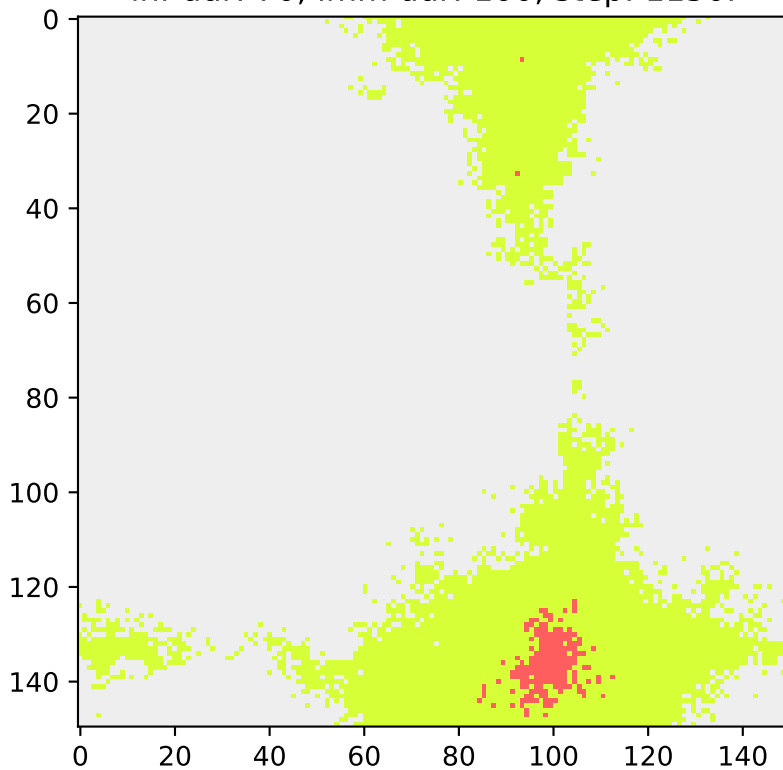
inf dur: 70, imm dur: 100, step: 1100.



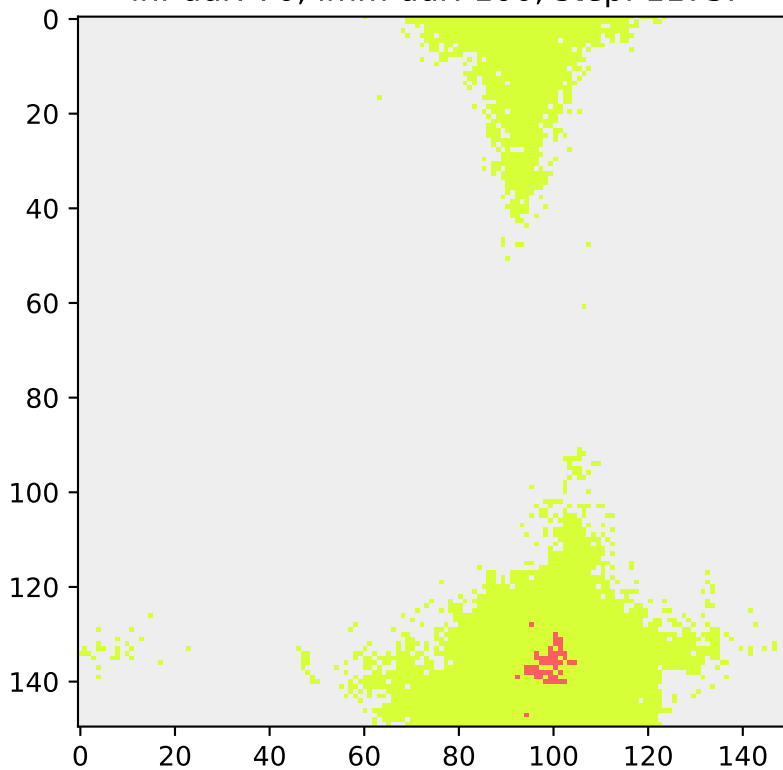
inf dur: 70, imm dur: 100, step: 1125.



inf dur: 70, imm dur: 100, step: 1150.

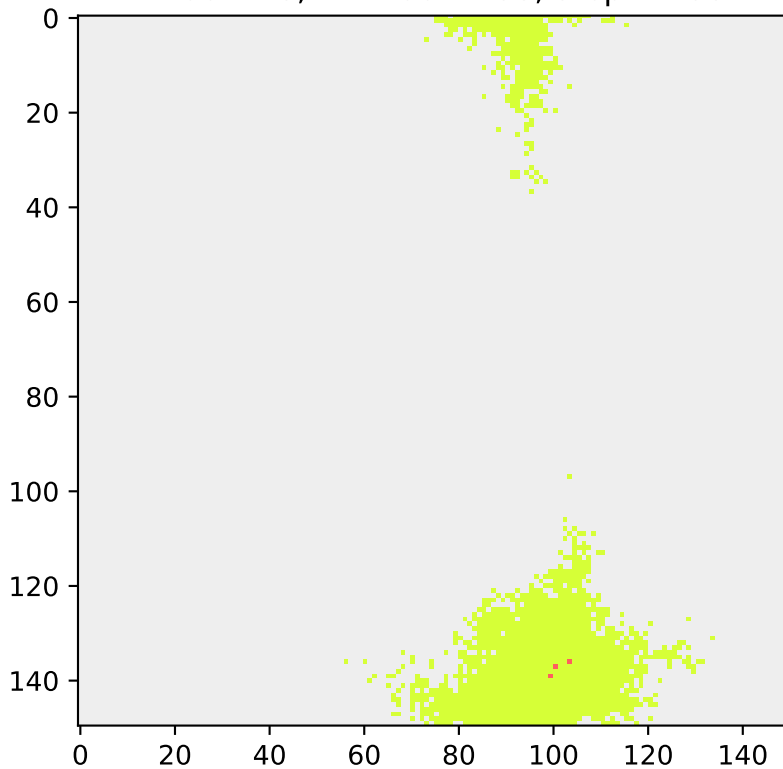


inf dur: 70, imm dur: 100, step: 1175.

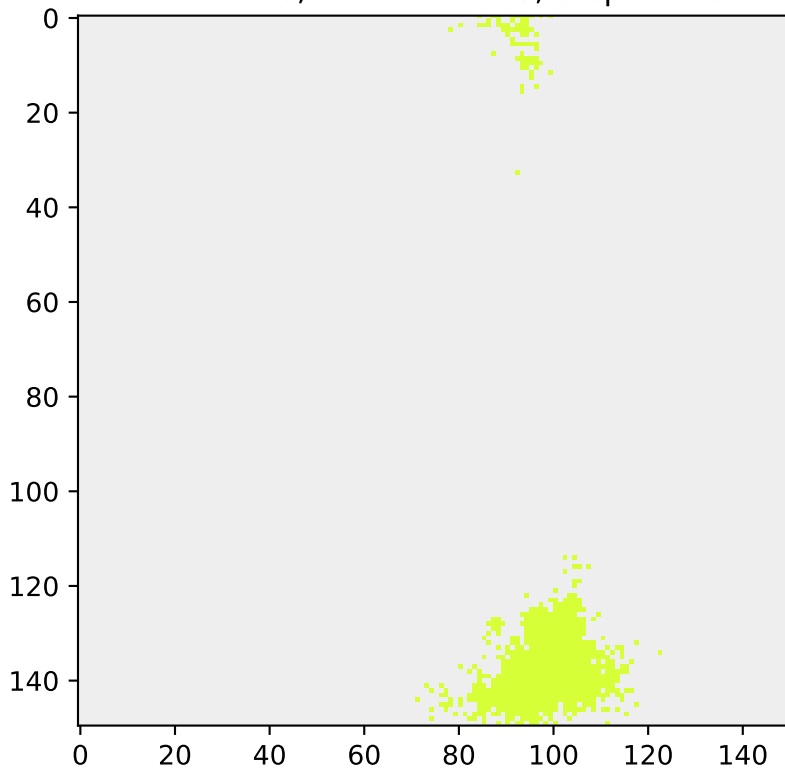




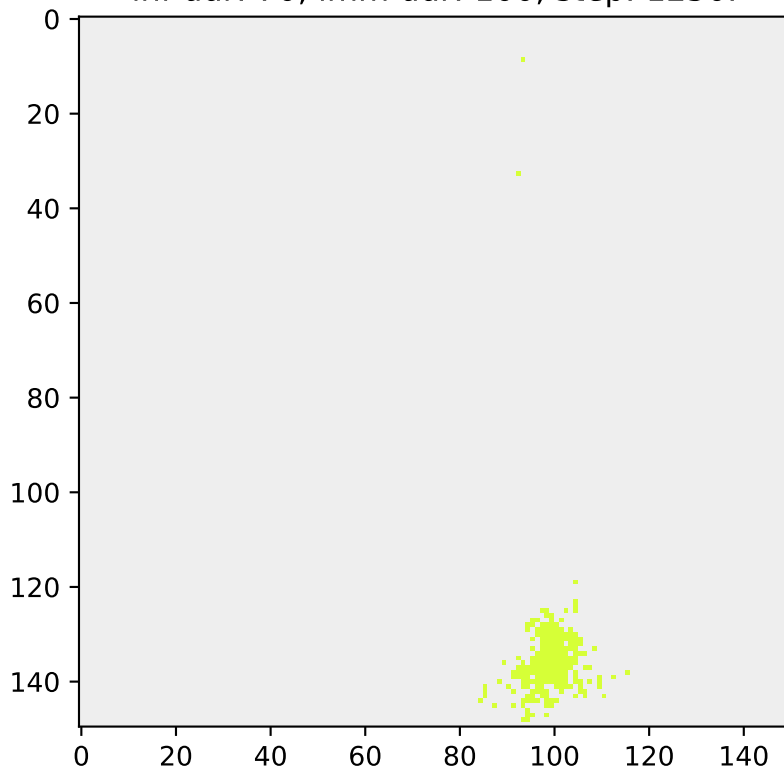
inf dur: 70, imm dur: 100, step: 1200.



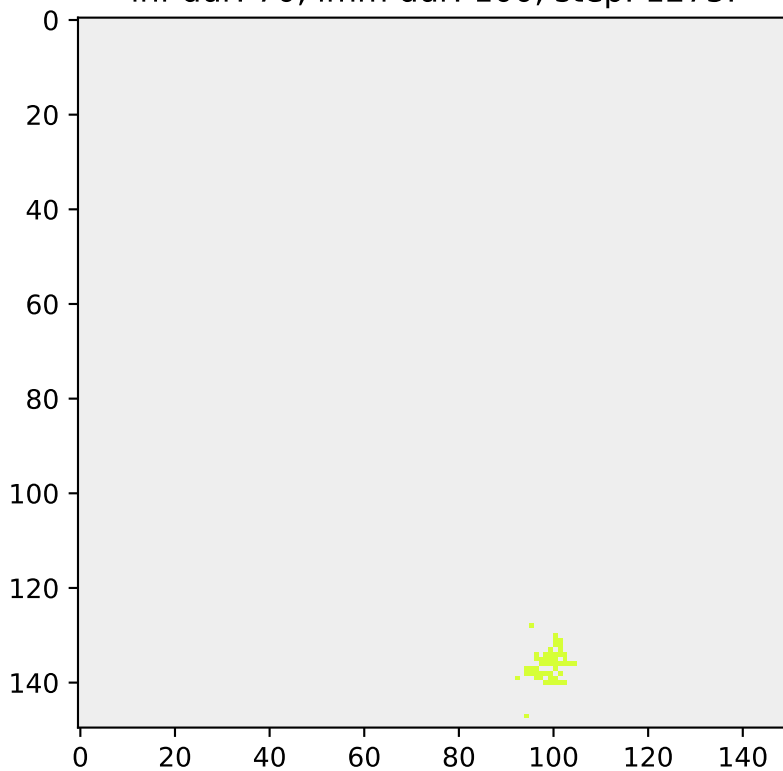
inf dur: 70, imm dur: 100, step: 1225.



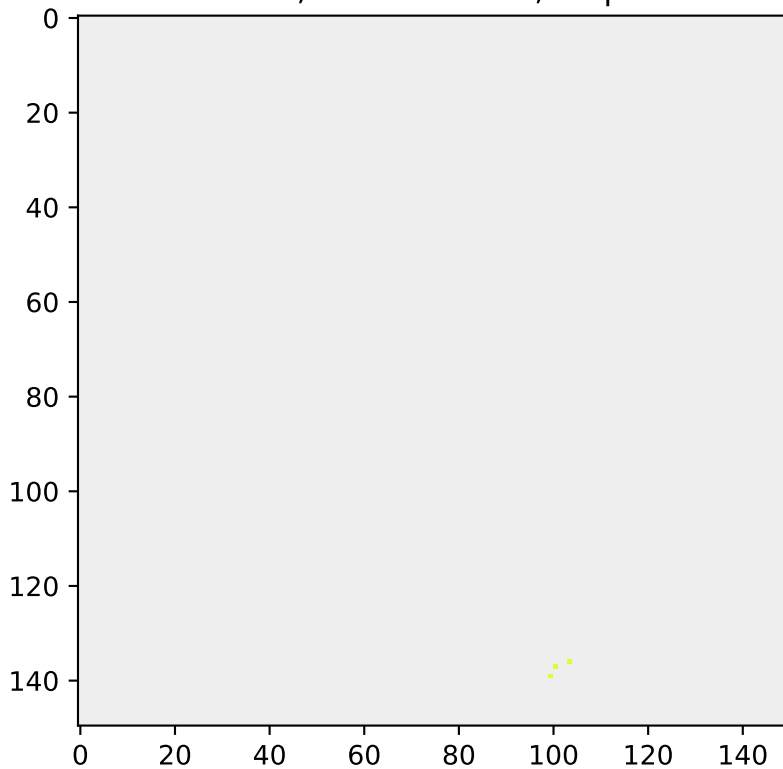
inf dur: 70, imm dur: 100, step: 1250.



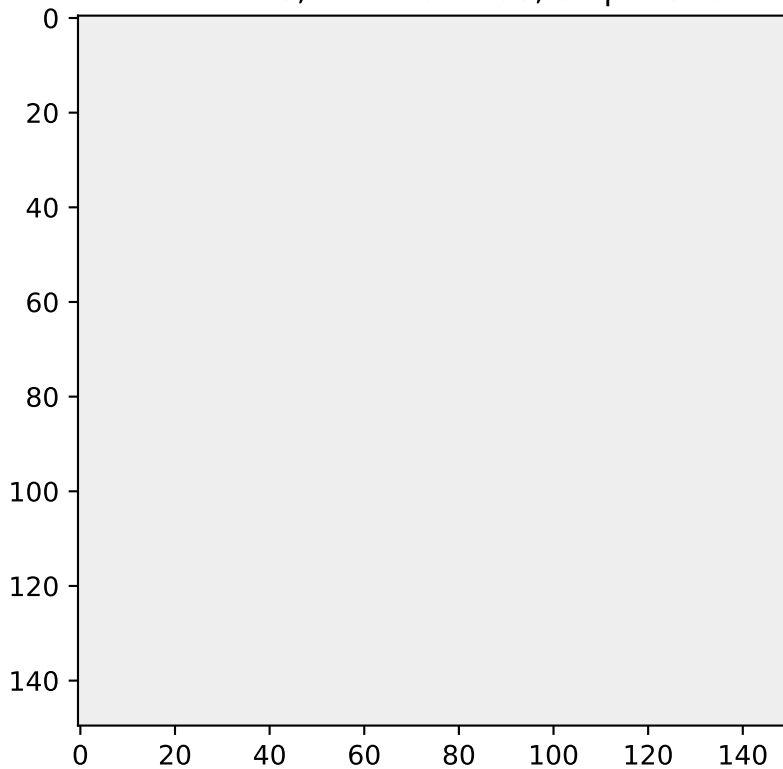
inf dur: 70, imm dur: 100, step: 1275.



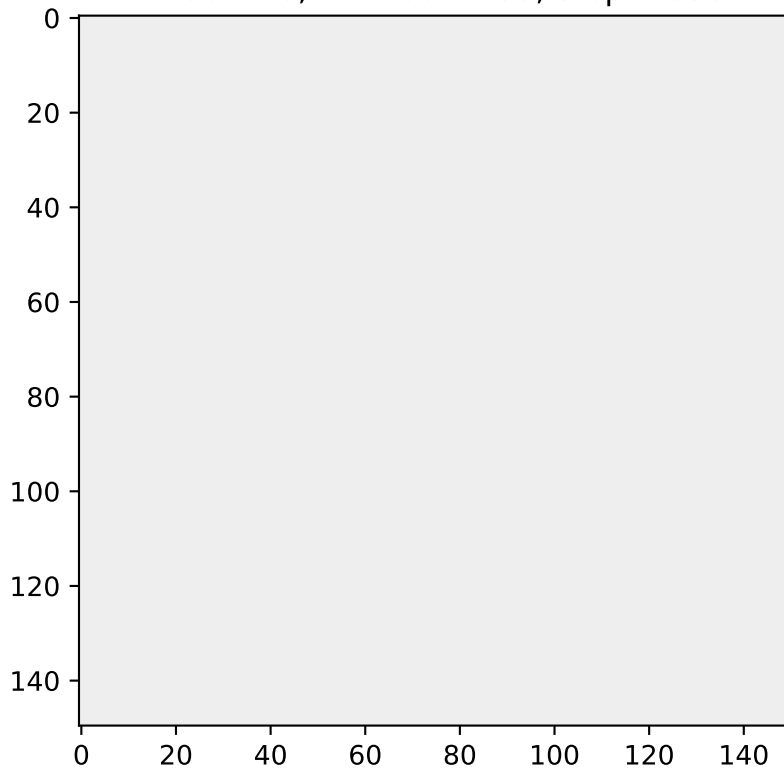
inf dur: 70, imm dur: 100, step: 1300.



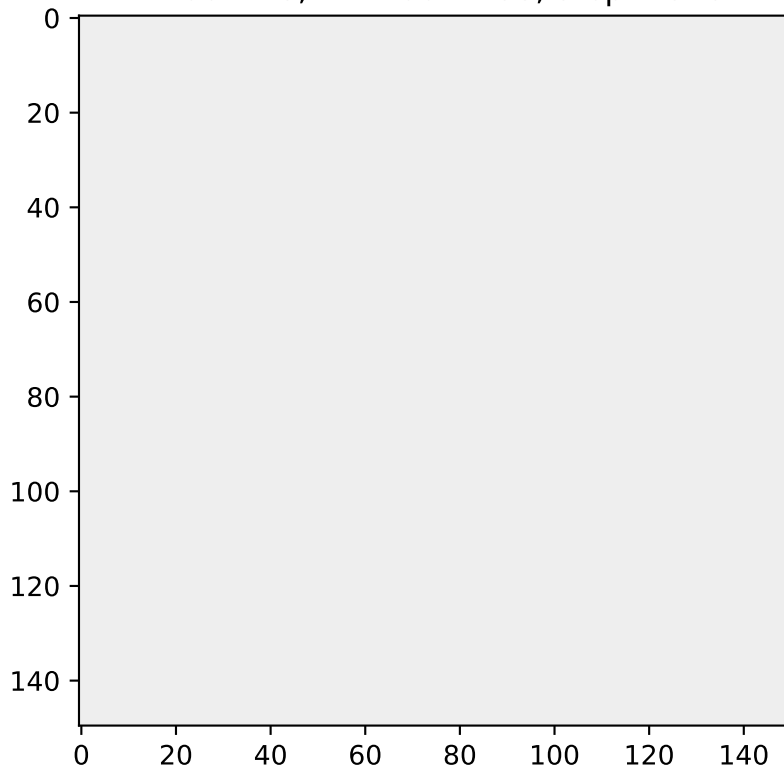
inf dur: 70, imm dur: 100, step: 1325.



inf dur: 70, imm dur: 100, step: 1350.

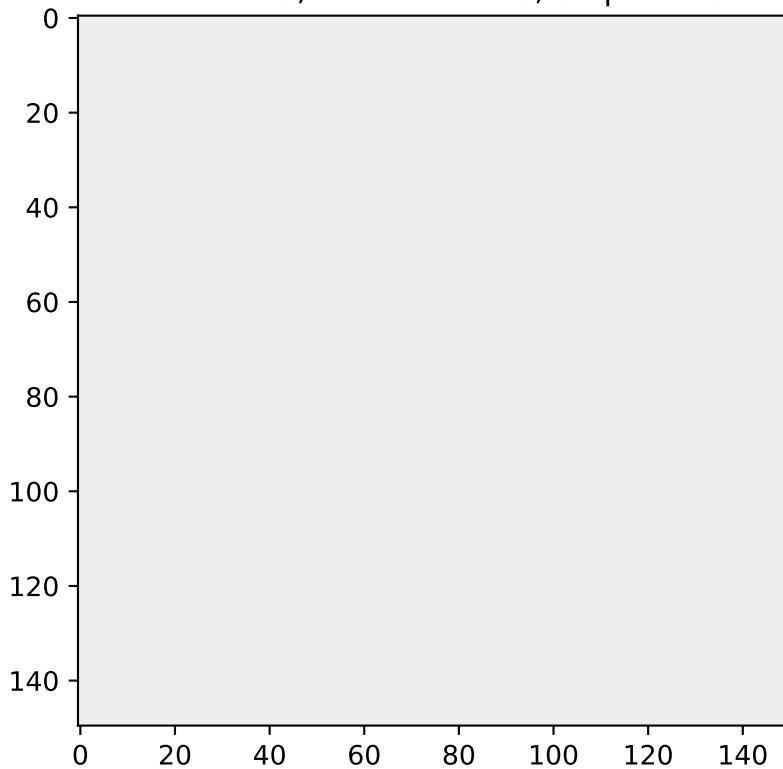


inf dur: 70, imm dur: 100, step: 1375.

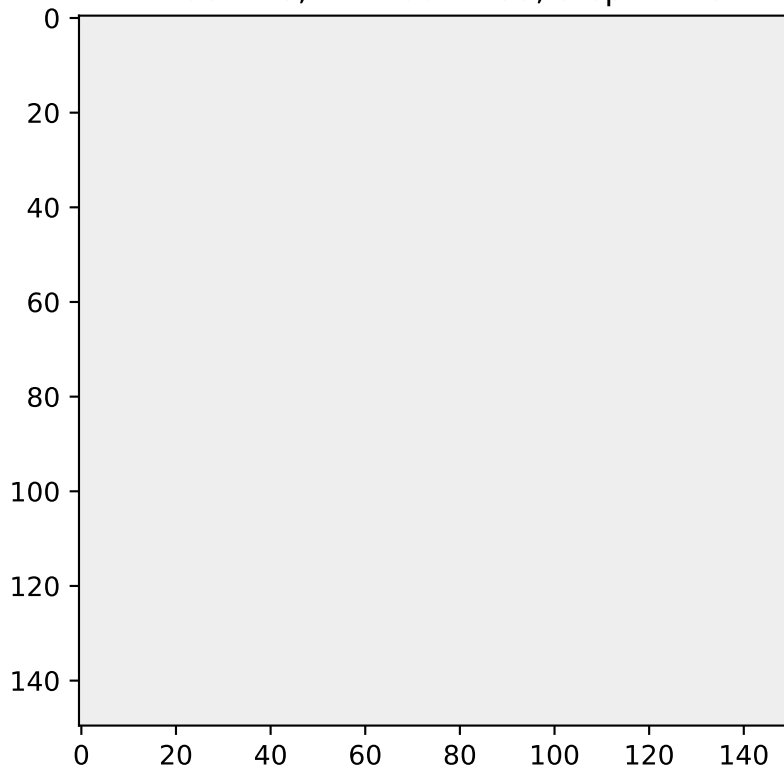




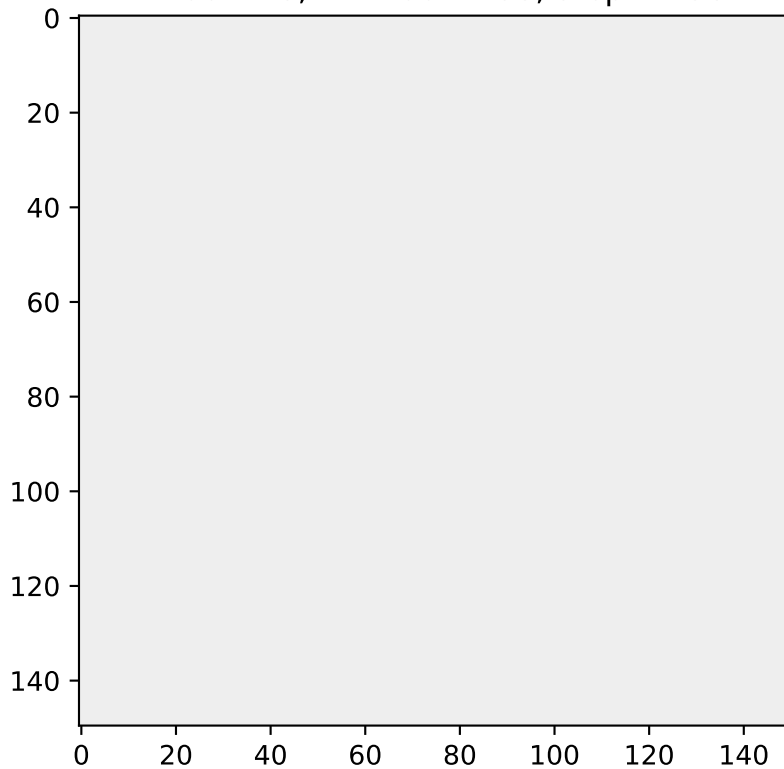
inf dur: 70, imm dur: 100, step: 1400.



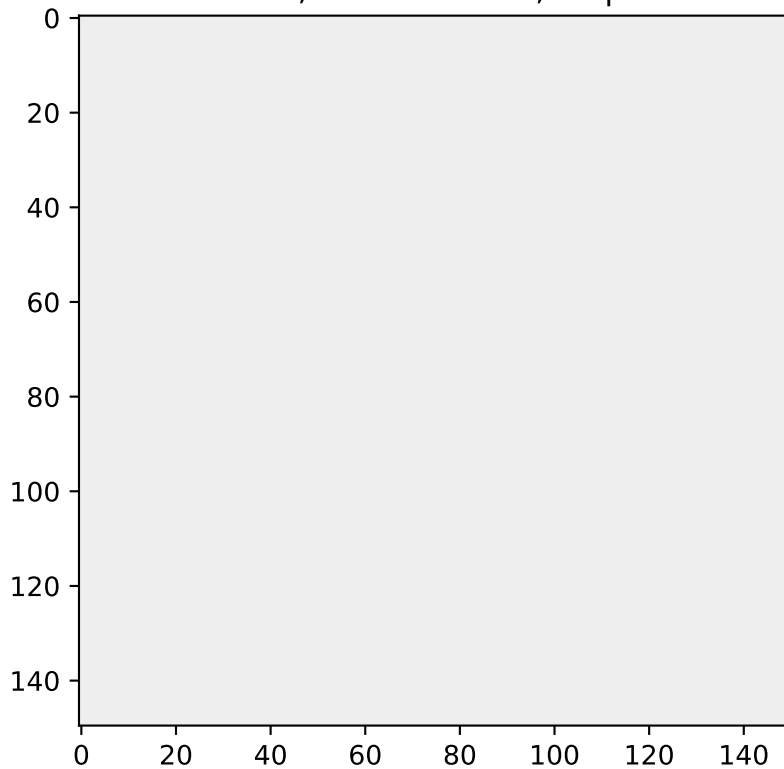
inf dur: 70, imm dur: 100, step: 1425.



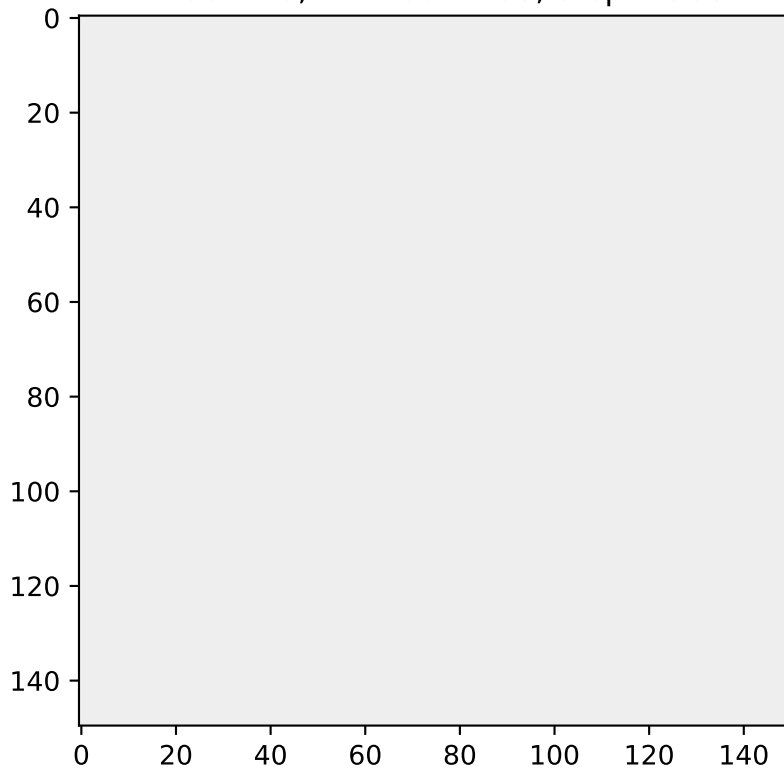
inf dur: 70, imm dur: 100, step: 1450.



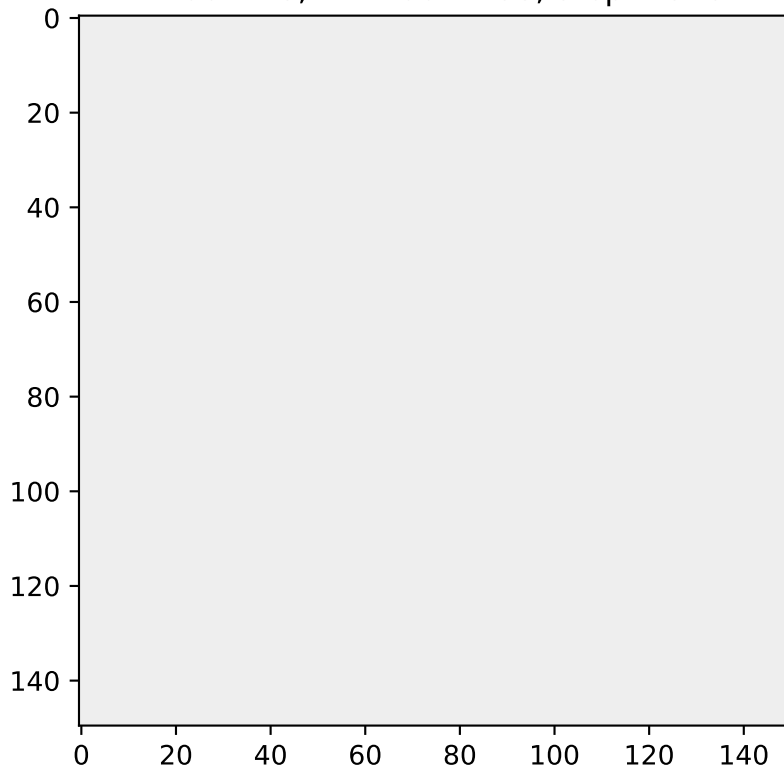
inf dur: 70, imm dur: 100, step: 1475.



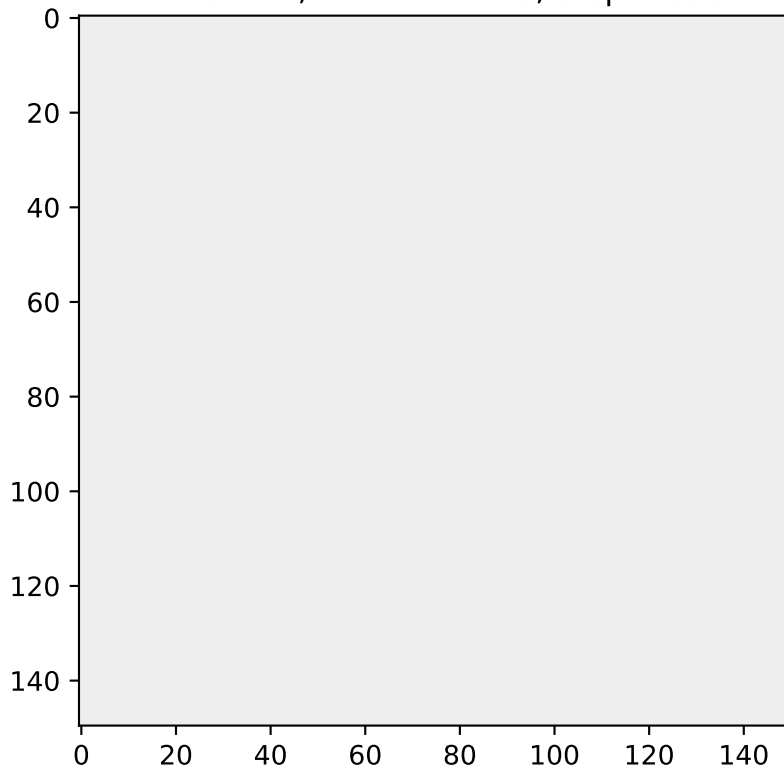
inf dur: 70, imm dur: 100, step: 1500.



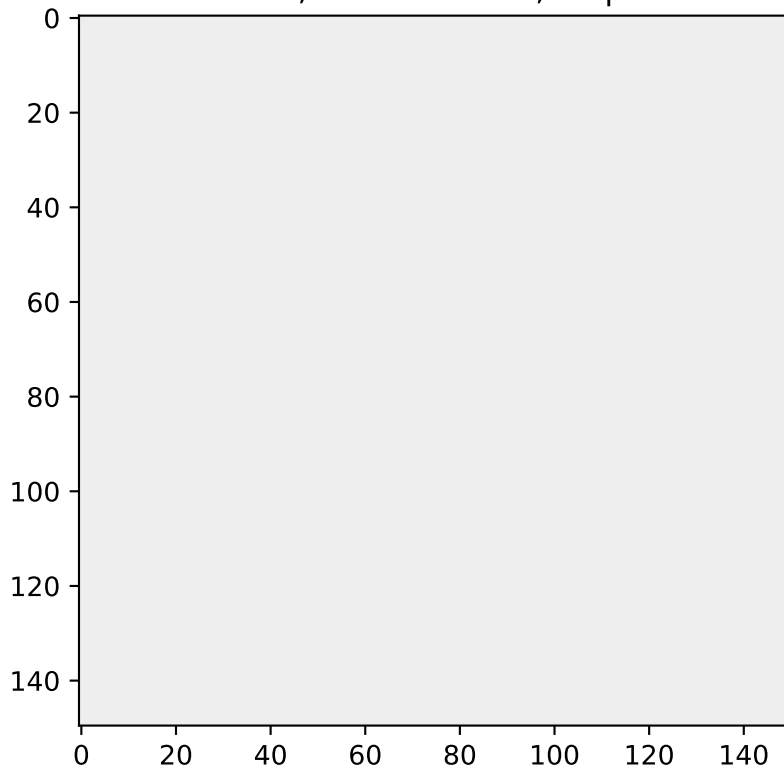
inf dur: 70, imm dur: 100, step: 1525.



inf dur: 70, imm dur: 100, step: 1550.

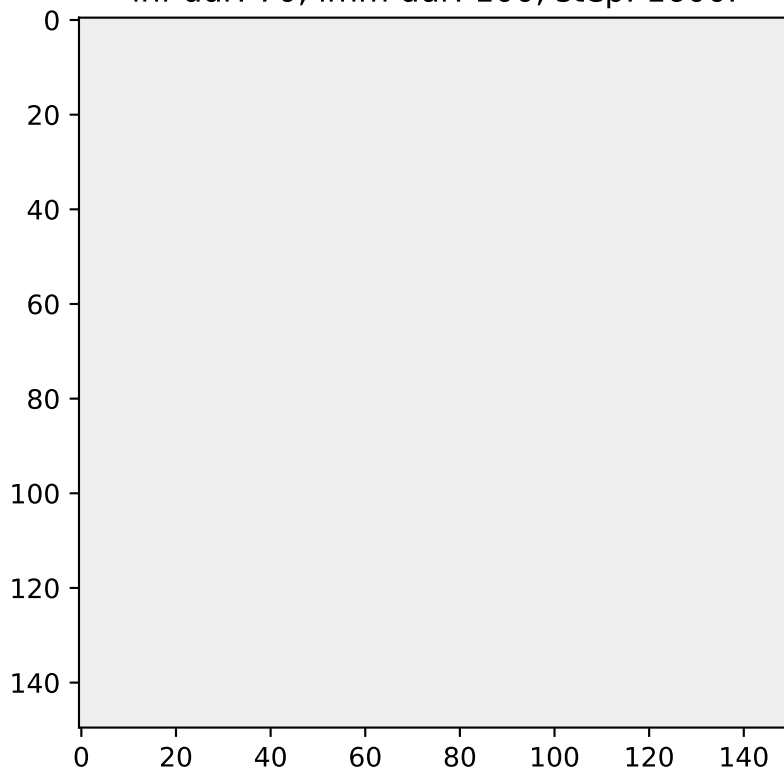


inf dur: 70, imm dur: 100, step: 1575.

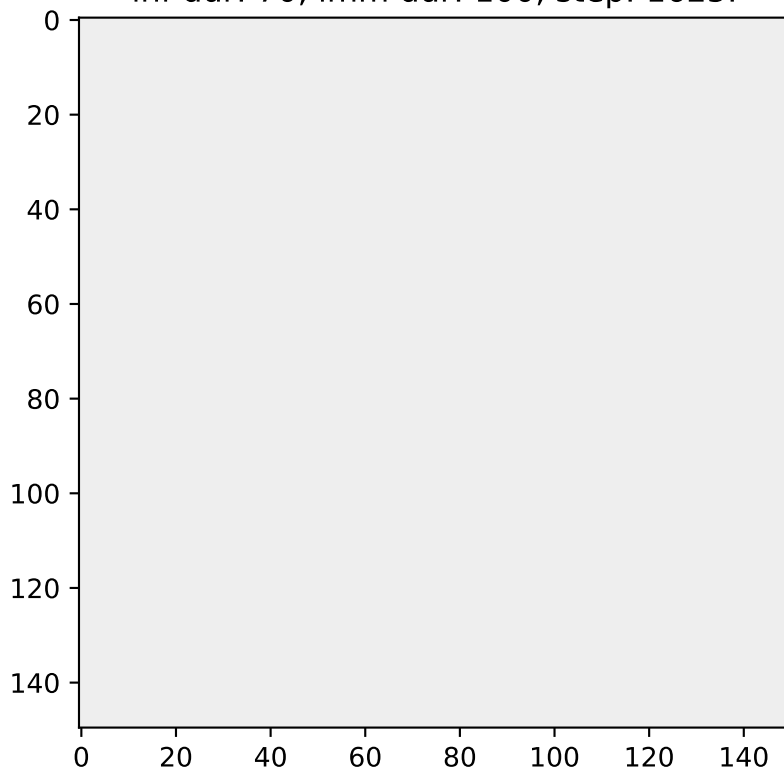




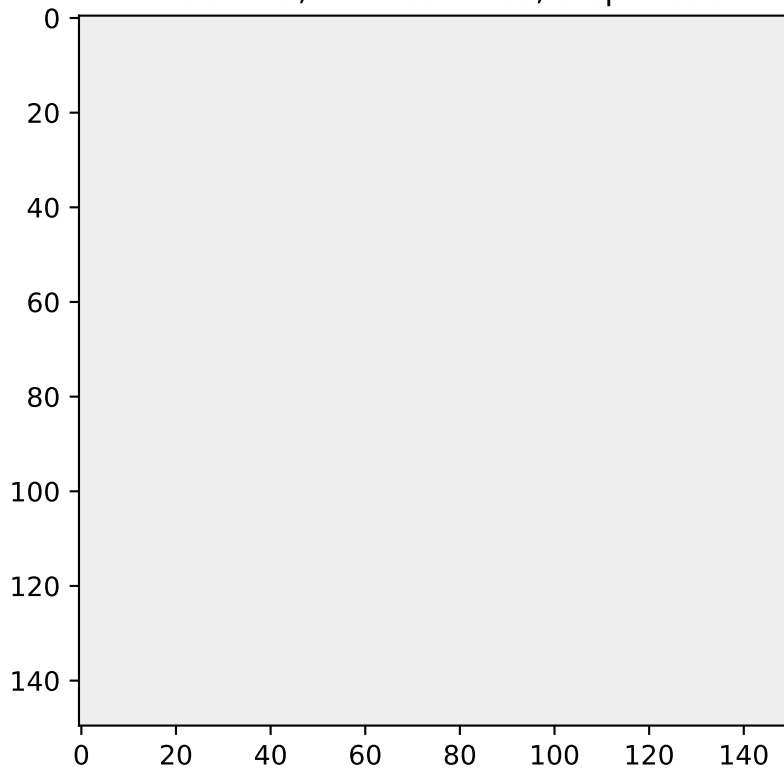
inf dur: 70, imm dur: 100, step: 1600.



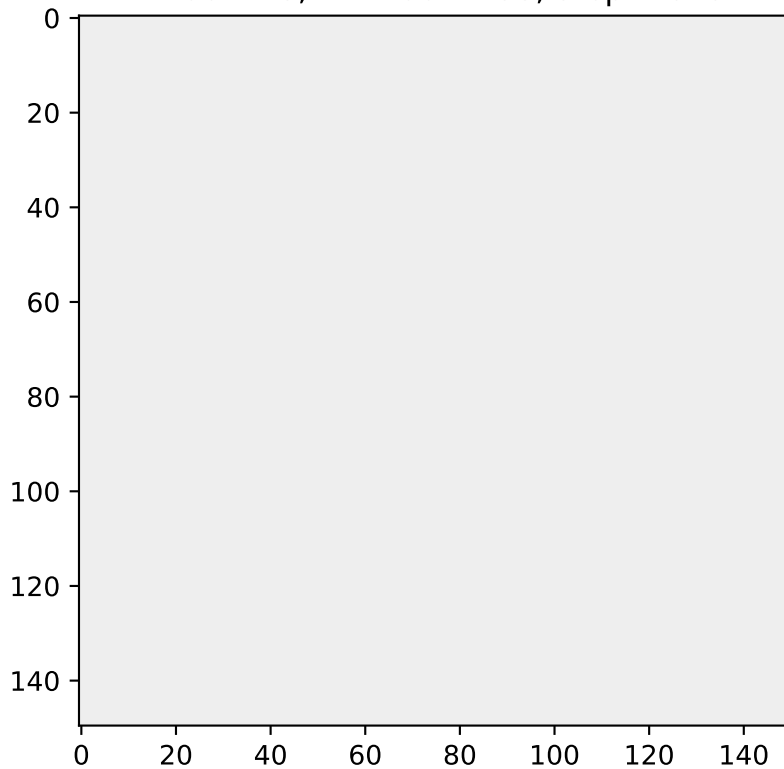
inf dur: 70, imm dur: 100, step: 1625.



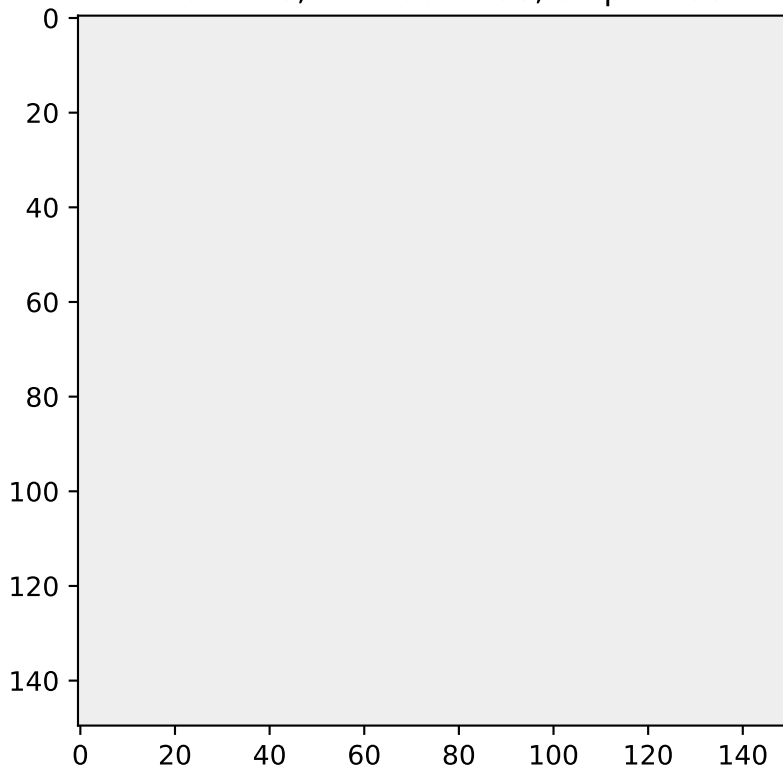
inf dur: 70, imm dur: 100, step: 1650.



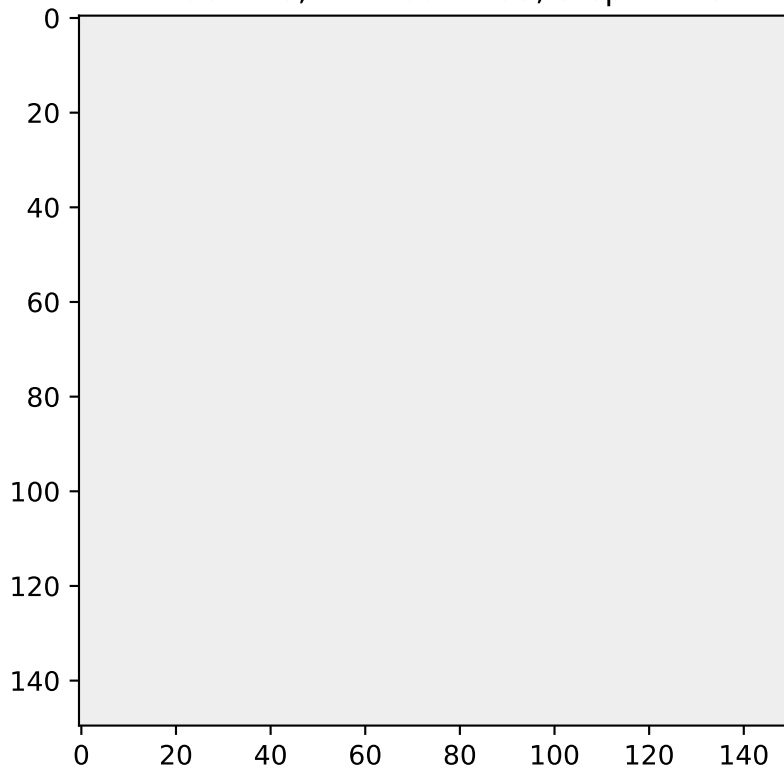
inf dur: 70, imm dur: 100, step: 1675.



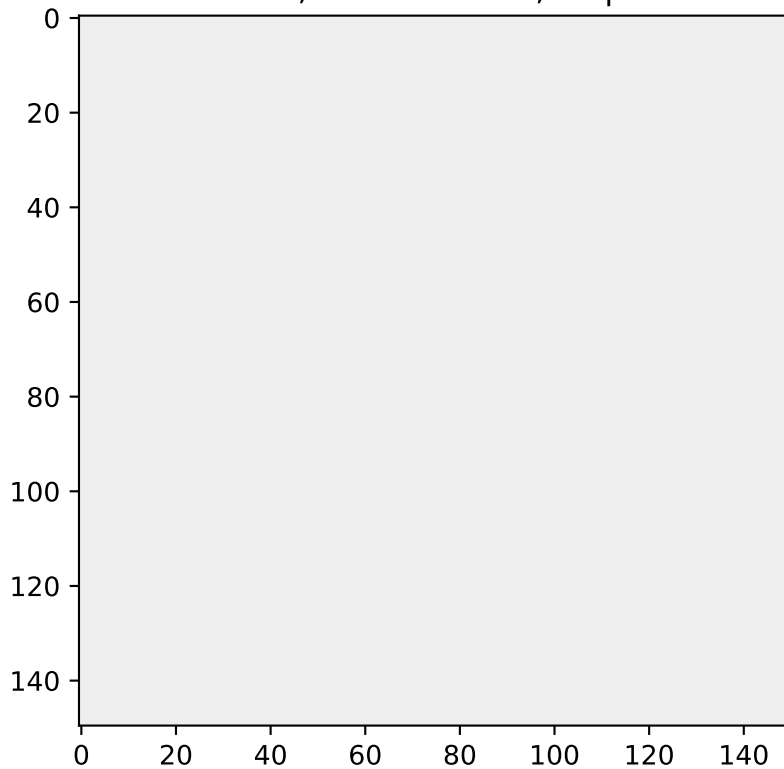
inf dur: 70, imm dur: 100, step: 1700.



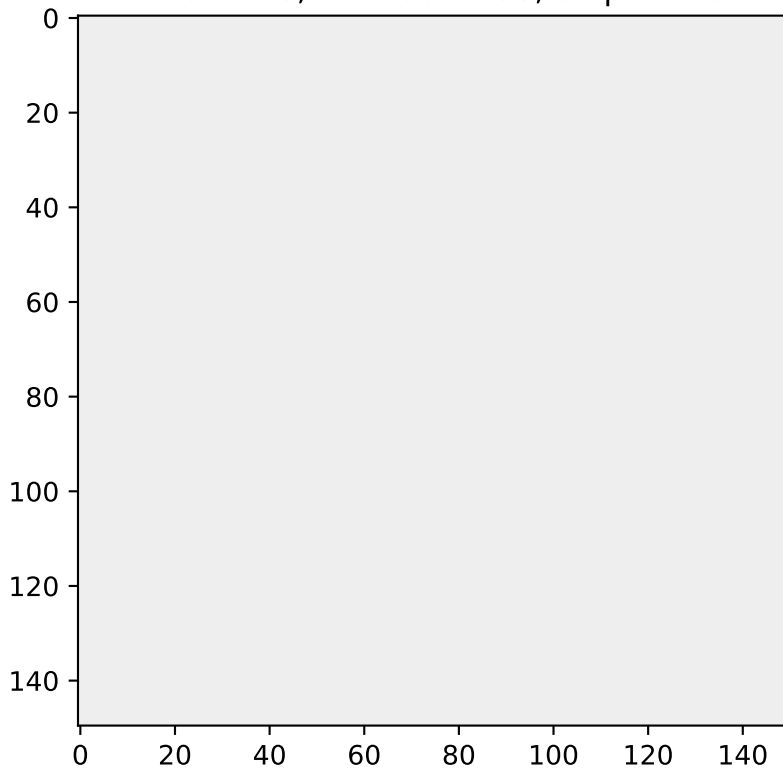
inf dur: 70, imm dur: 100, step: 1725.



inf dur: 70, imm dur: 100, step: 1750.

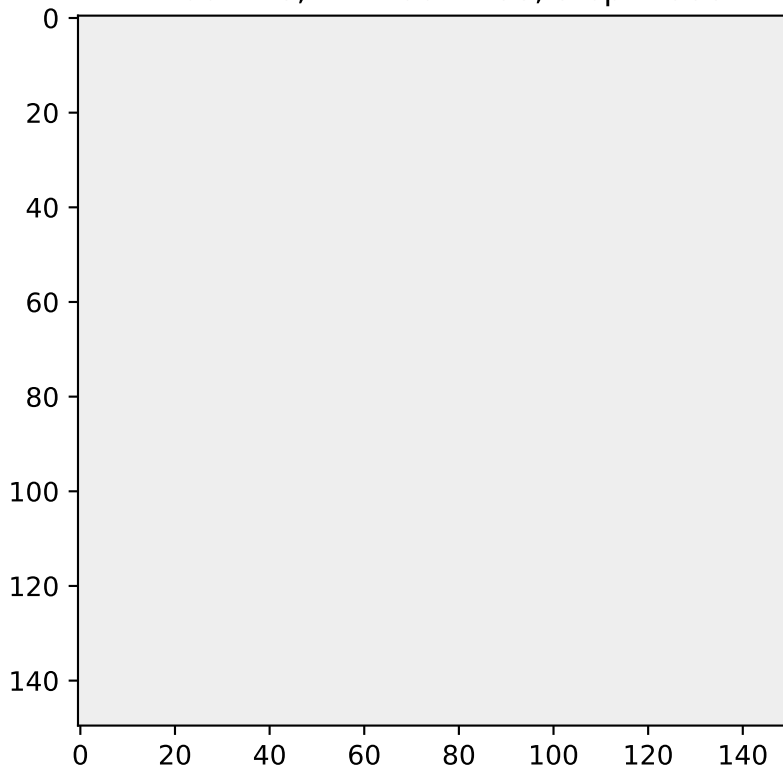


inf dur: 70, imm dur: 100, step: 1775.

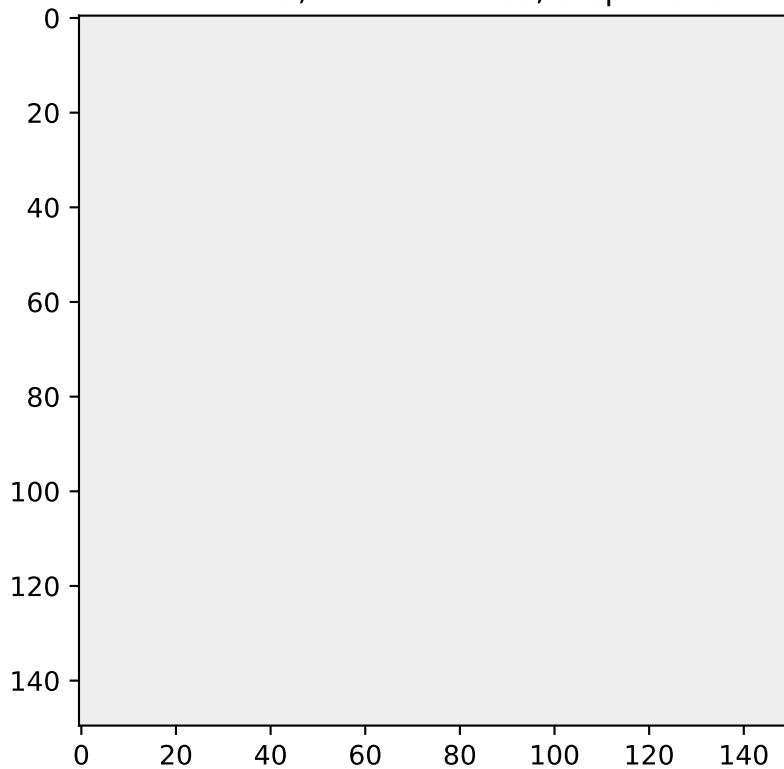




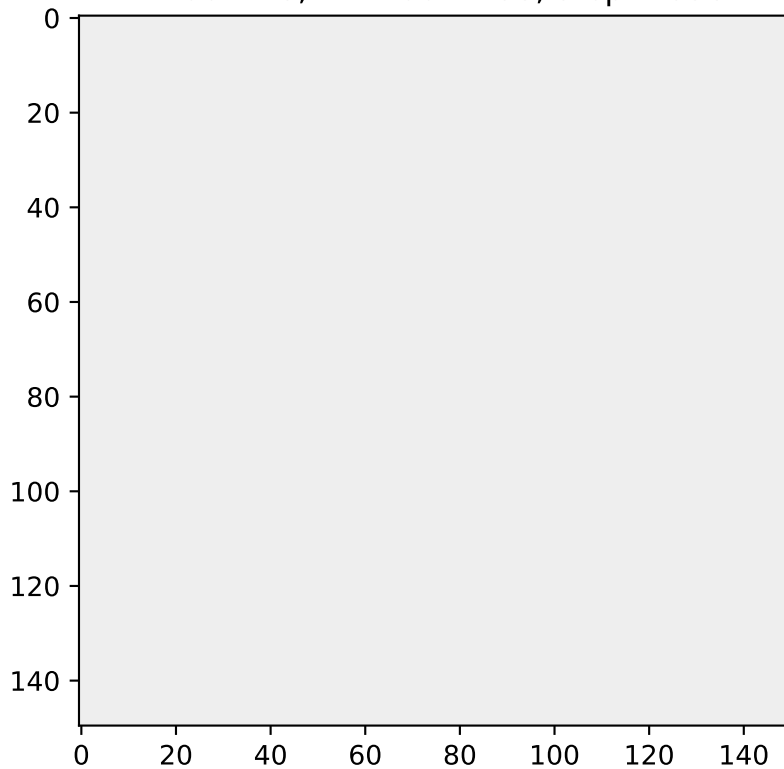
inf dur: 70, imm dur: 100, step: 1800.



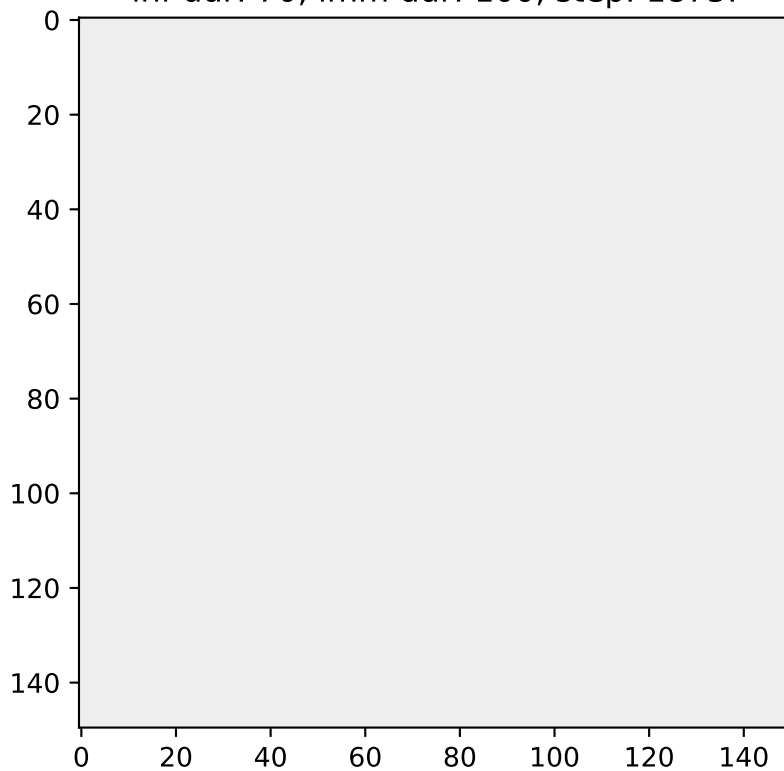
inf dur: 70, imm dur: 100, step: 1825.



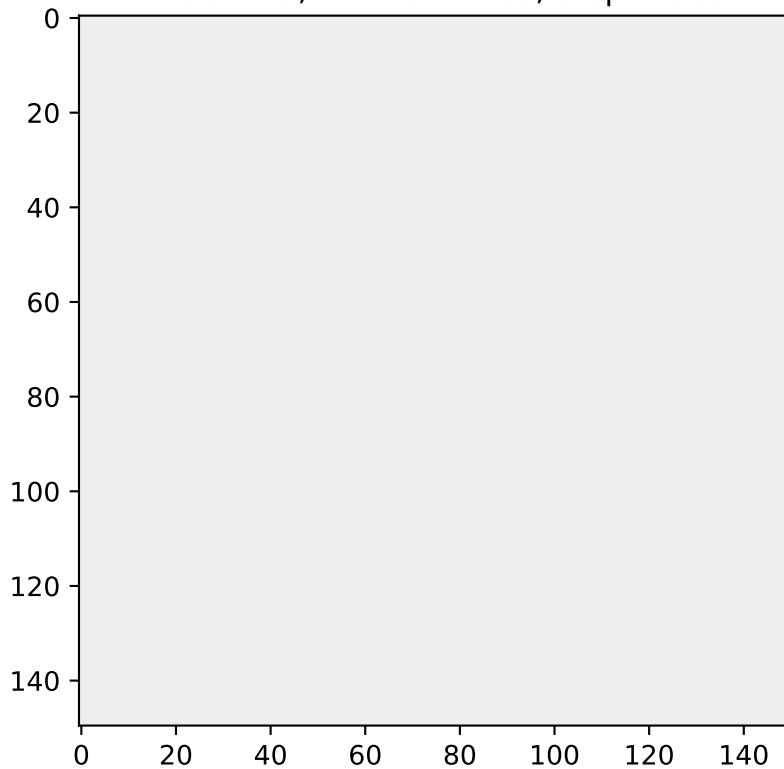
inf dur: 70, imm dur: 100, step: 1850.



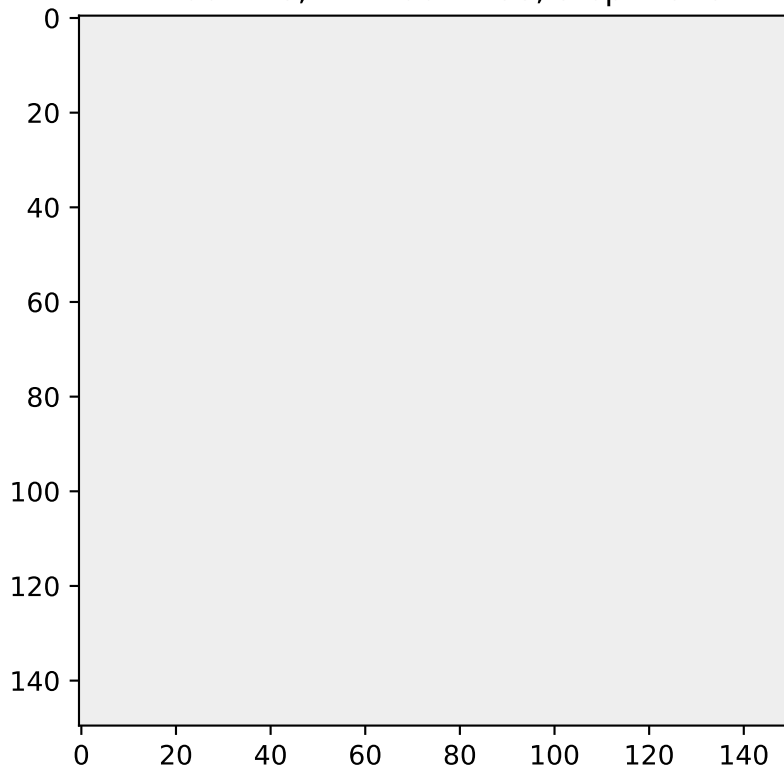
inf dur: 70, imm dur: 100, step: 1875.



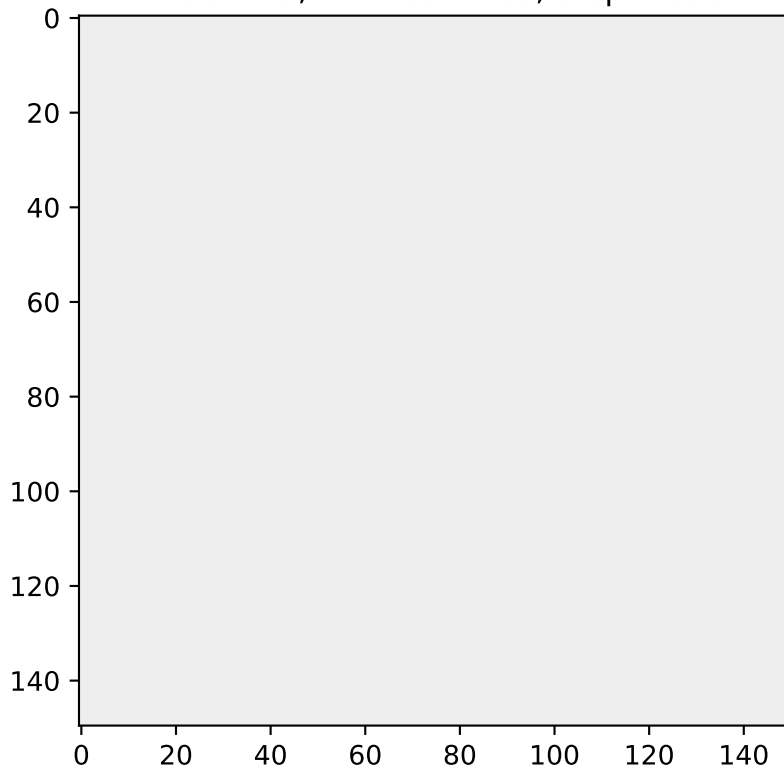
inf dur: 70, imm dur: 100, step: 1900.



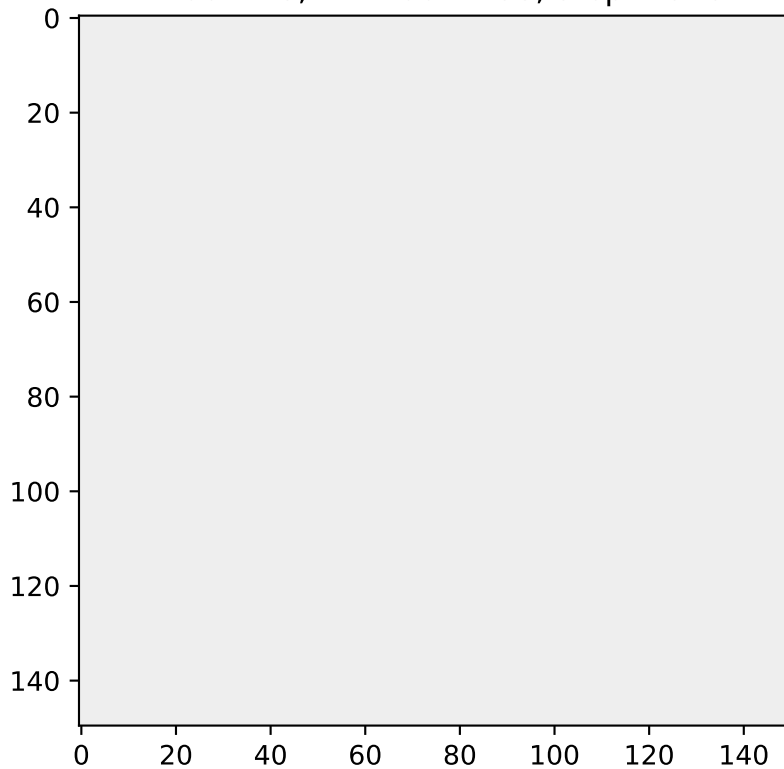
inf dur: 70, imm dur: 100, step: 1925.



inf dur: 70, imm dur: 100, step: 1950.

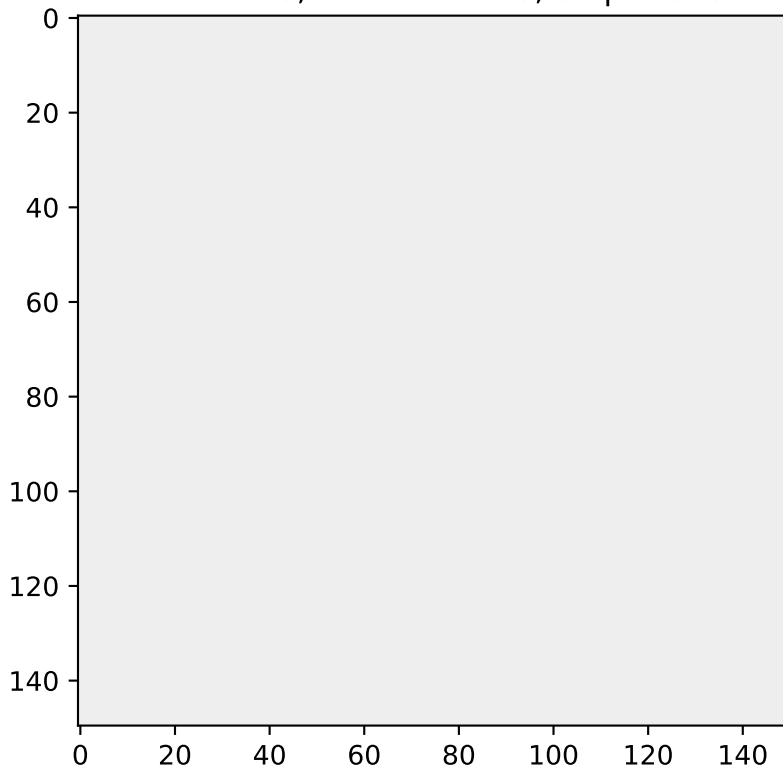


inf dur: 70, imm dur: 100, step: 1975.

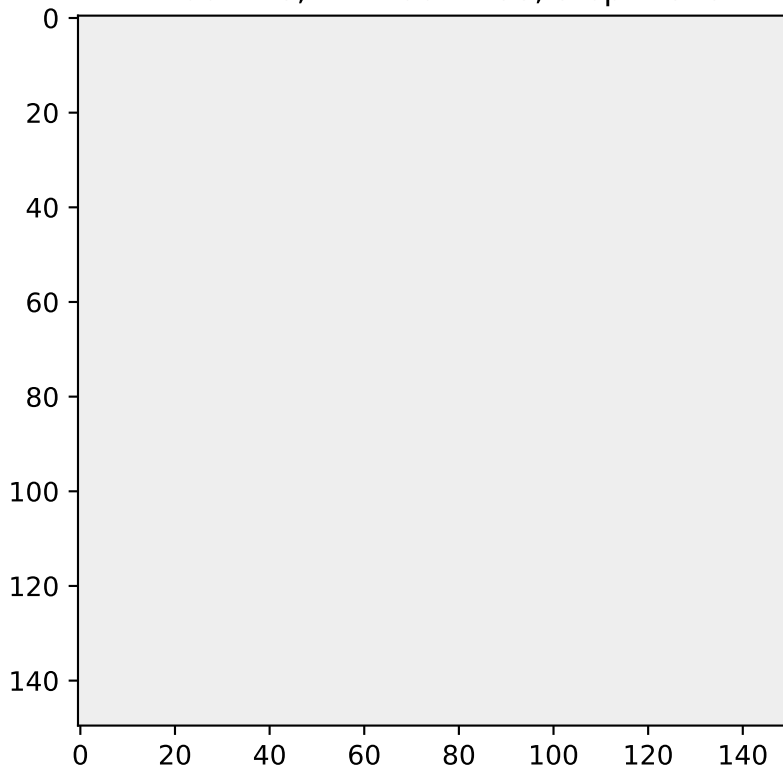




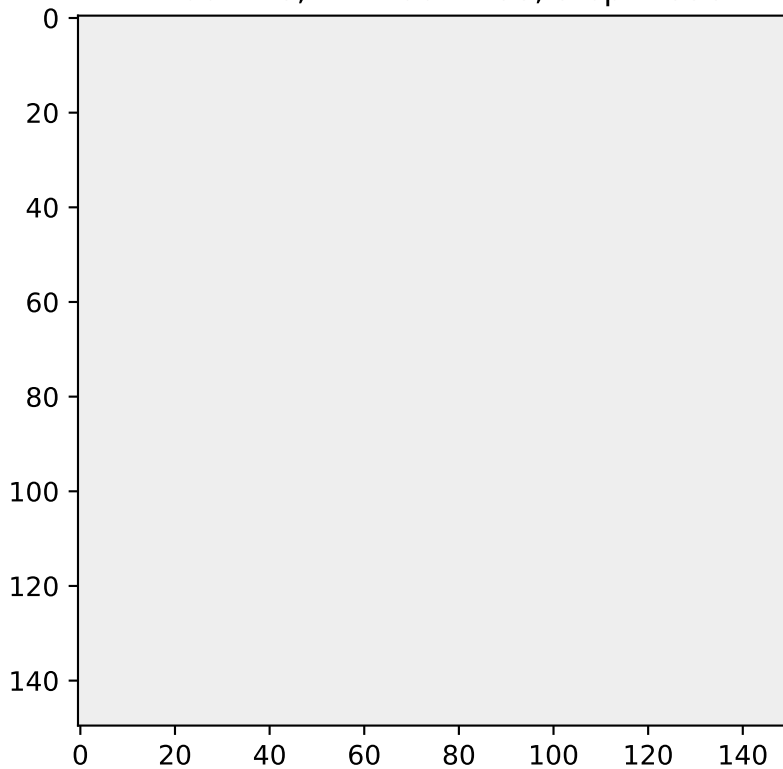
inf dur: 70, imm dur: 100, step: 2000.



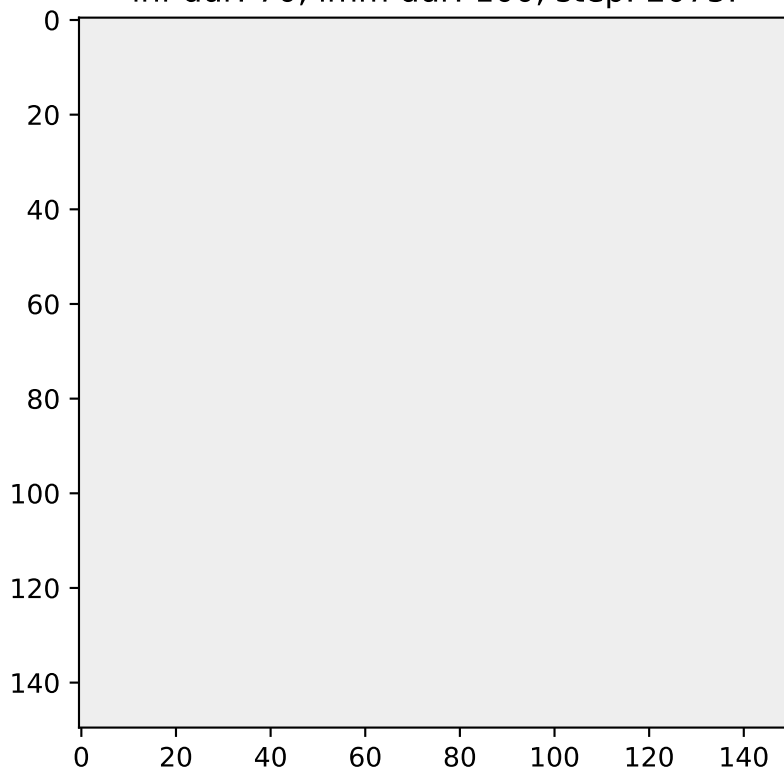
inf dur: 70, imm dur: 100, step: 2025.



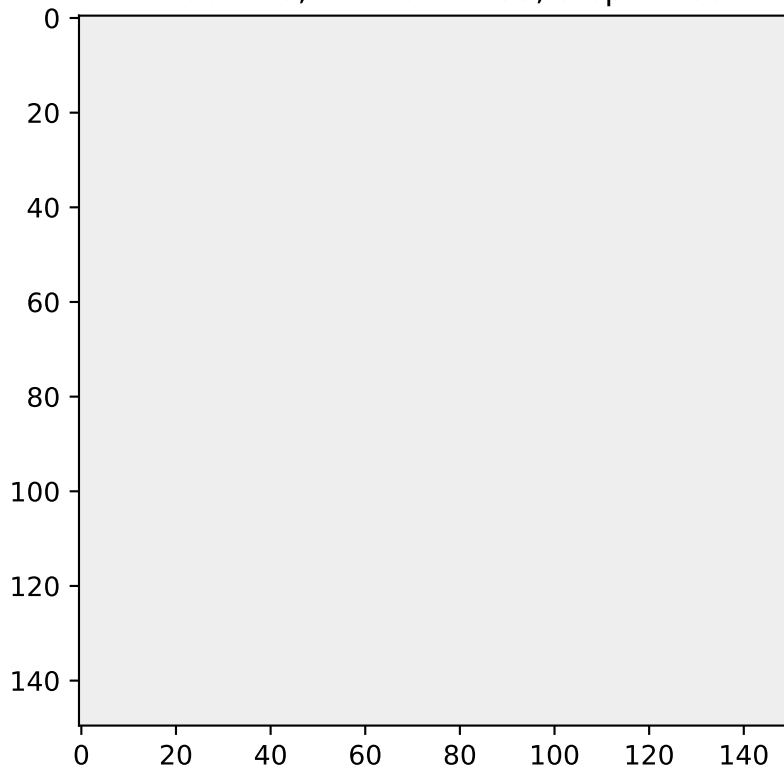
inf dur: 70, imm dur: 100, step: 2050.



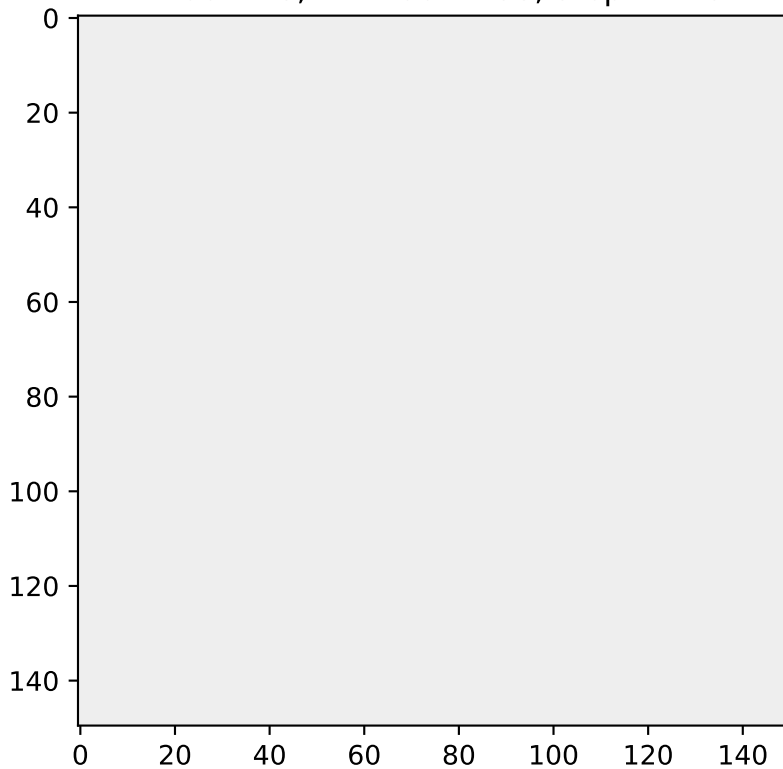
inf dur: 70, imm dur: 100, step: 2075.



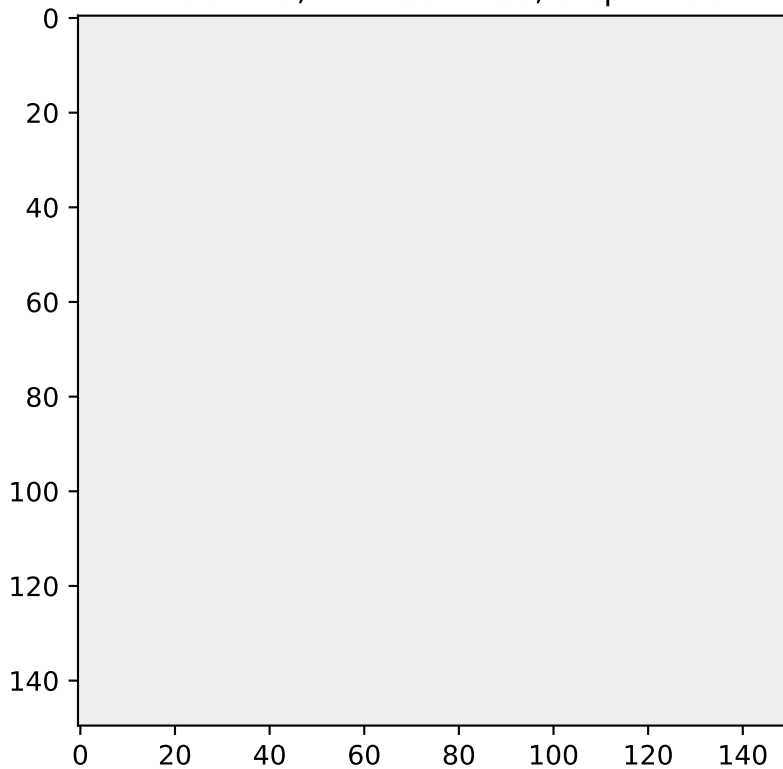
inf dur: 70, imm dur: 100, step: 2100.



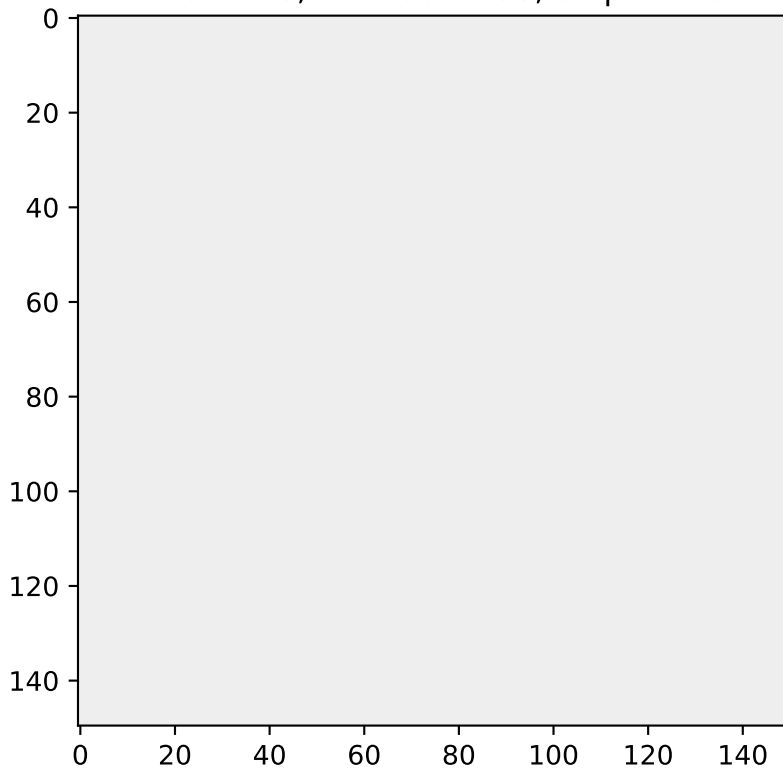
inf dur: 70, imm dur: 100, step: 2125.



inf dur: 70, imm dur: 100, step: 2150.

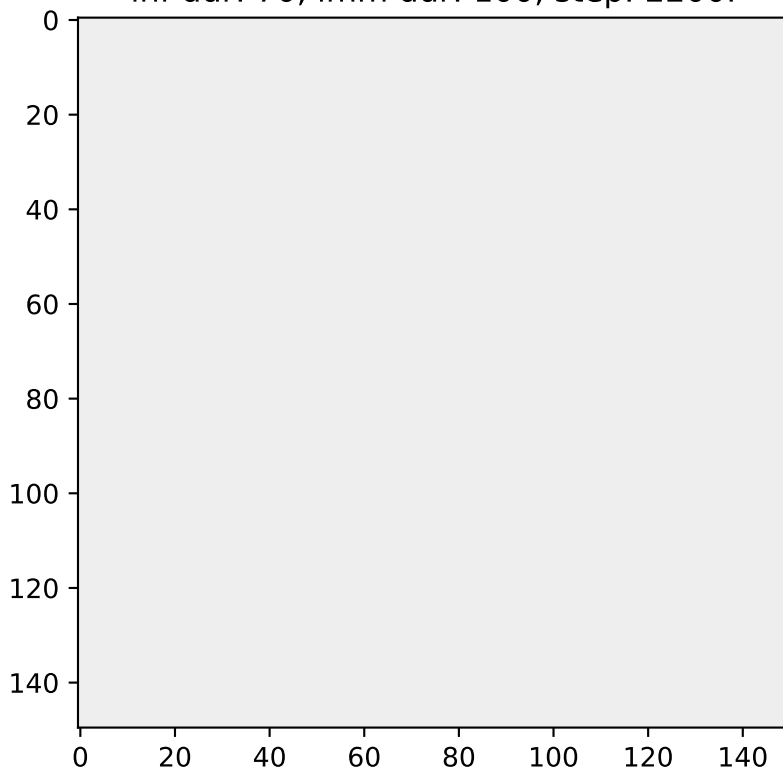


inf dur: 70, imm dur: 100, step: 2175.

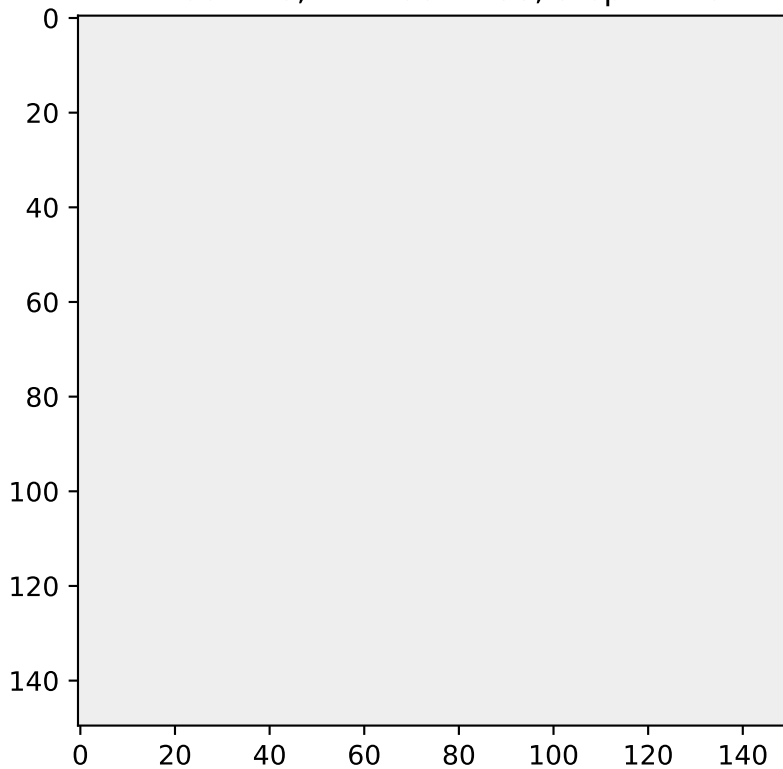




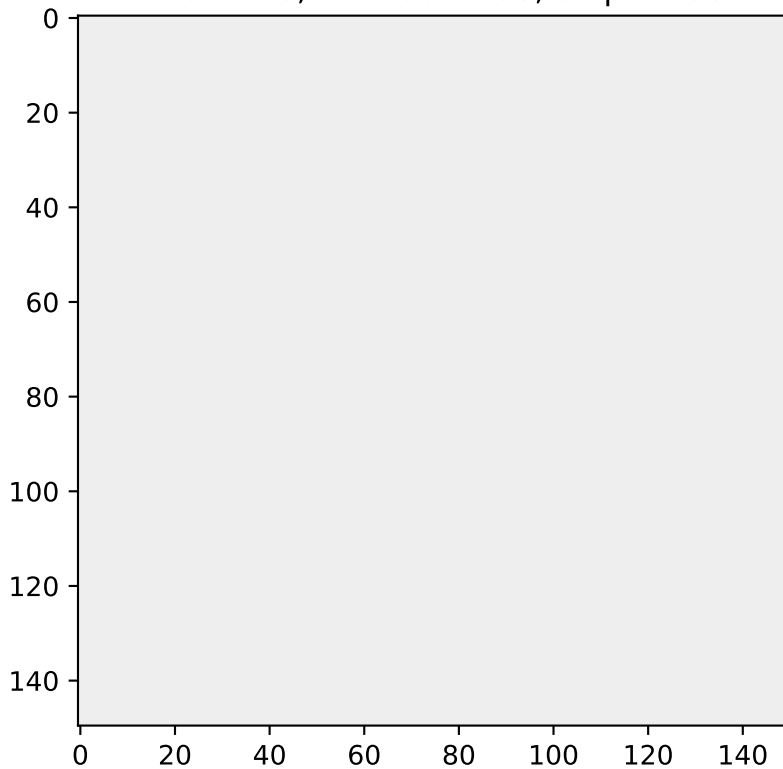
inf dur: 70, imm dur: 100, step: 2200.



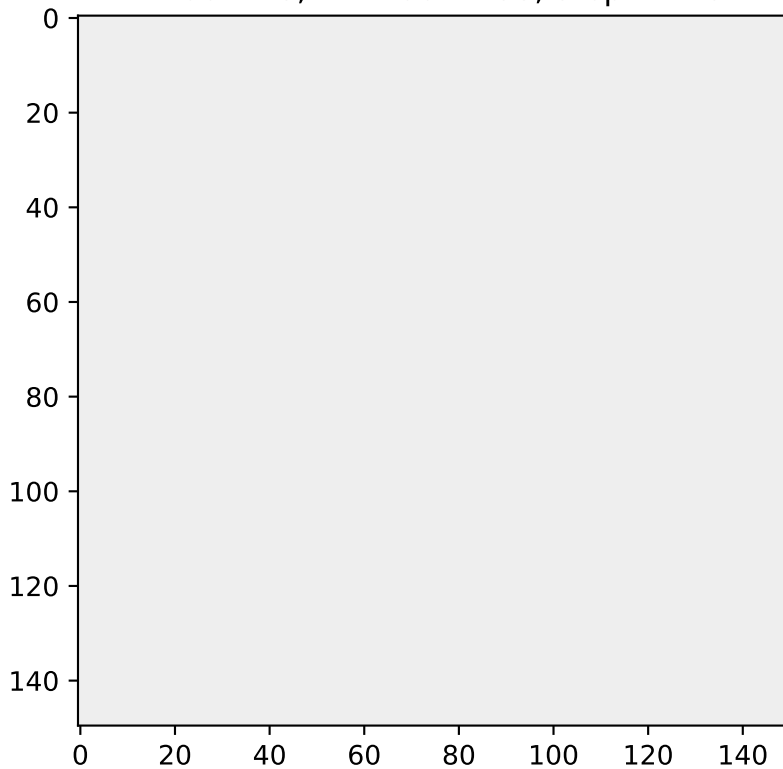
inf dur: 70, imm dur: 100, step: 2225.



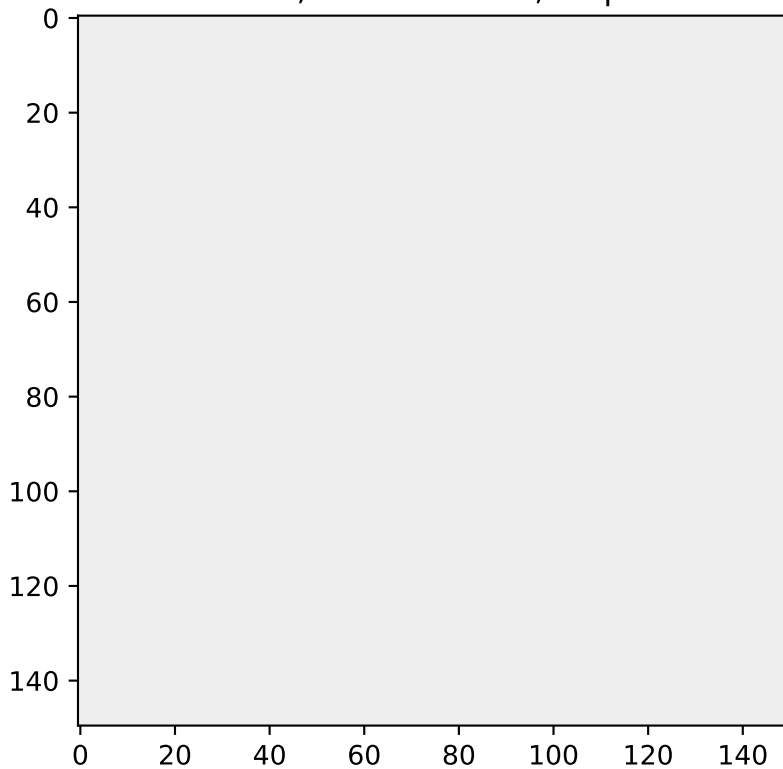
inf dur: 70, imm dur: 100, step: 2250.



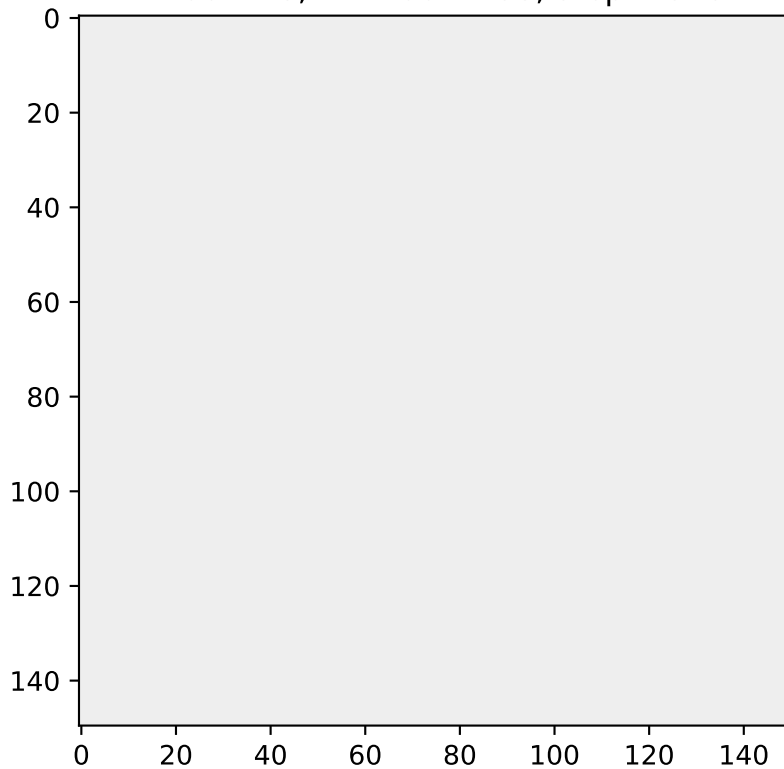
inf dur: 70, imm dur: 100, step: 2275.



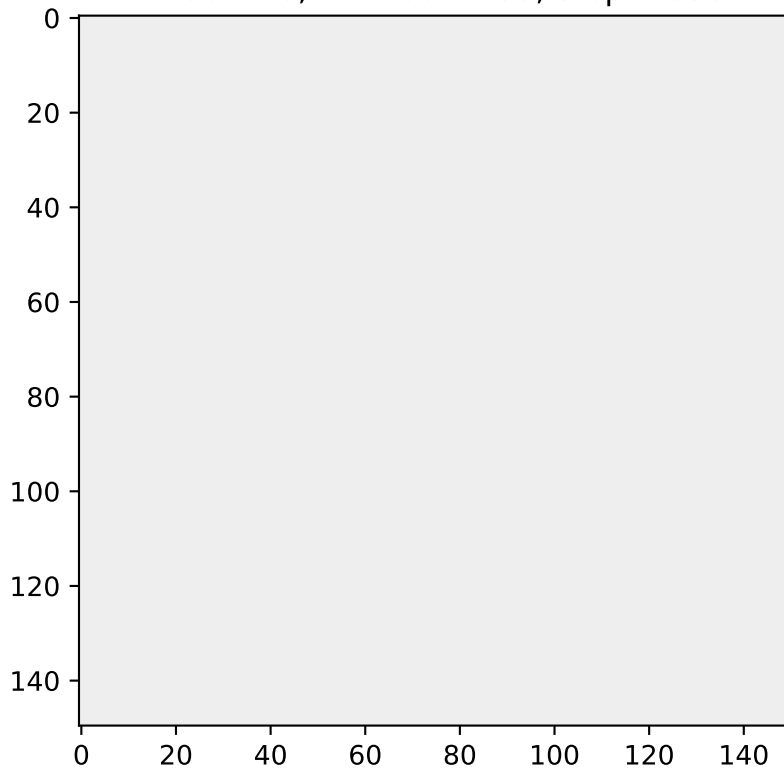
inf dur: 70, imm dur: 100, step: 2300.



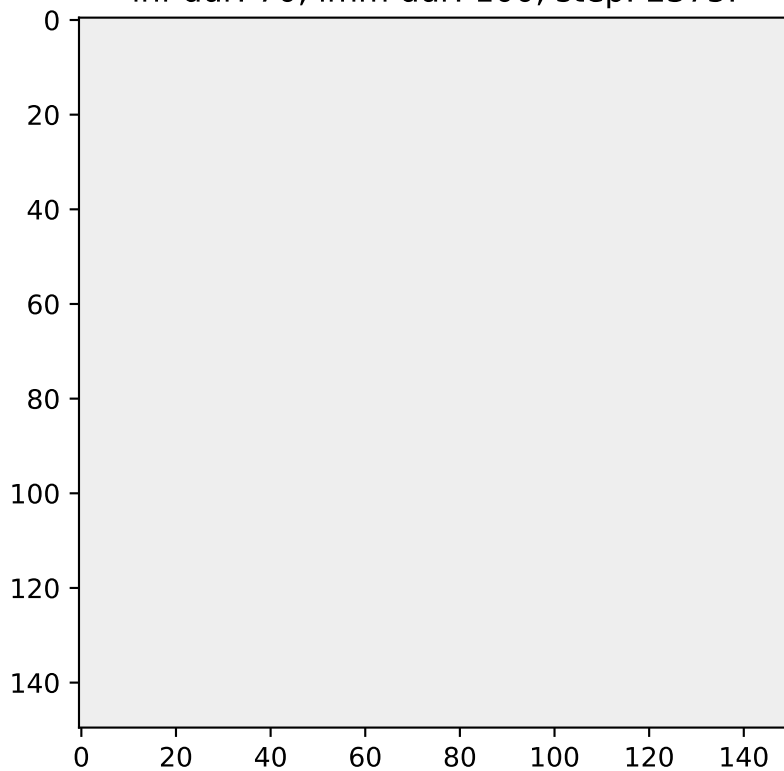
inf dur: 70, imm dur: 100, step: 2325.



inf dur: 70, imm dur: 100, step: 2350.

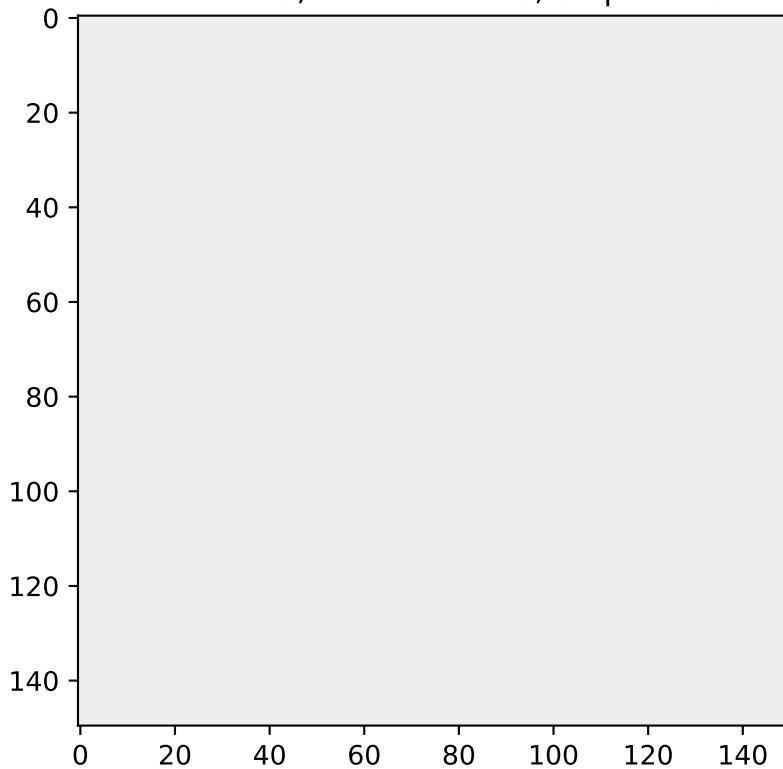


inf dur: 70, imm dur: 100, step: 2375.

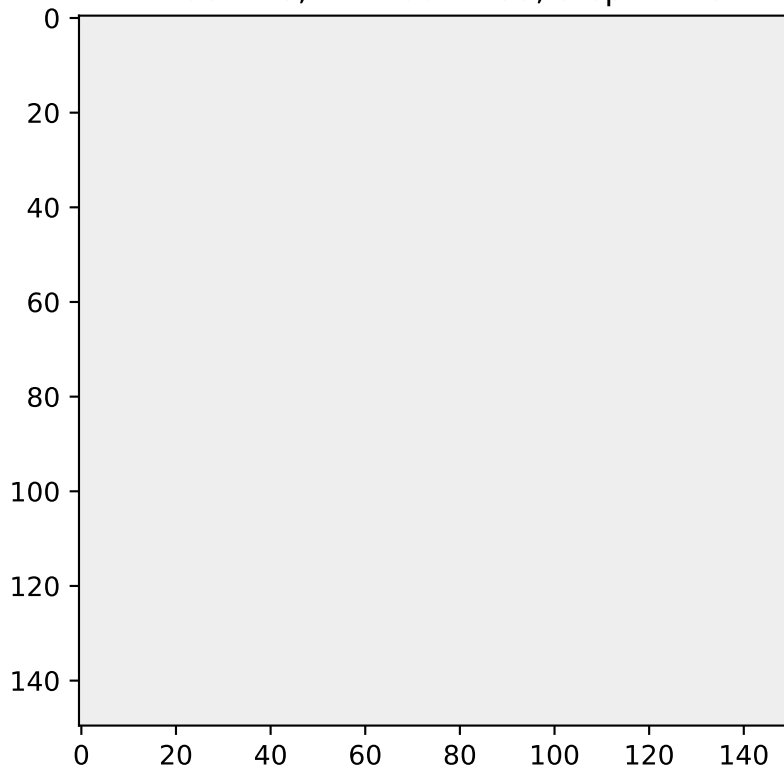




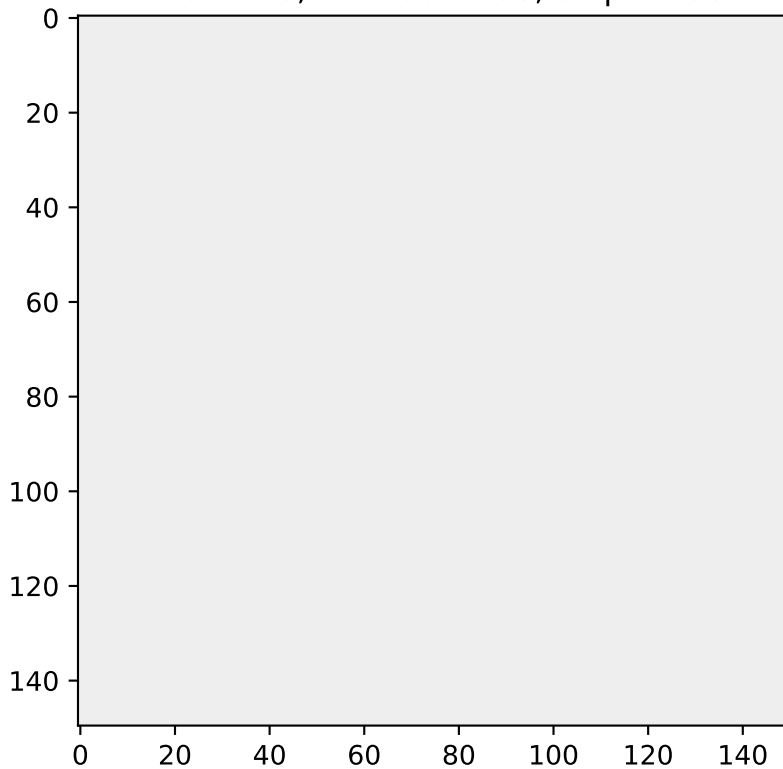
inf dur: 70, imm dur: 100, step: 2400.



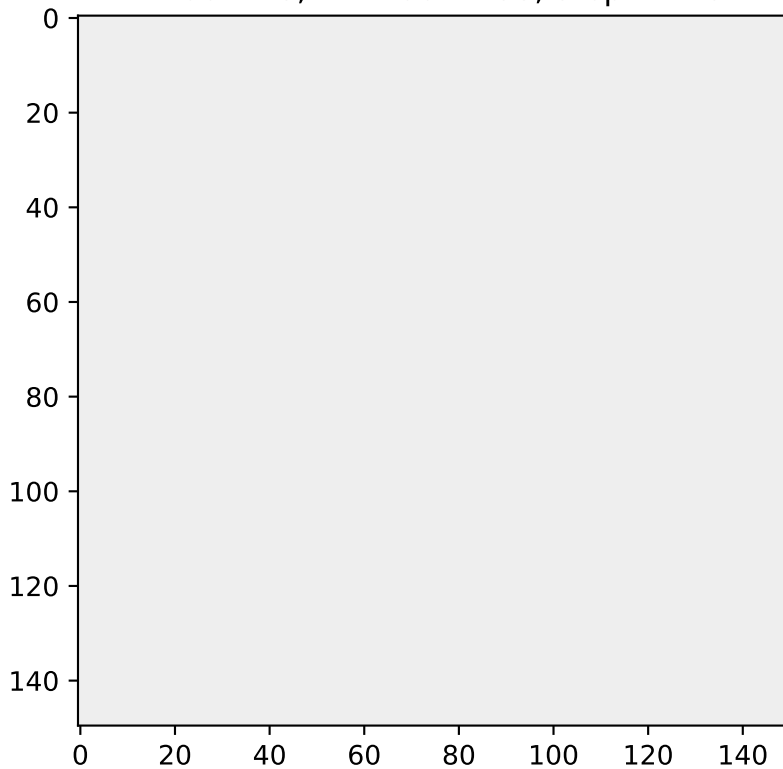
inf dur: 70, imm dur: 100, step: 2425.



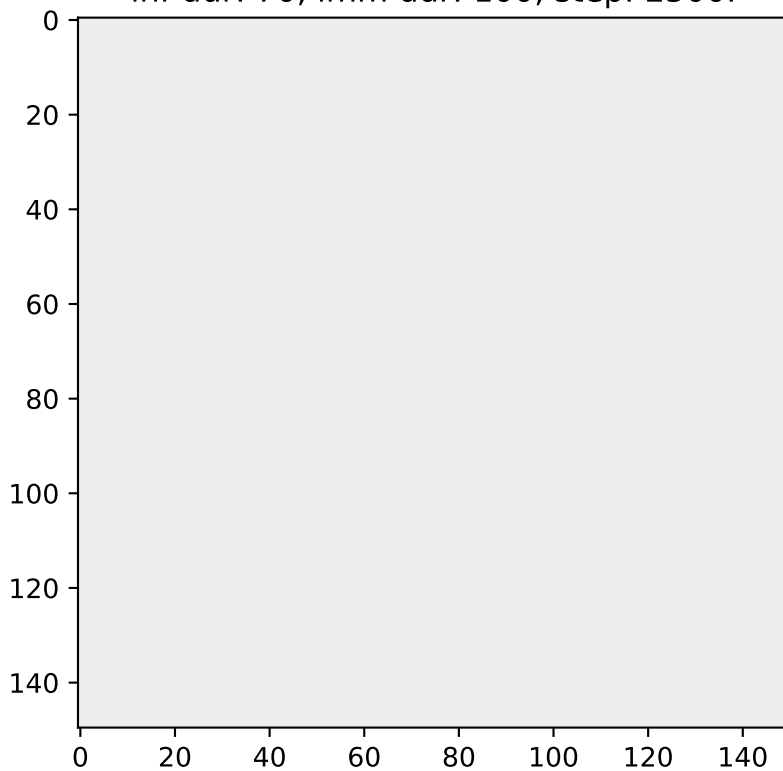
inf dur: 70, imm dur: 100, step: 2450.



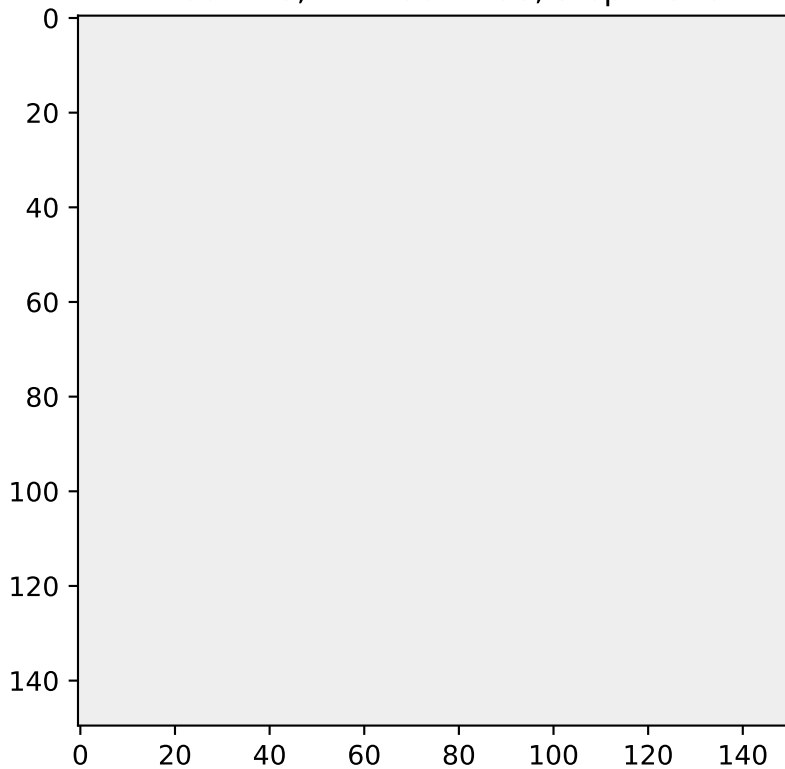
inf dur: 70, imm dur: 100, step: 2475.



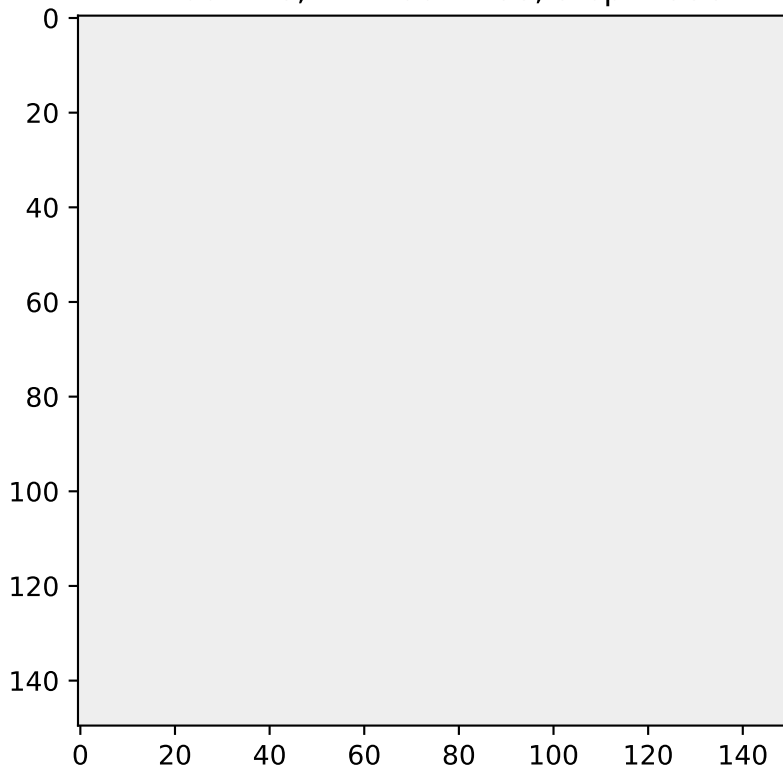
inf dur: 70, imm dur: 100, step: 2500.



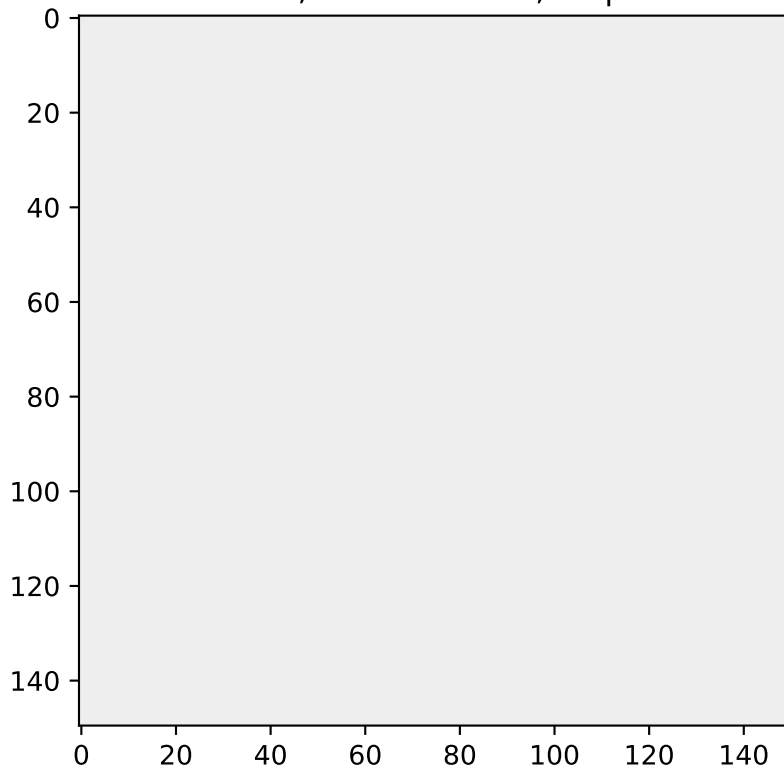
inf dur: 70, imm dur: 100, step: 2525.



inf dur: 70, imm dur: 100, step: 2550.

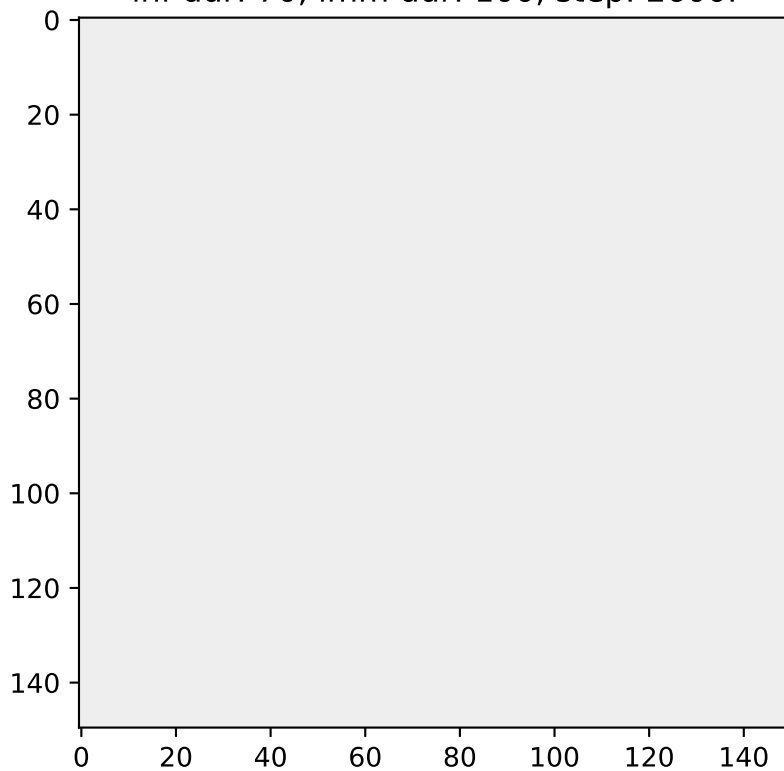


inf dur: 70, imm dur: 100, step: 2575.

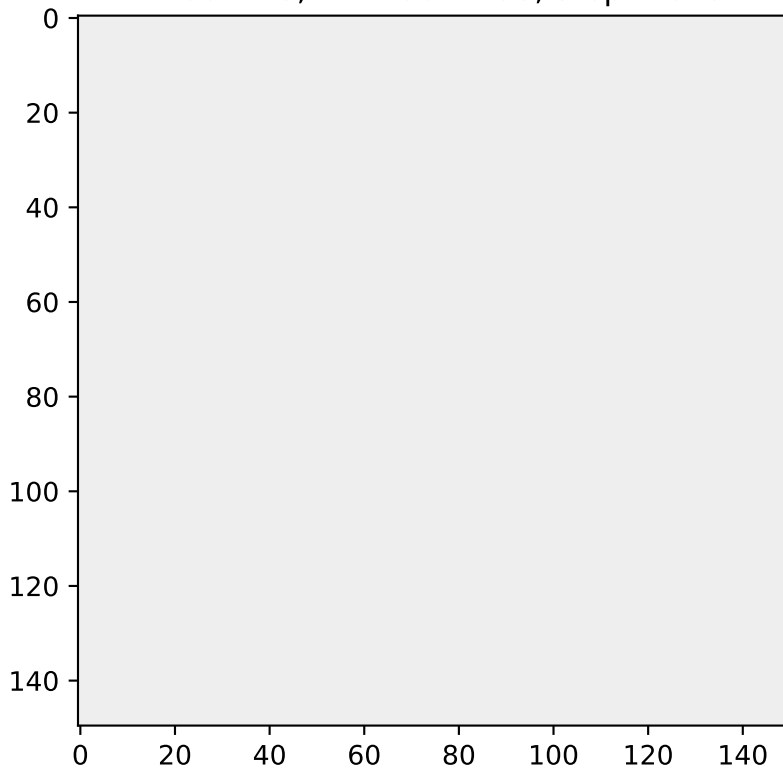




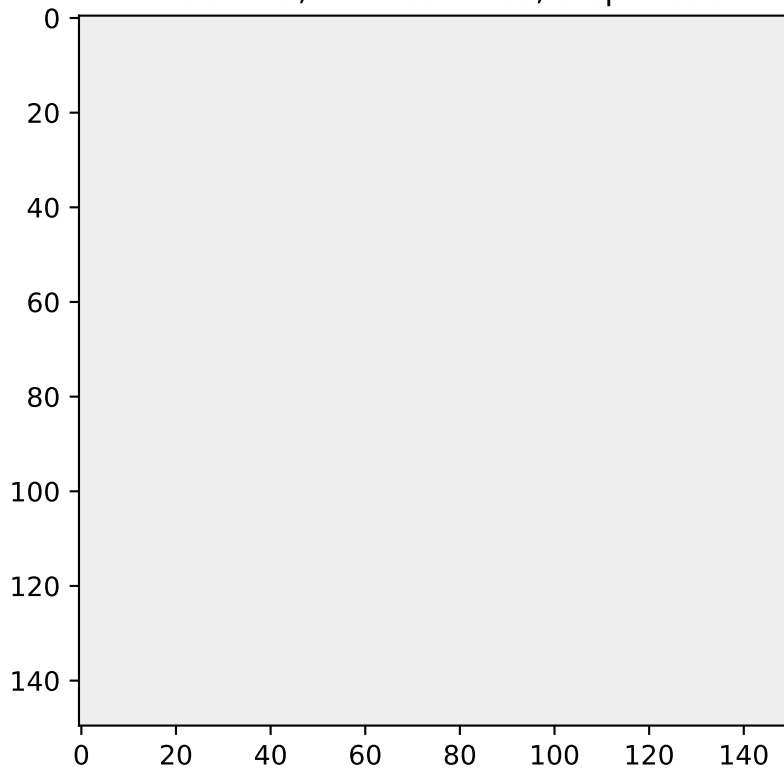
inf dur: 70, imm dur: 100, step: 2600.



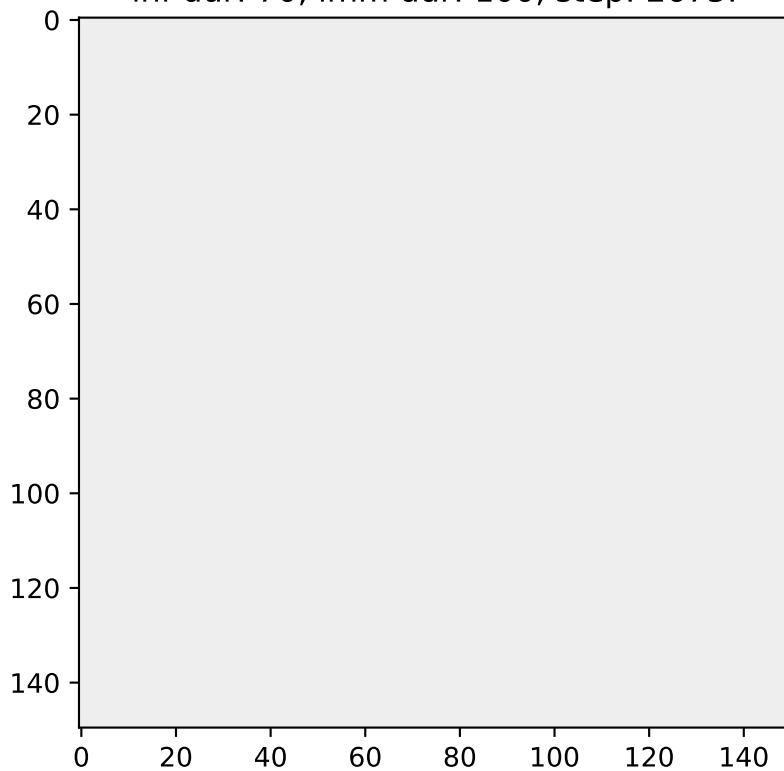
inf dur: 70, imm dur: 100, step: 2625.



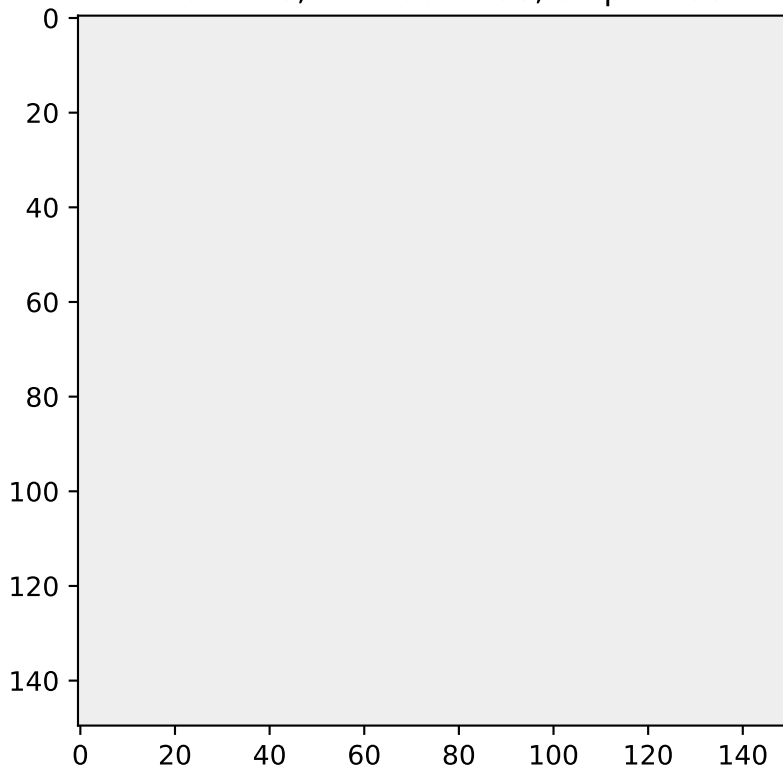
inf dur: 70, imm dur: 100, step: 2650.



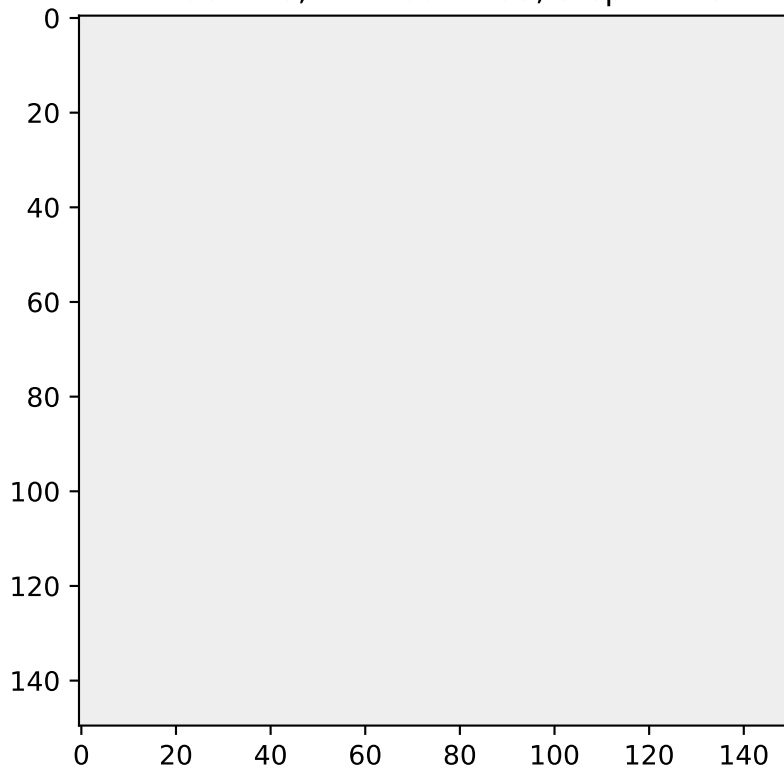
inf dur: 70, imm dur: 100, step: 2675.



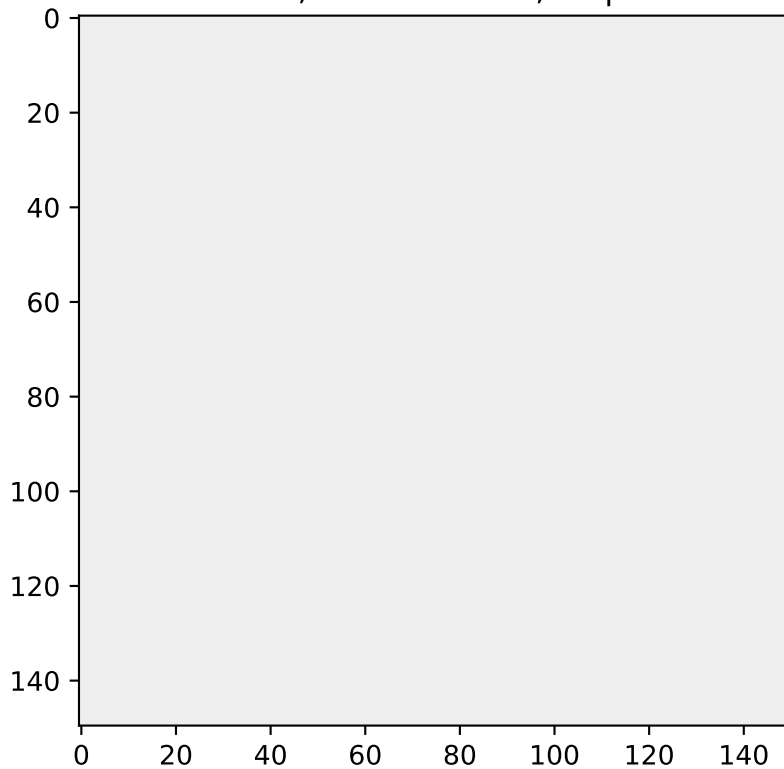
inf dur: 70, imm dur: 100, step: 2700.



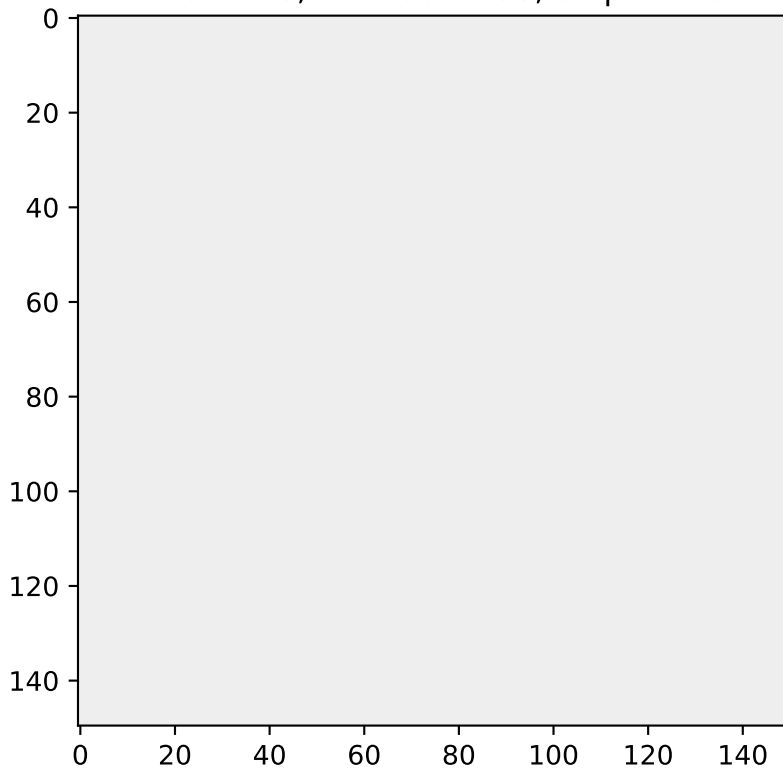
inf dur: 70, imm dur: 100, step: 2725.



inf dur: 70, imm dur: 100, step: 2750.

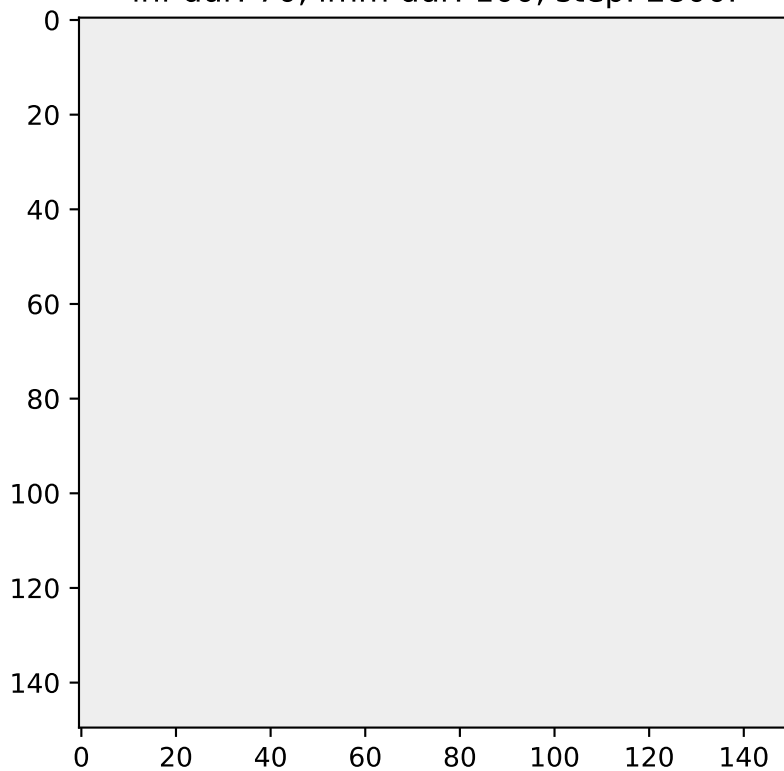


inf dur: 70, imm dur: 100, step: 2775.

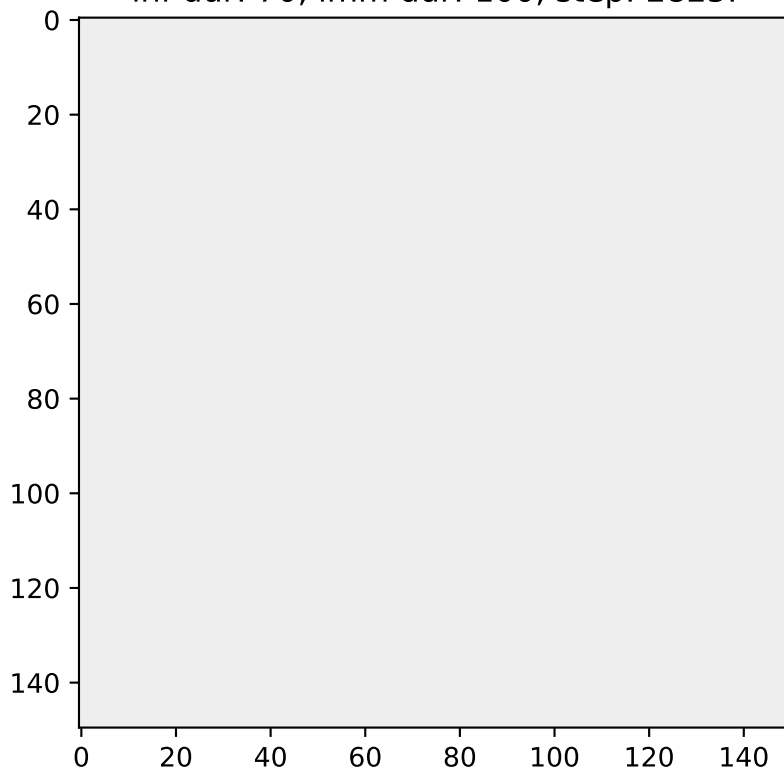




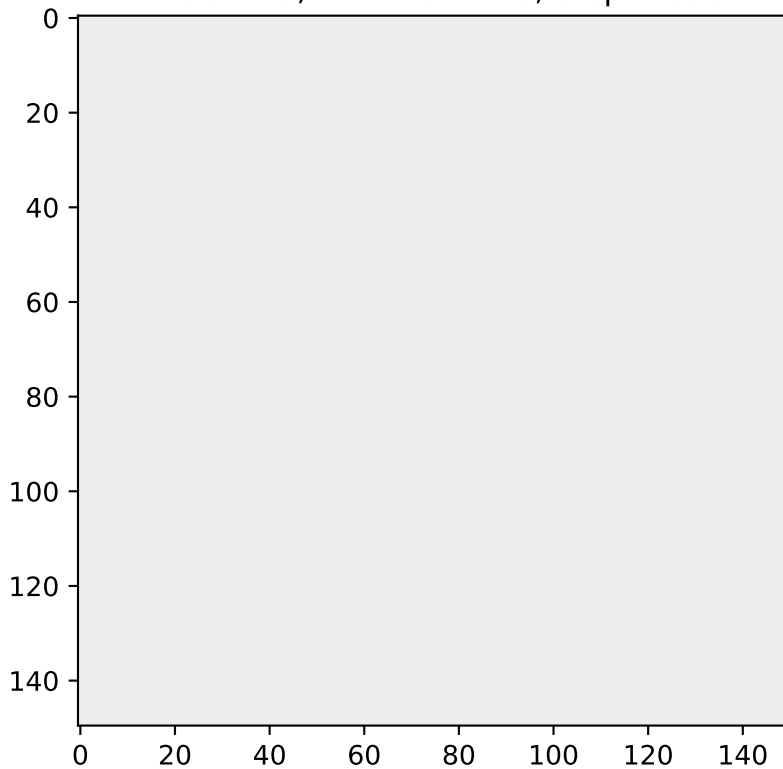
inf dur: 70, imm dur: 100, step: 2800.



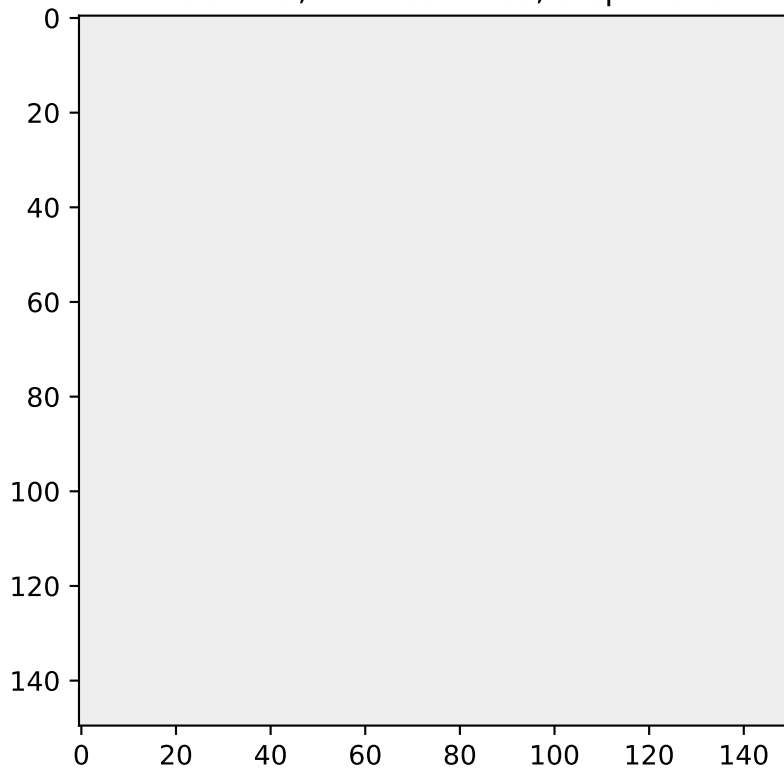
inf dur: 70, imm dur: 100, step: 2825.



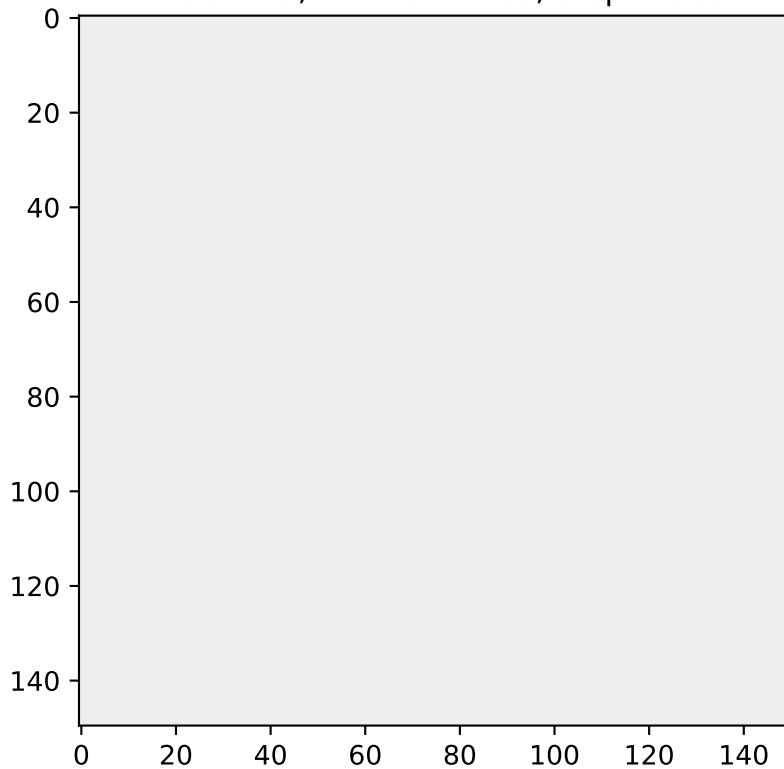
inf dur: 70, imm dur: 100, step: 2850.



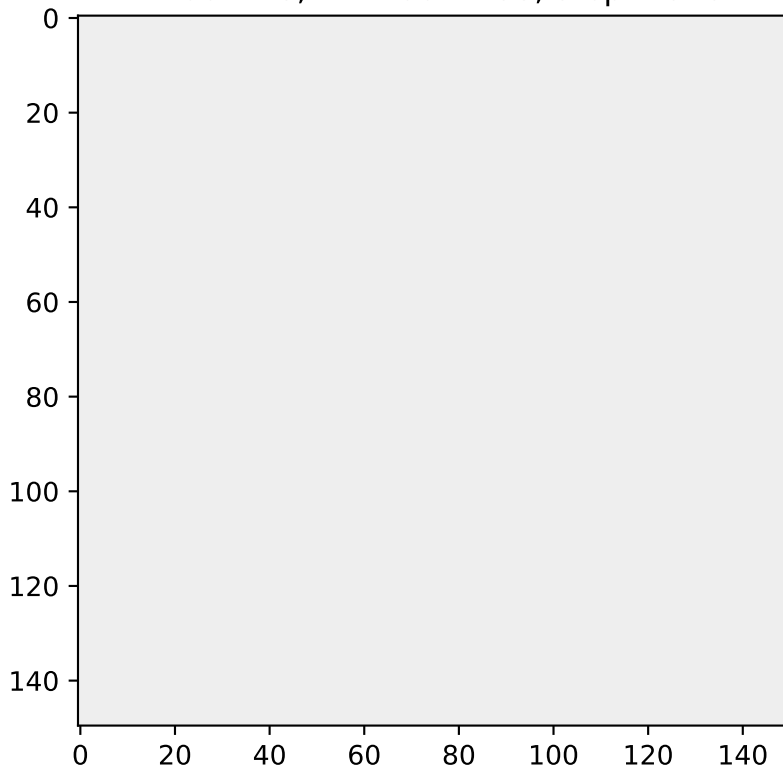
inf dur: 70, imm dur: 100, step: 2875.



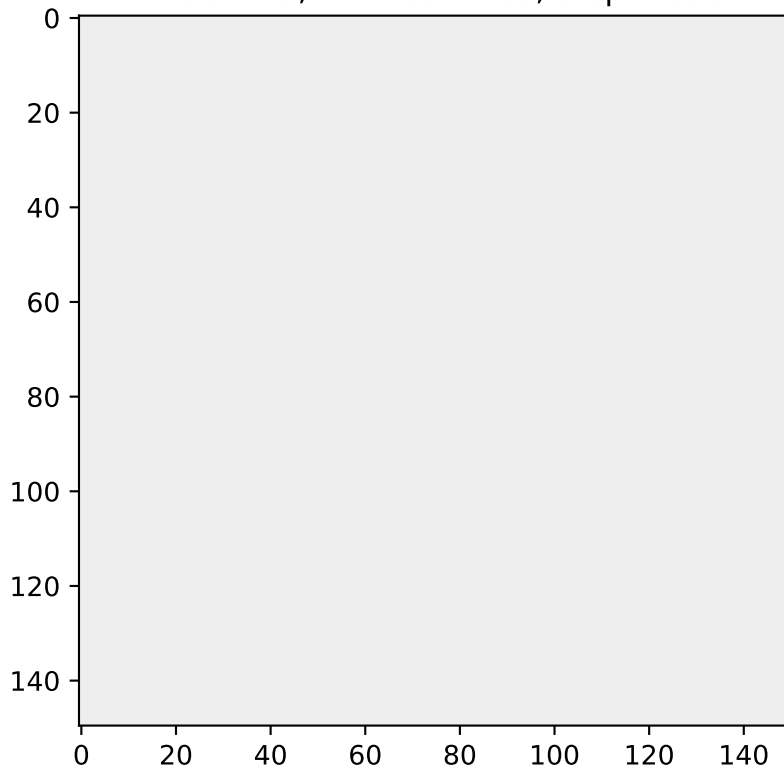
inf dur: 70, imm dur: 100, step: 2900.



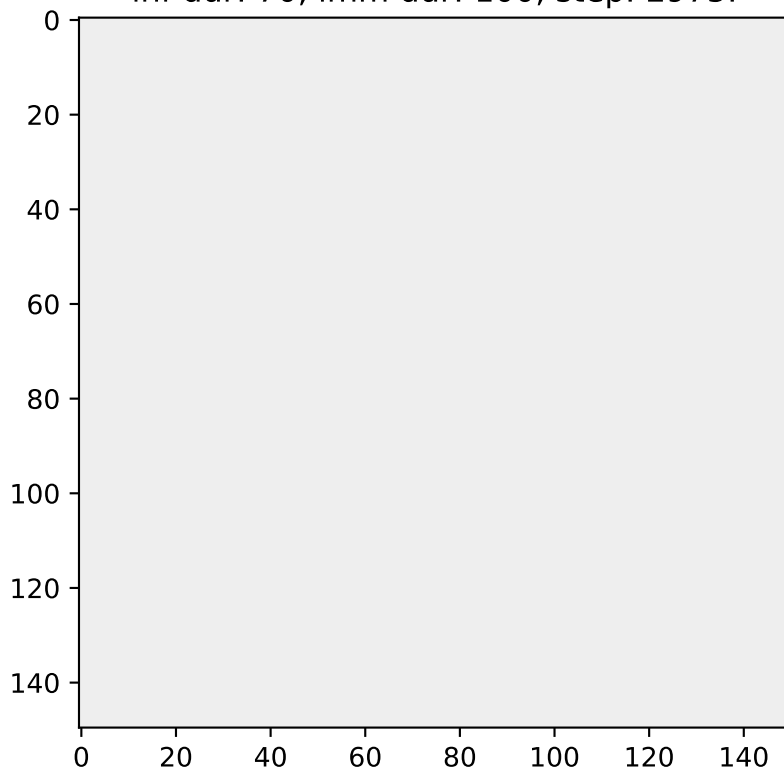
inf dur: 70, imm dur: 100, step: 2925.



inf dur: 70, imm dur: 100, step: 2950.

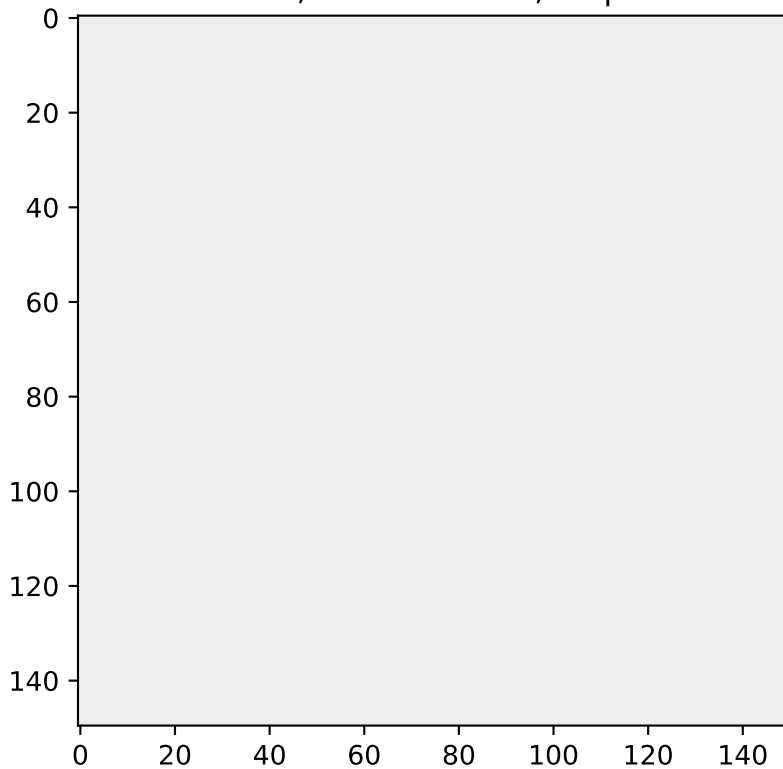


inf dur: 70, imm dur: 100, step: 2975.

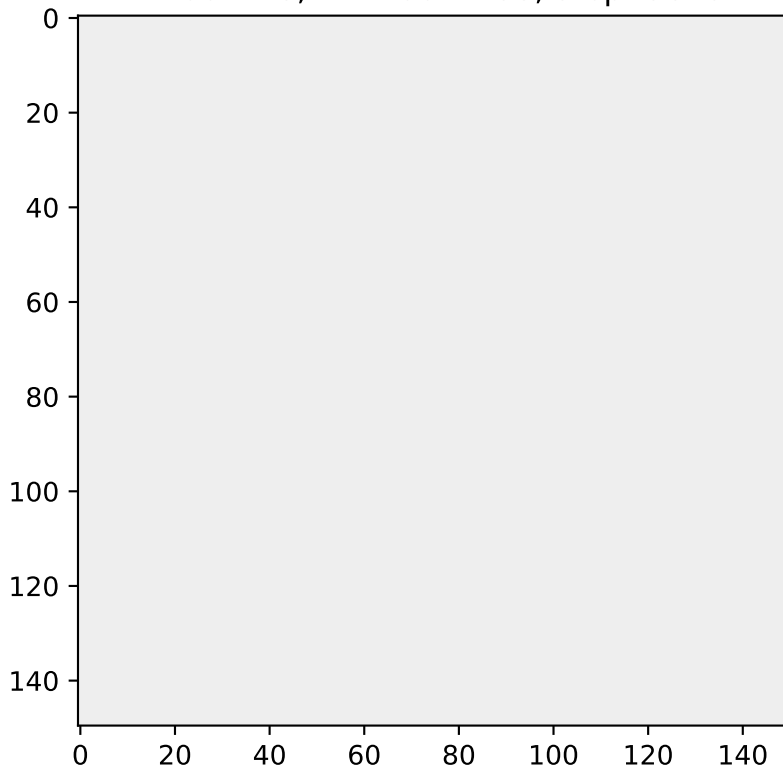




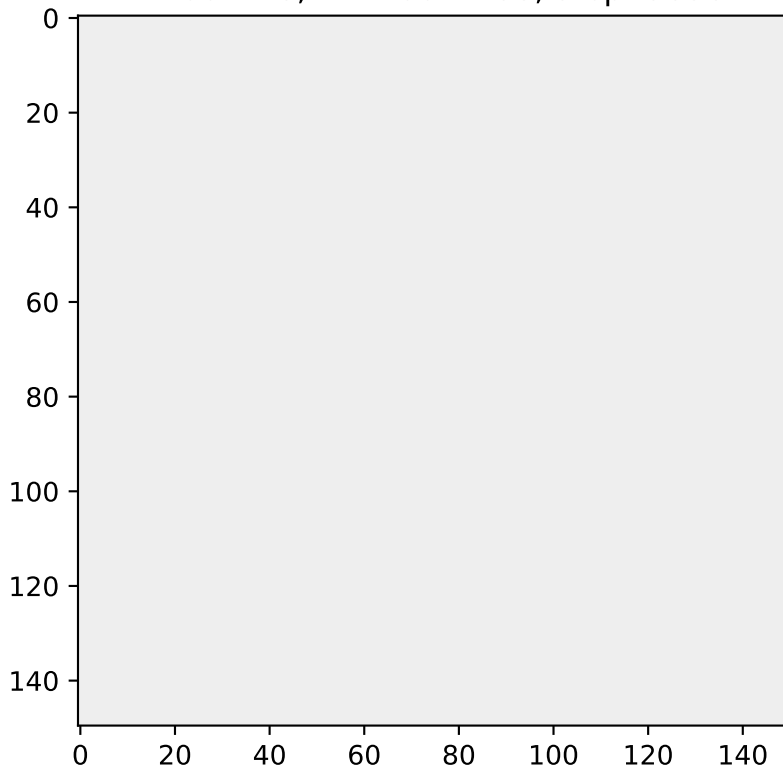
inf dur: 70, imm dur: 100, step: 3000.



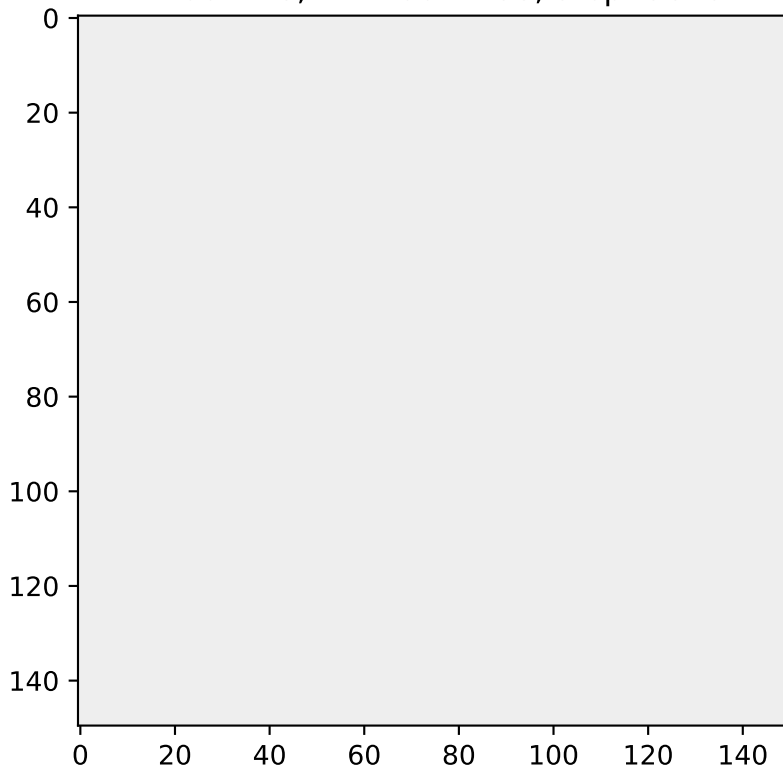
inf dur: 70, imm dur: 100, step: 3025.



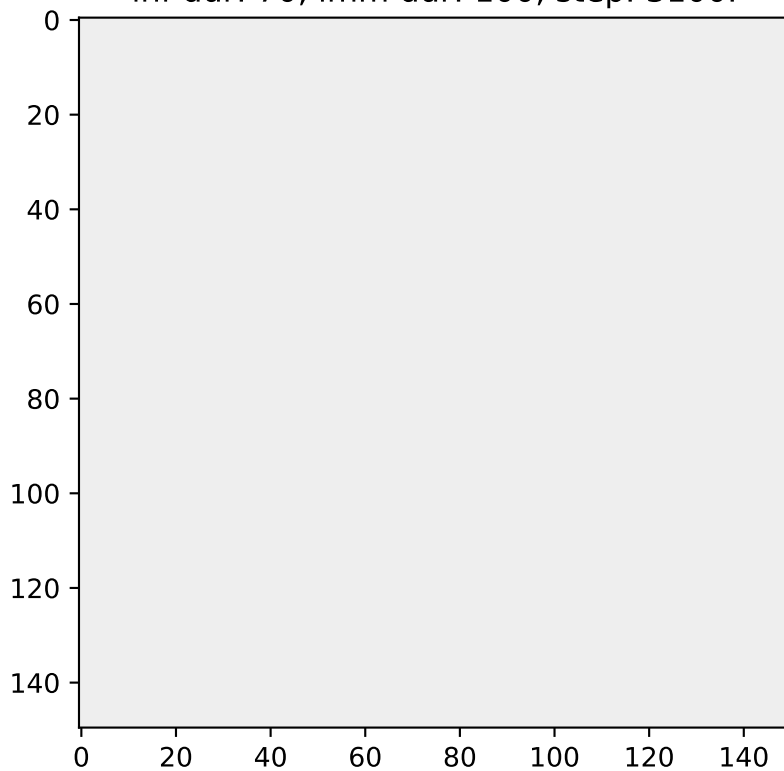
inf dur: 70, imm dur: 100, step: 3050.



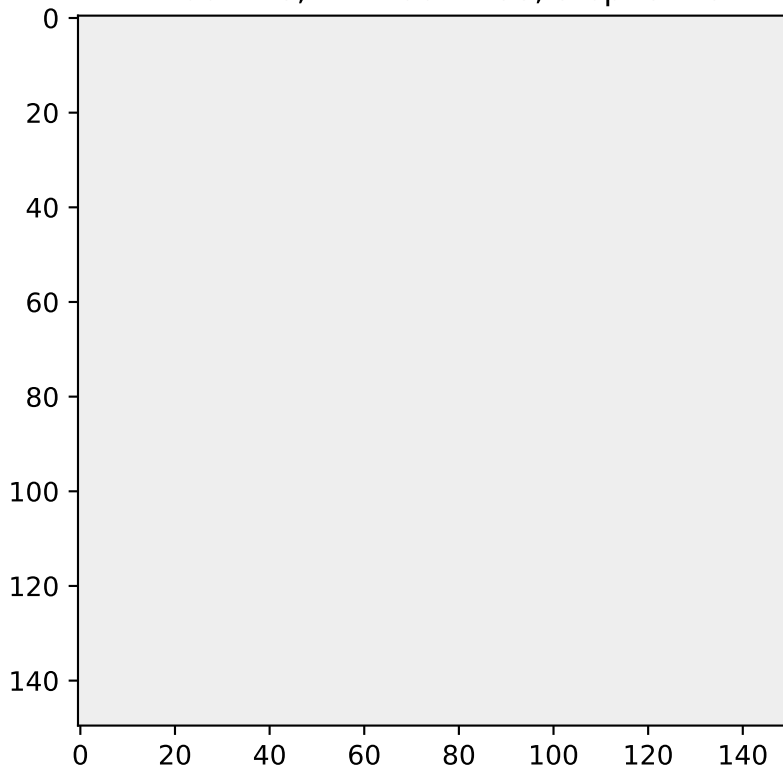
inf dur: 70, imm dur: 100, step: 3075.



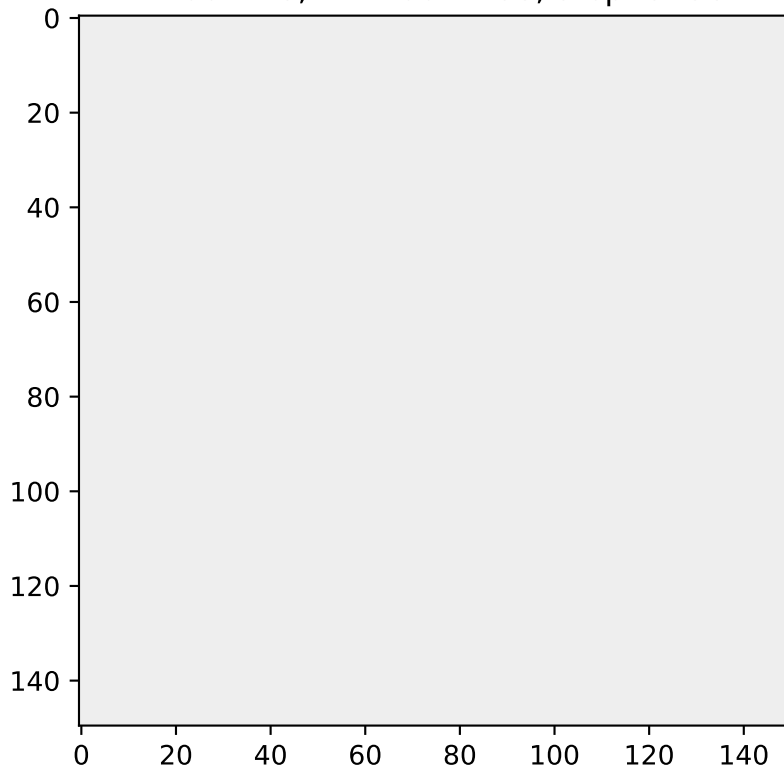
inf dur: 70, imm dur: 100, step: 3100.



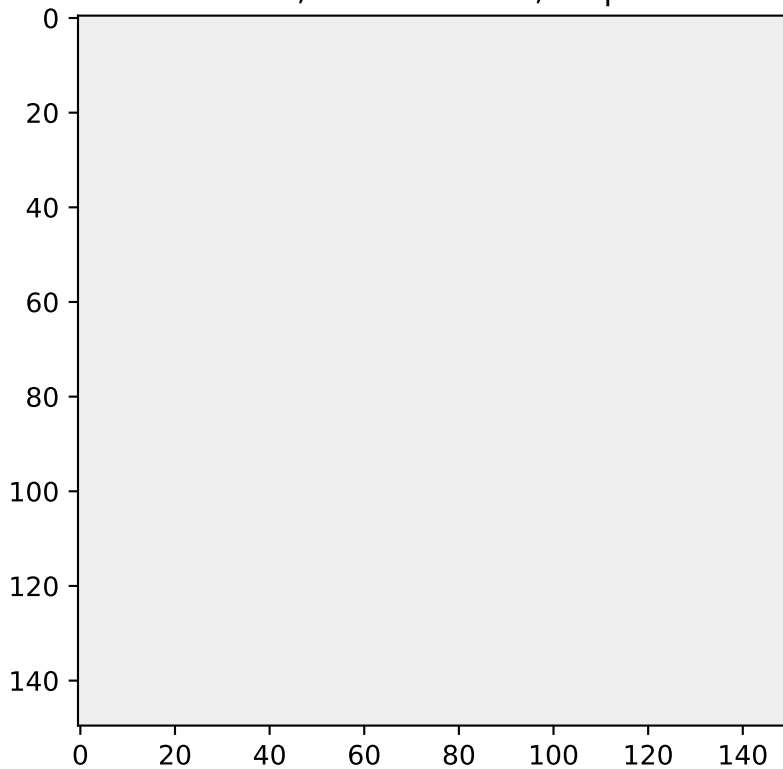
inf dur: 70, imm dur: 100, step: 3125.



inf dur: 70, imm dur: 100, step: 3150.

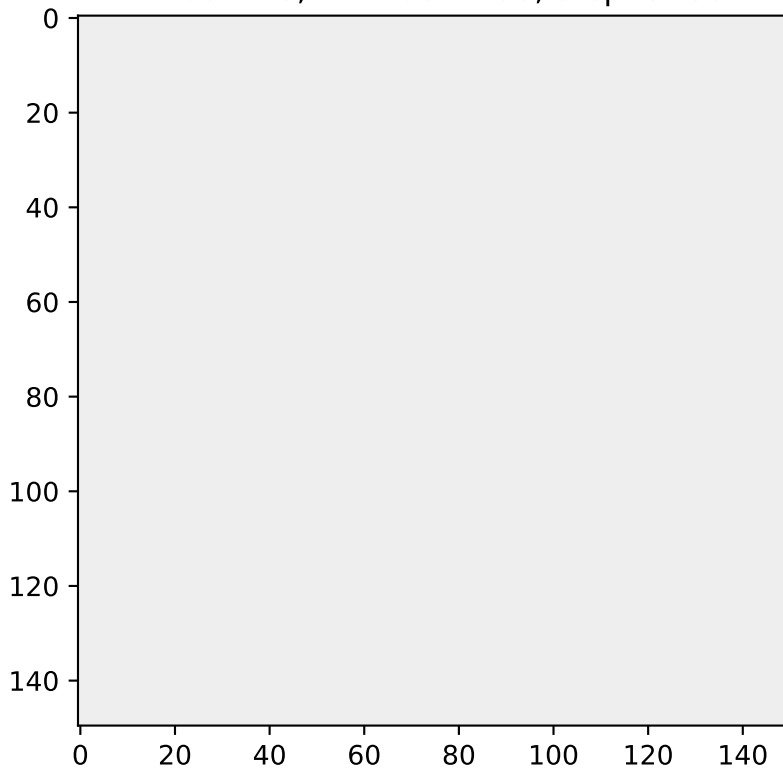


inf dur: 70, imm dur: 100, step: 3175.

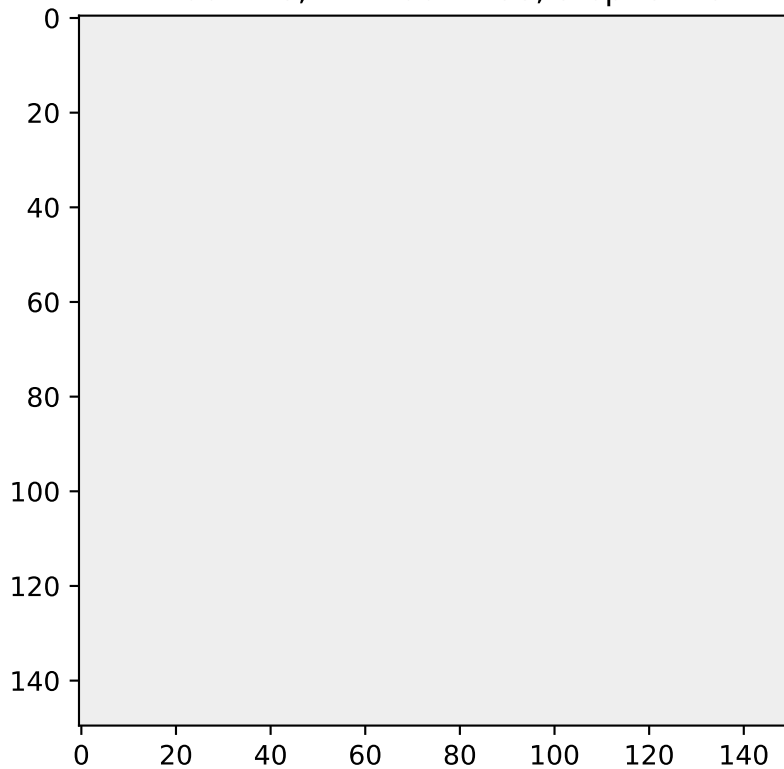




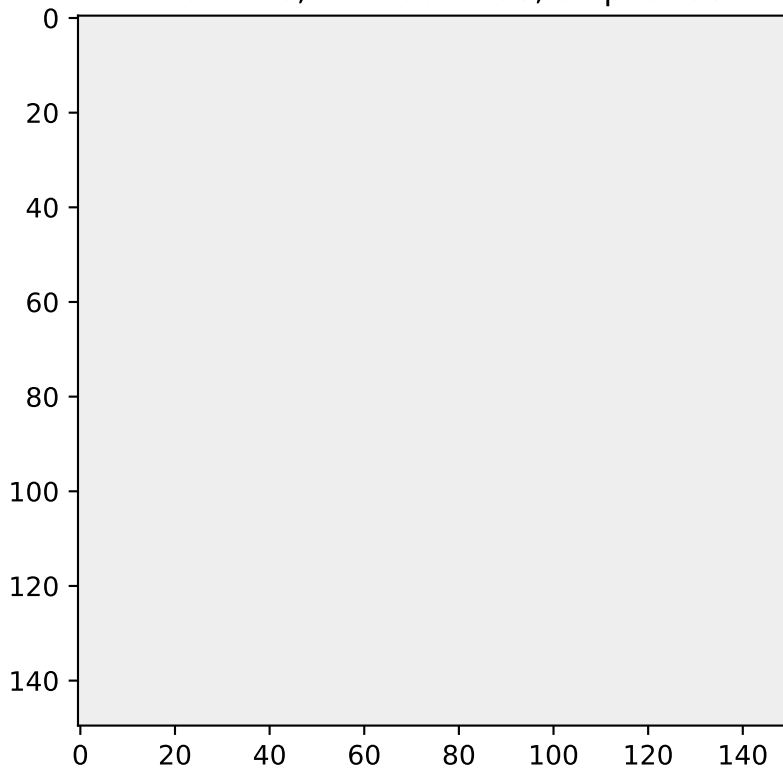
inf dur: 70, imm dur: 100, step: 3200.



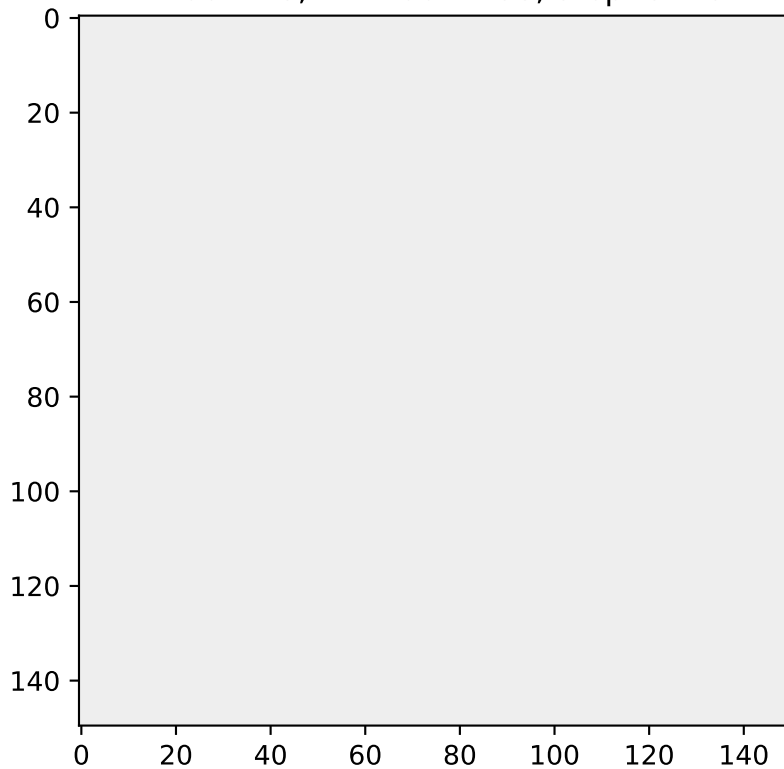
inf dur: 70, imm dur: 100, step: 3225.



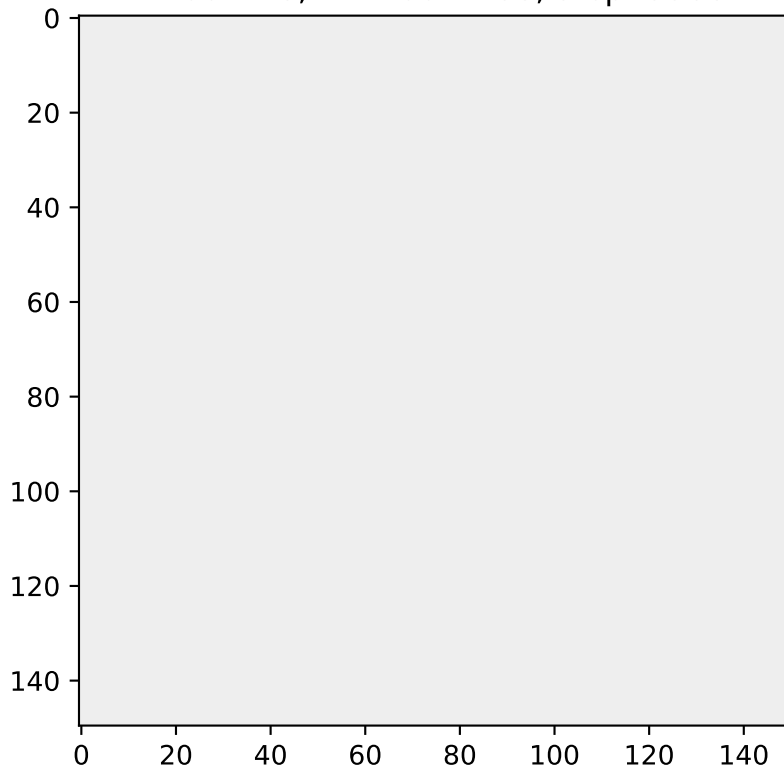
inf dur: 70, imm dur: 100, step: 3250.



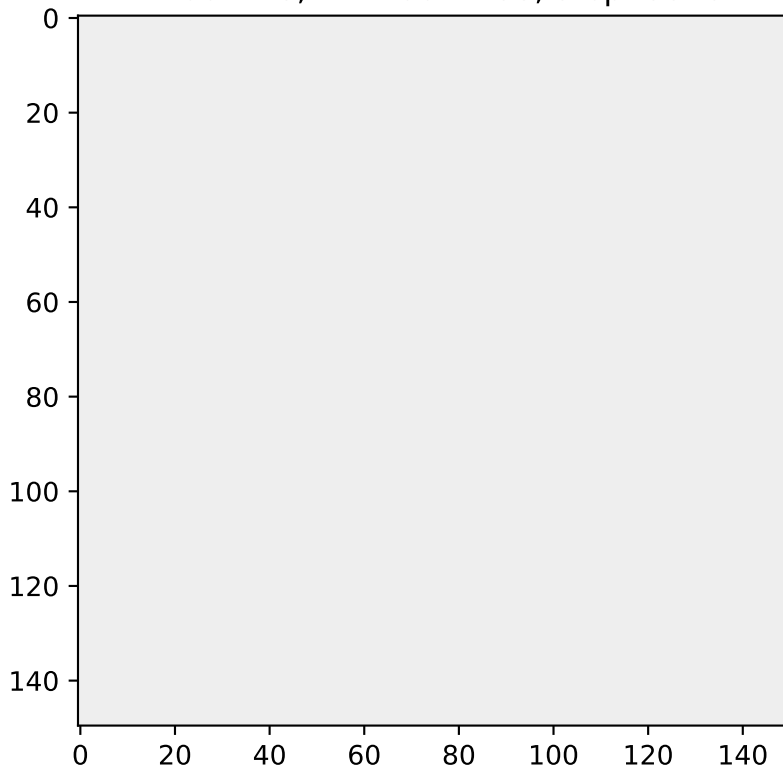
inf dur: 70, imm dur: 100, step: 3275.



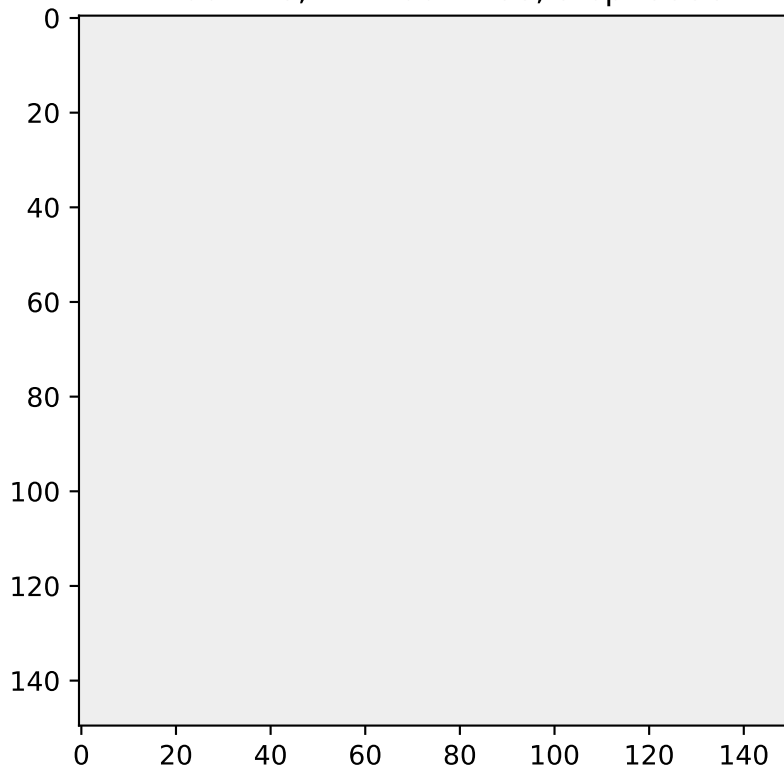
inf dur: 70, imm dur: 100, step: 3300.



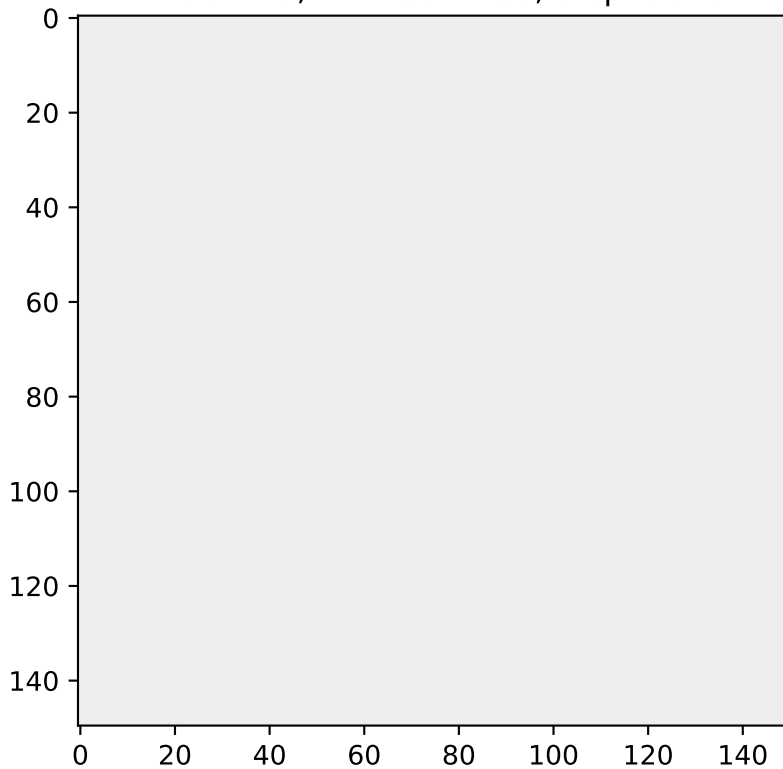
inf dur: 70, imm dur: 100, step: 3325.



inf dur: 70, imm dur: 100, step: 3350.

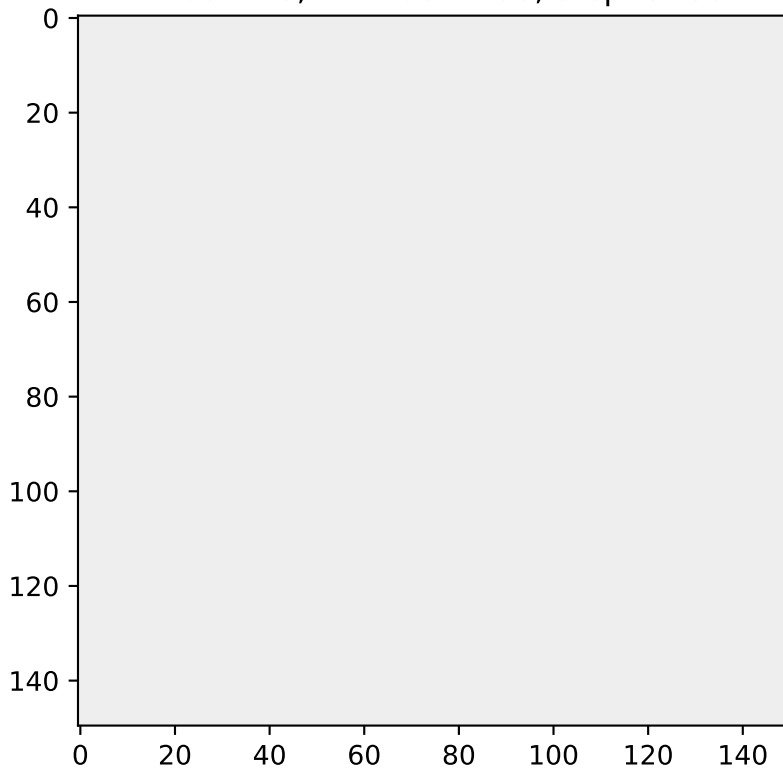


inf dur: 70, imm dur: 100, step: 3375.

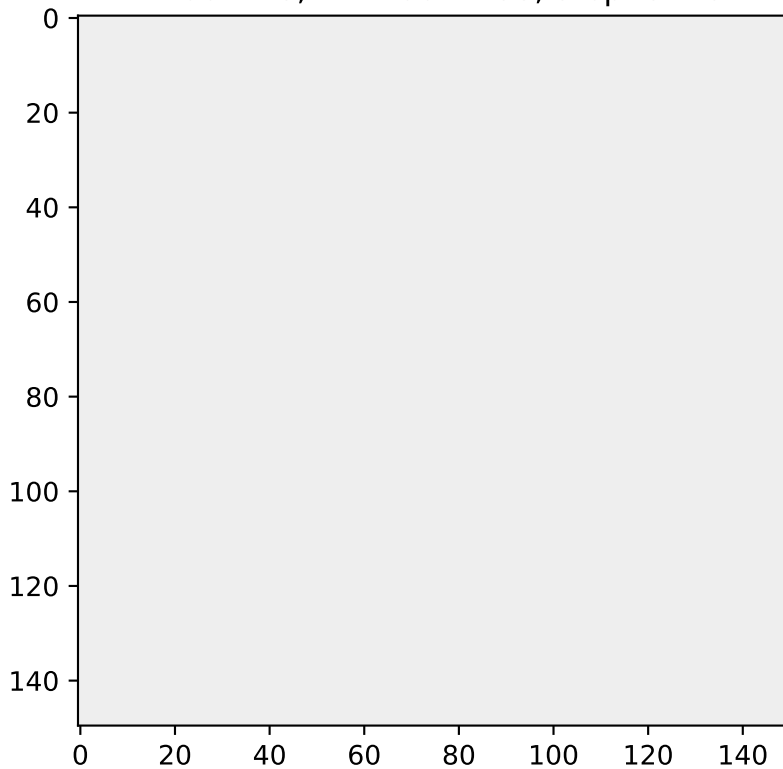




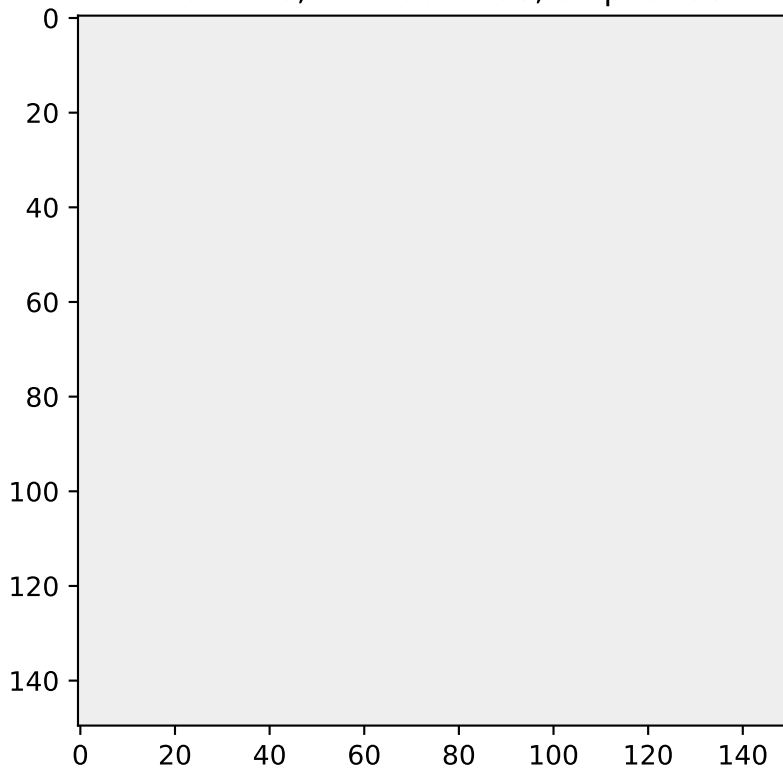
inf dur: 70, imm dur: 100, step: 3400.



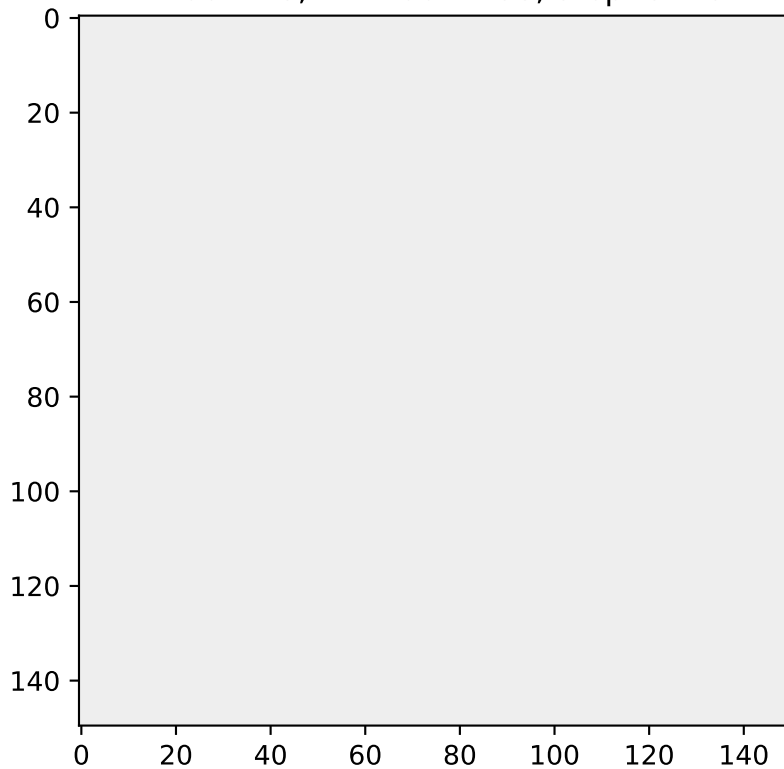
inf dur: 70, imm dur: 100, step: 3425.



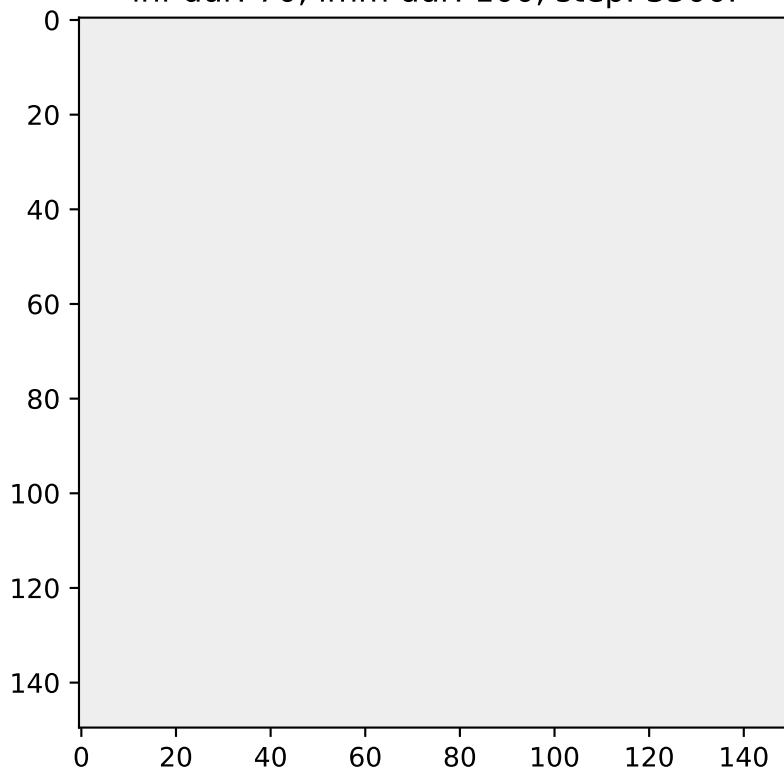
inf dur: 70, imm dur: 100, step: 3450.



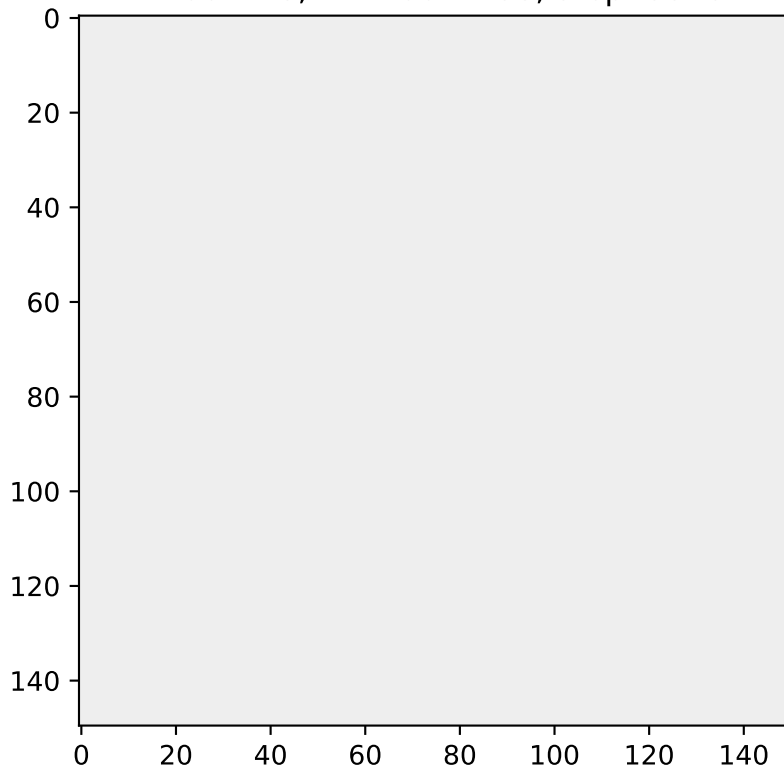
inf dur: 70, imm dur: 100, step: 3475.



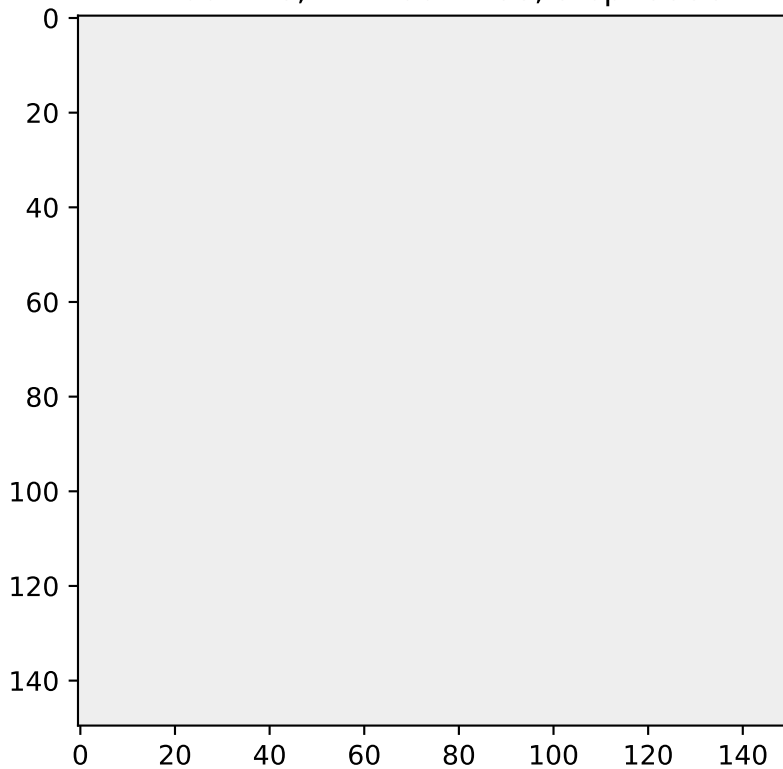
inf dur: 70, imm dur: 100, step: 3500.



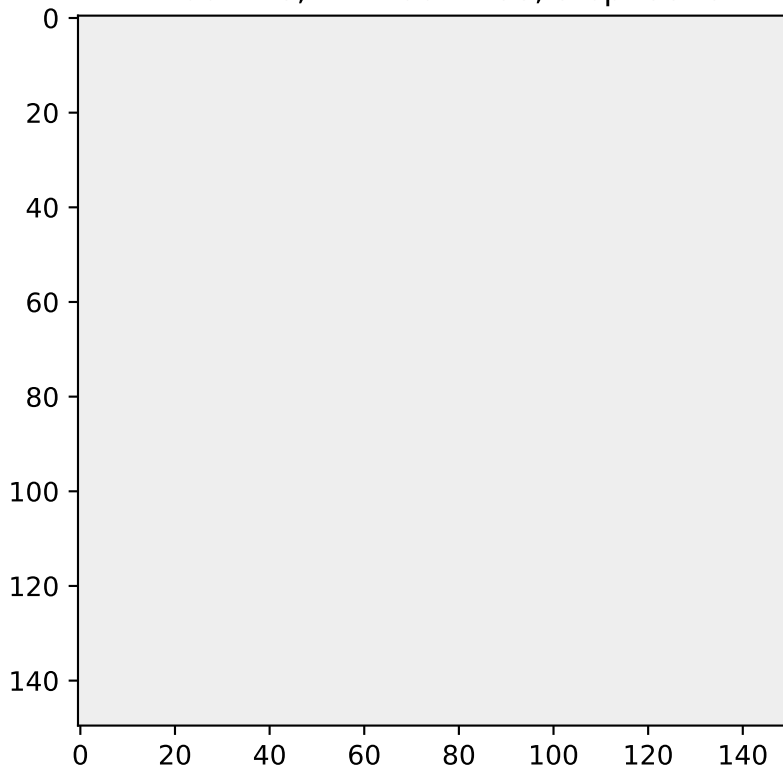
inf dur: 70, imm dur: 100, step: 3525.



inf dur: 70, imm dur: 100, step: 3550.

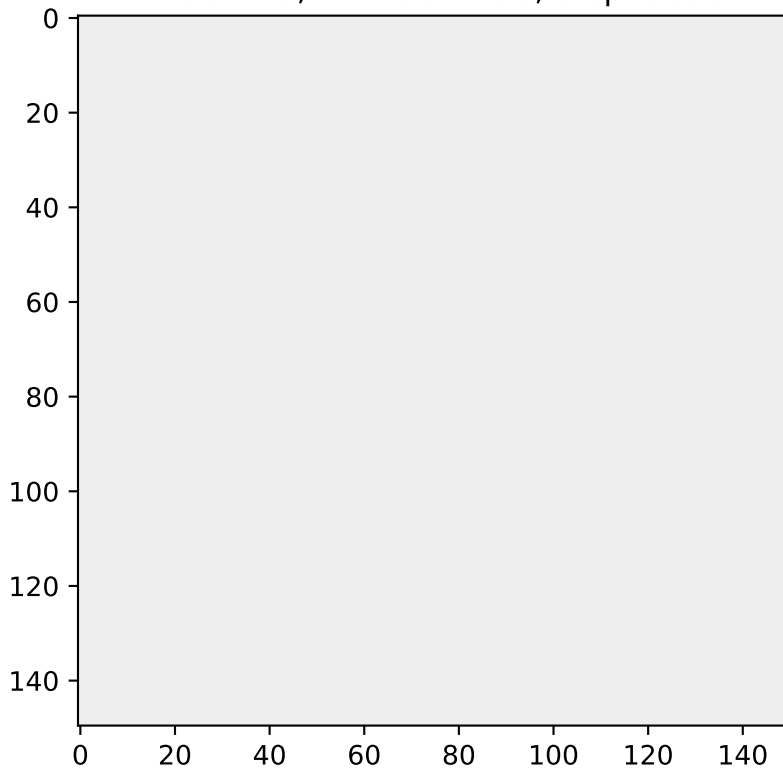


inf dur: 70, imm dur: 100, step: 3575.

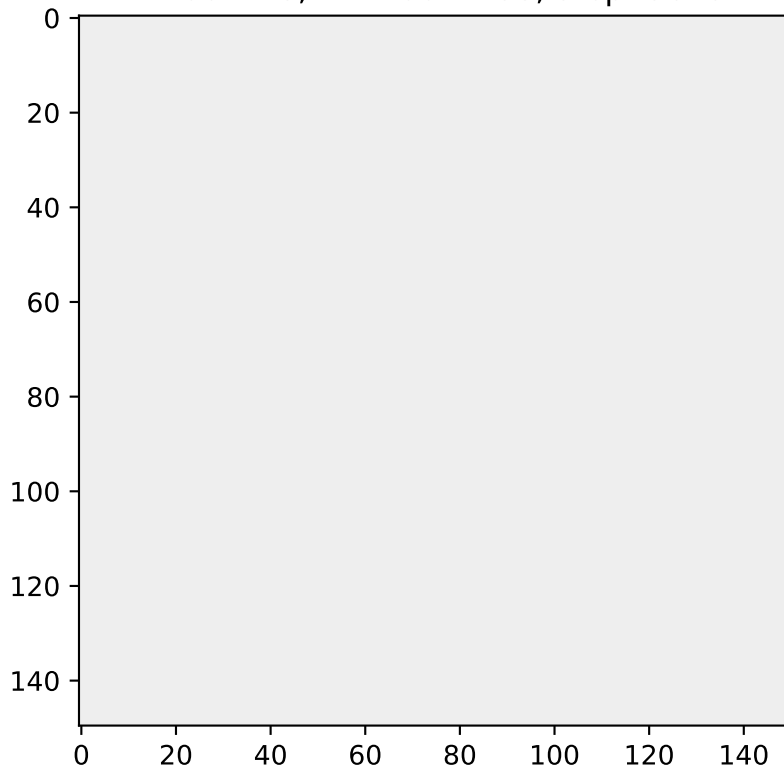




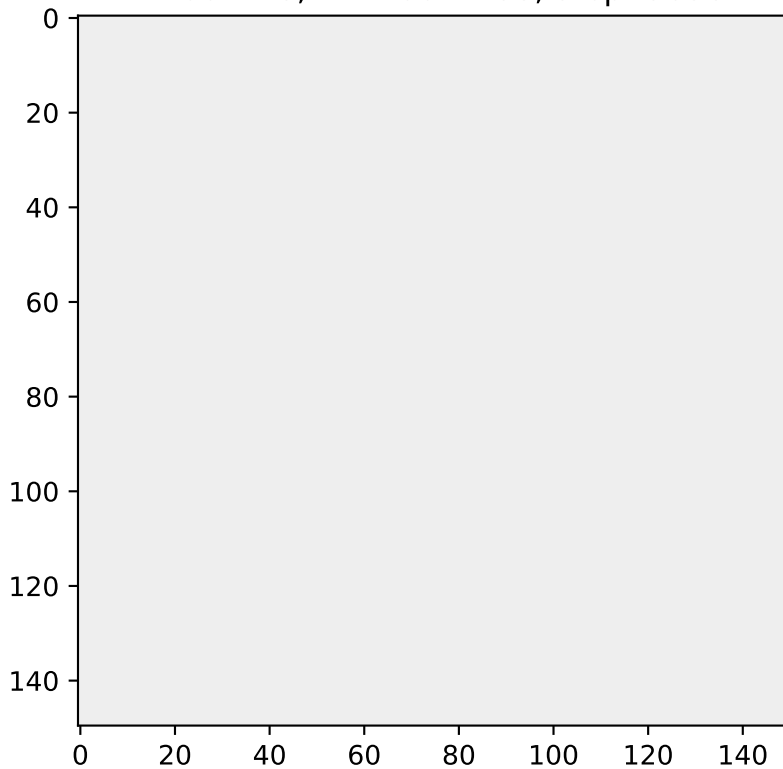
inf dur: 70, imm dur: 100, step: 3600.



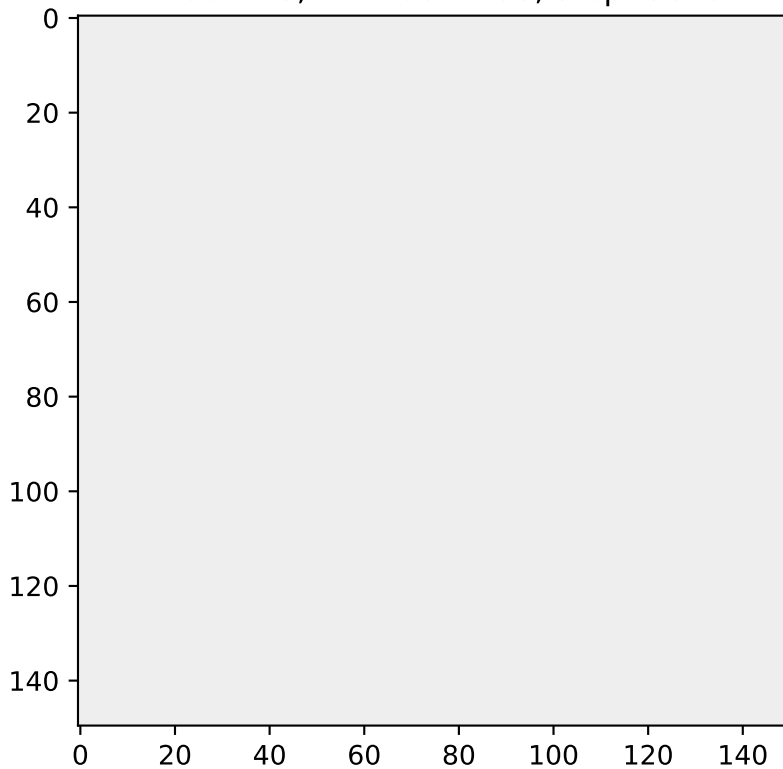
inf dur: 70, imm dur: 100, step: 3625.



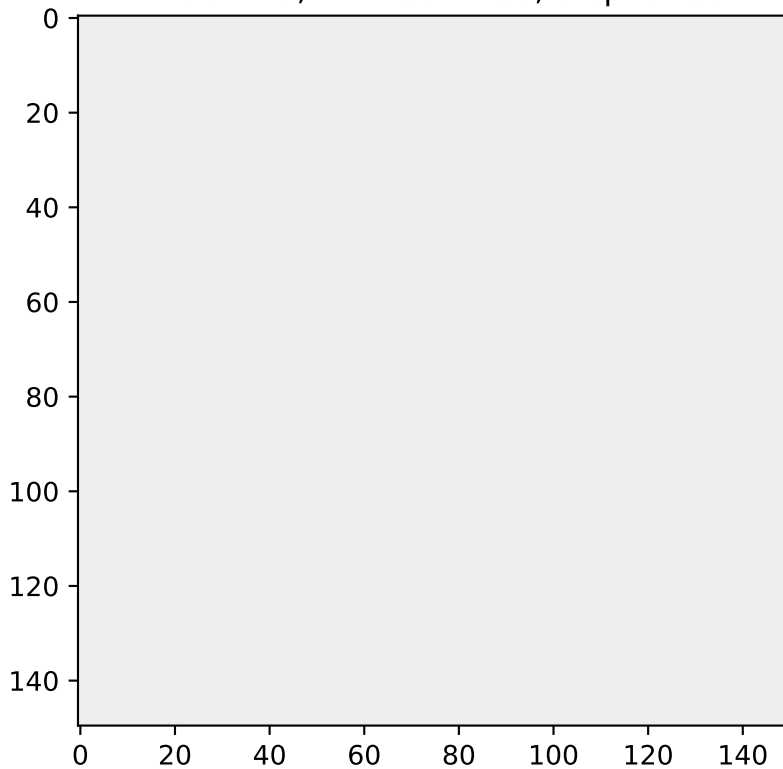
inf dur: 70, imm dur: 100, step: 3650.



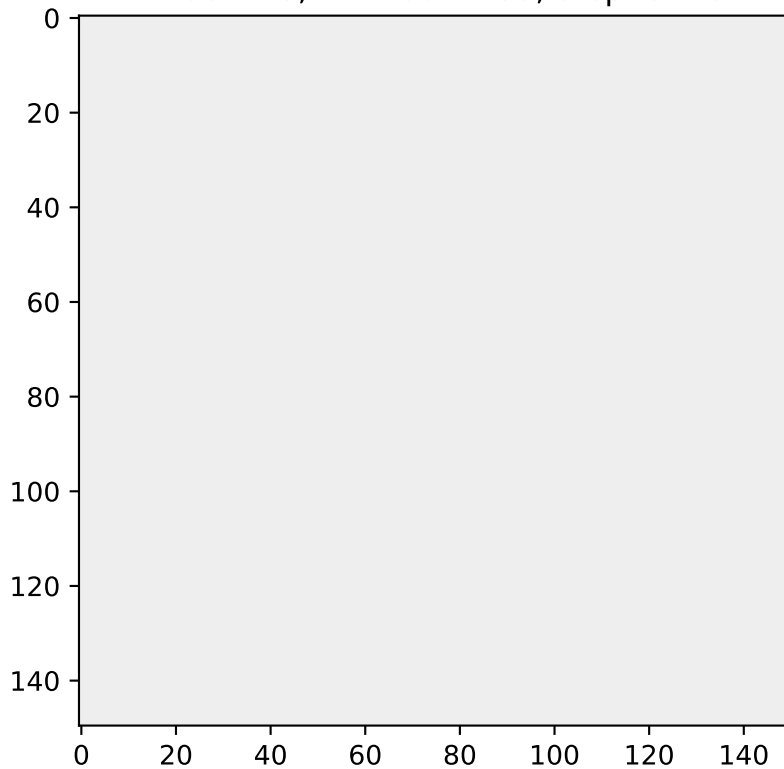
inf dur: 70, imm dur: 100, step: 3675.



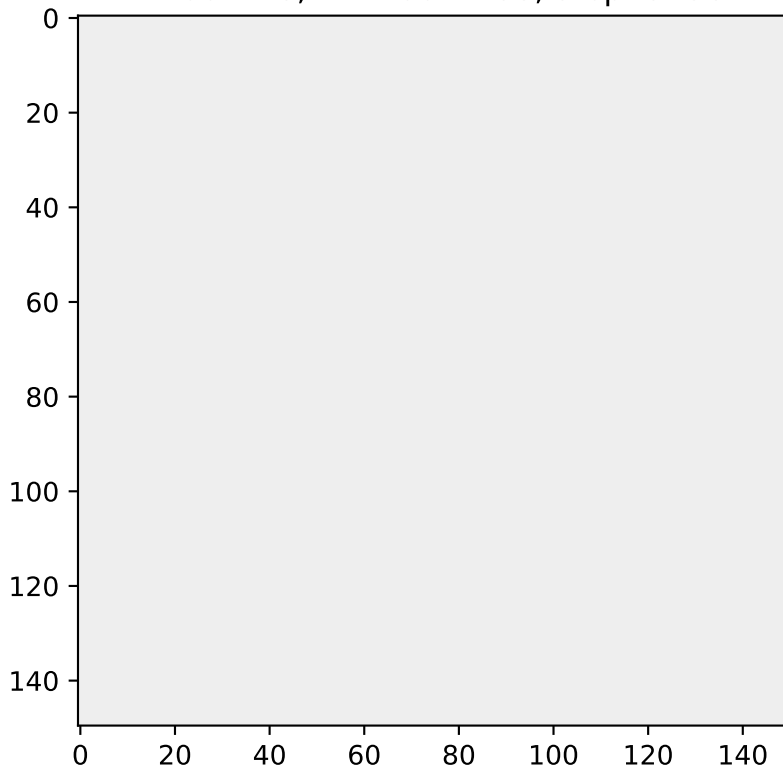
inf dur: 70, imm dur: 100, step: 3700.



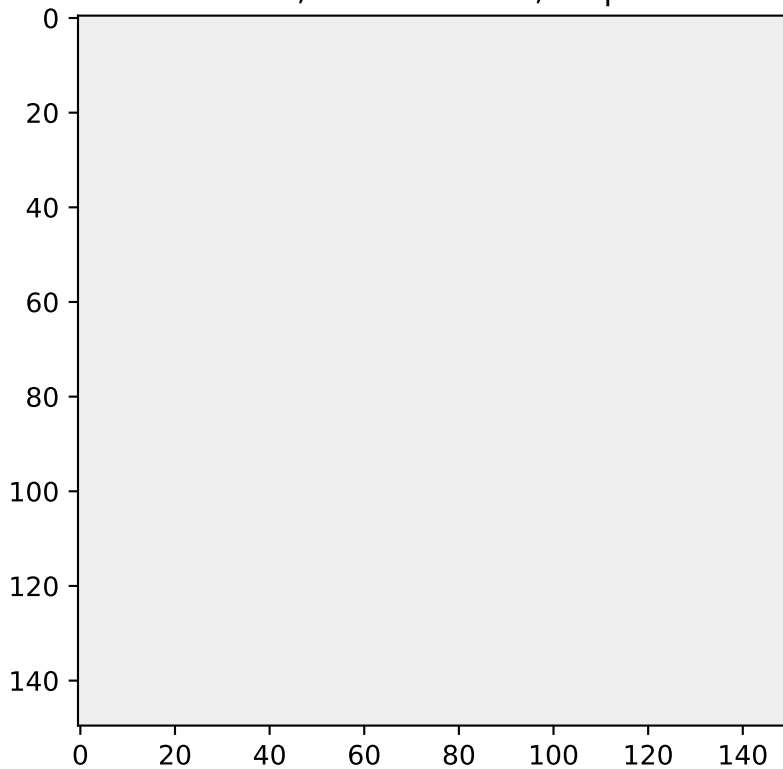
inf dur: 70, imm dur: 100, step: 3725.



inf dur: 70, imm dur: 100, step: 3750.

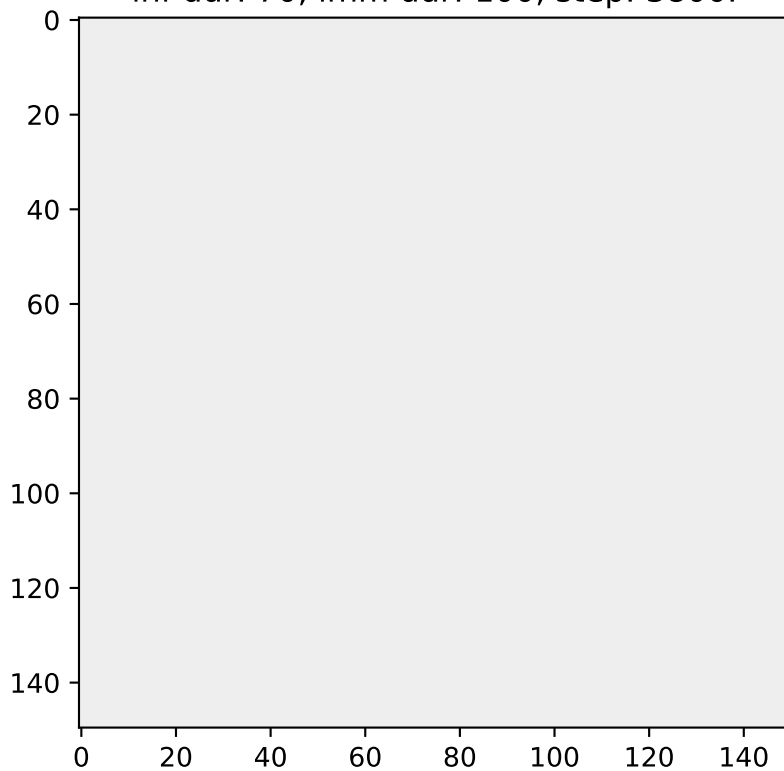


inf dur: 70, imm dur: 100, step: 3775.

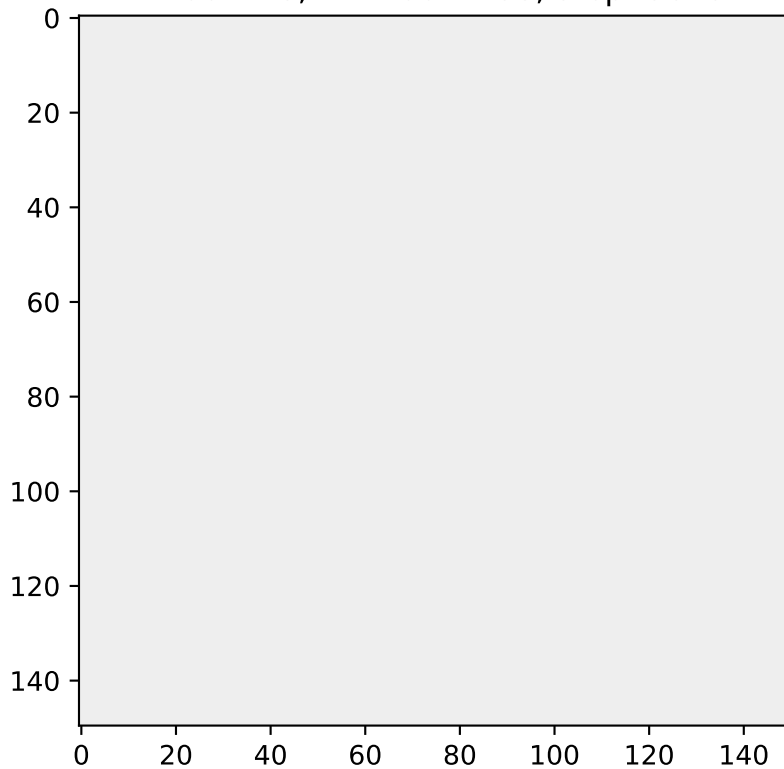




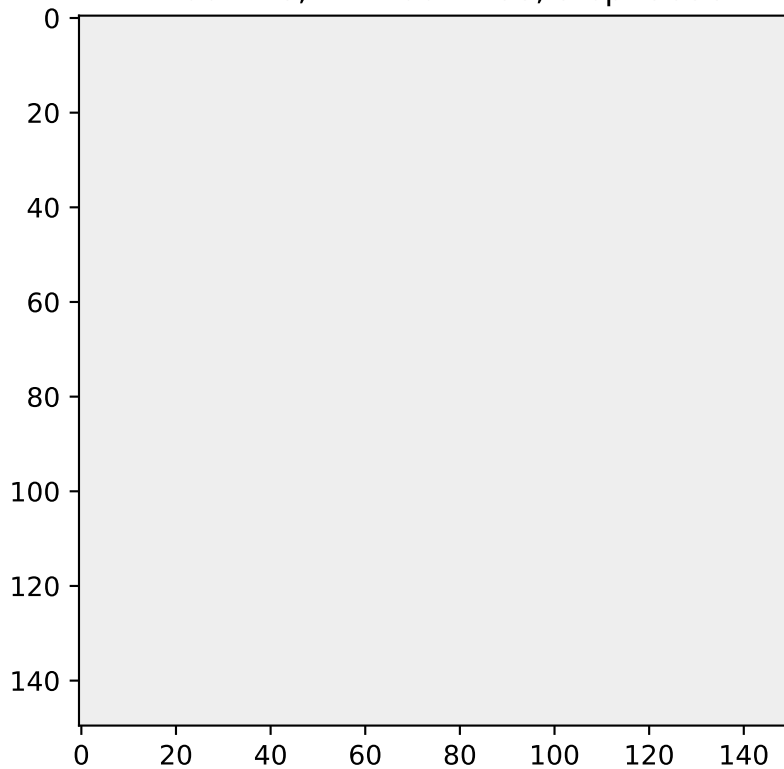
inf dur: 70, imm dur: 100, step: 3800.



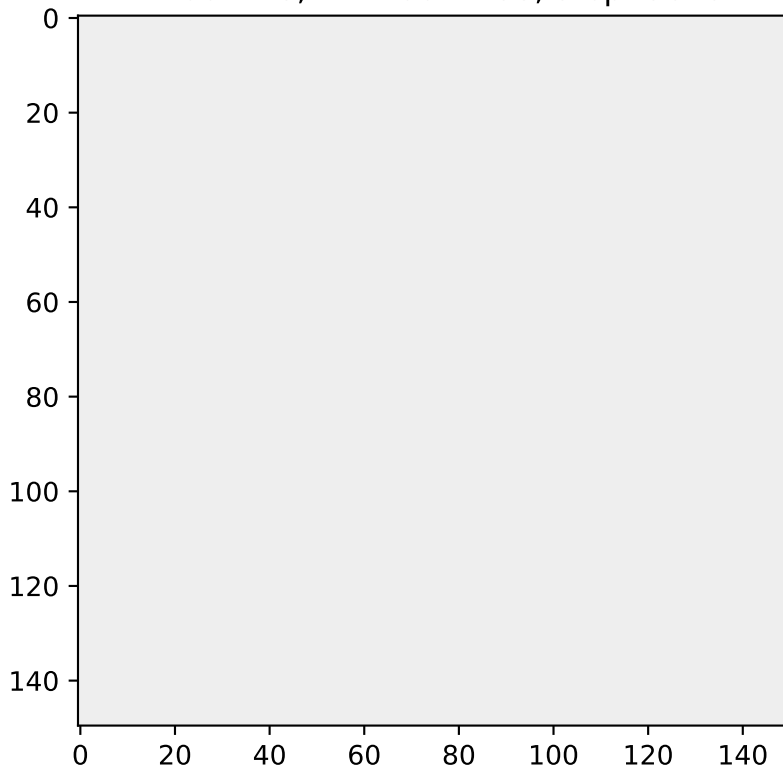
inf dur: 70, imm dur: 100, step: 3825.



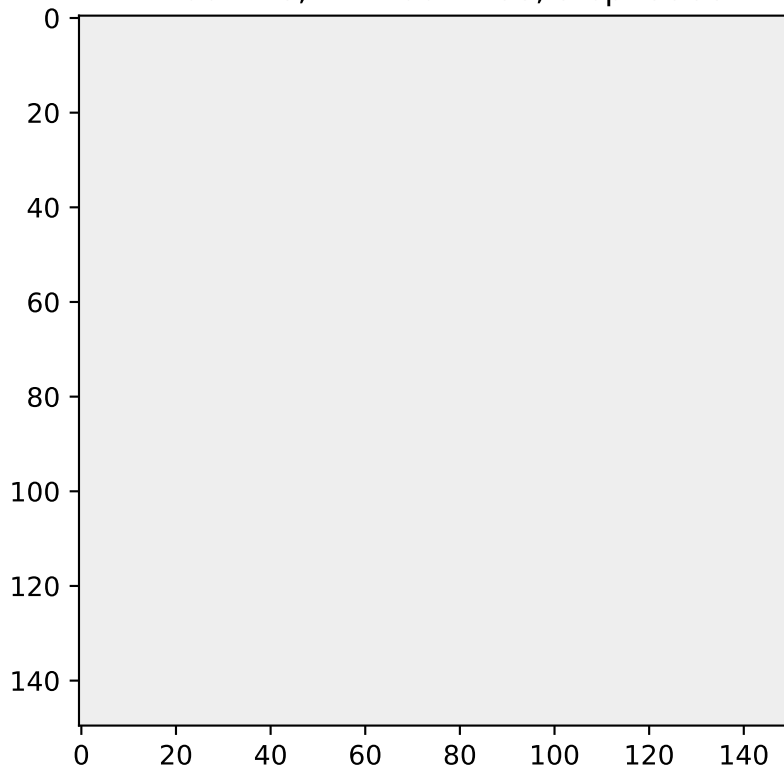
inf dur: 70, imm dur: 100, step: 3850.



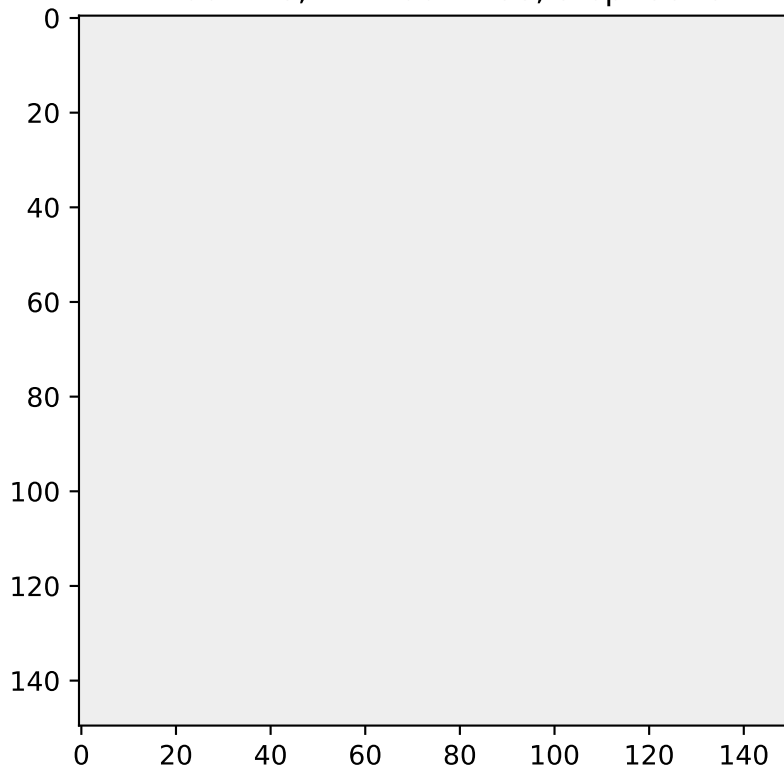
inf dur: 70, imm dur: 100, step: 3875.



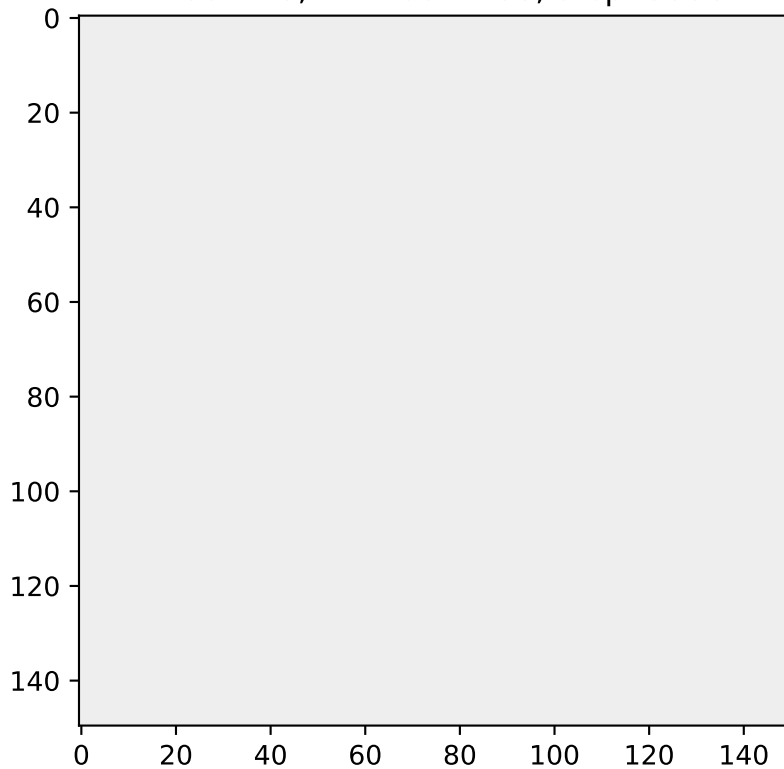
inf dur: 70, imm dur: 100, step: 3900.



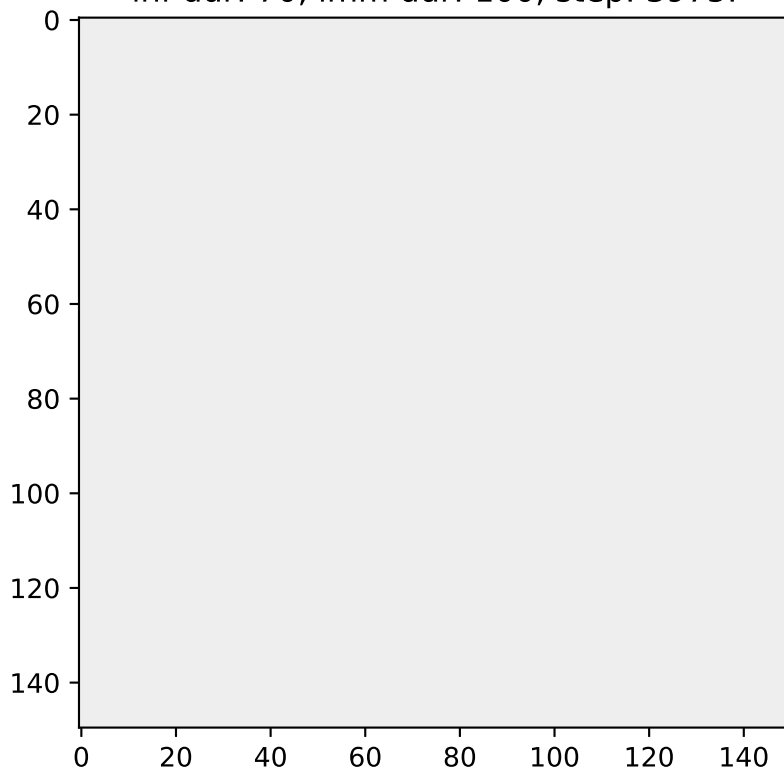
inf dur: 70, imm dur: 100, step: 3925.



inf dur: 70, imm dur: 100, step: 3950.

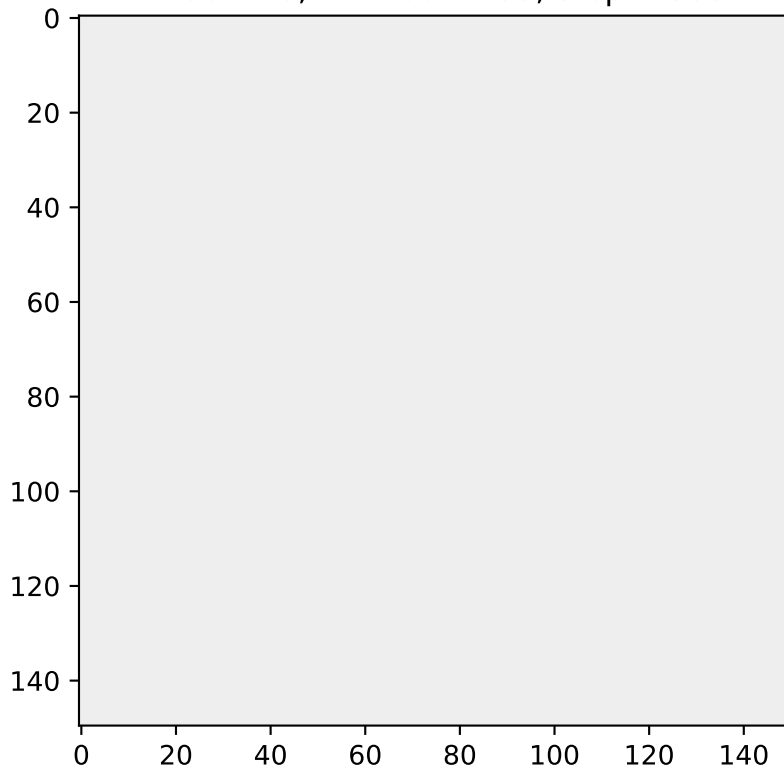


inf dur: 70, imm dur: 100, step: 3975.

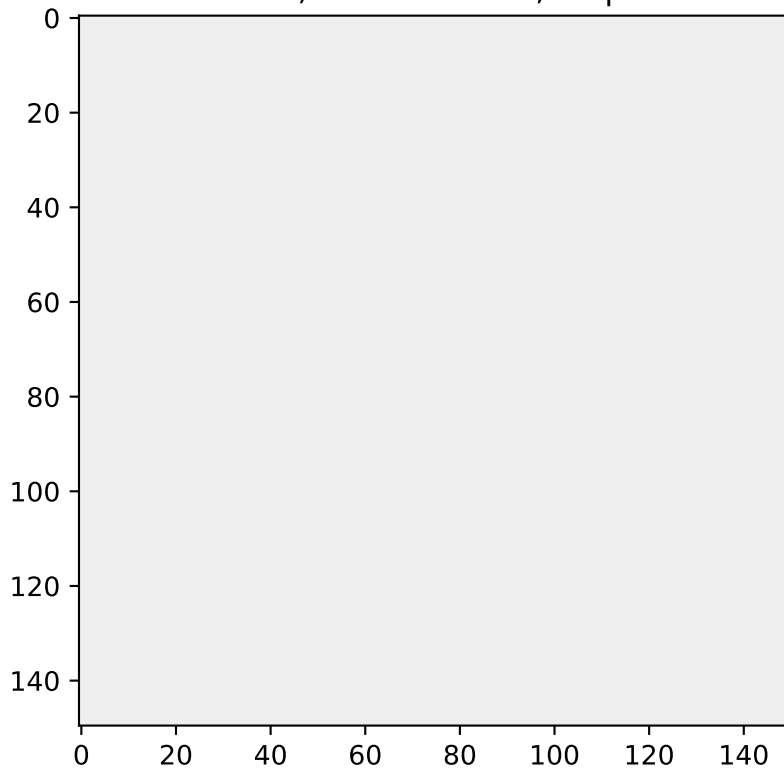




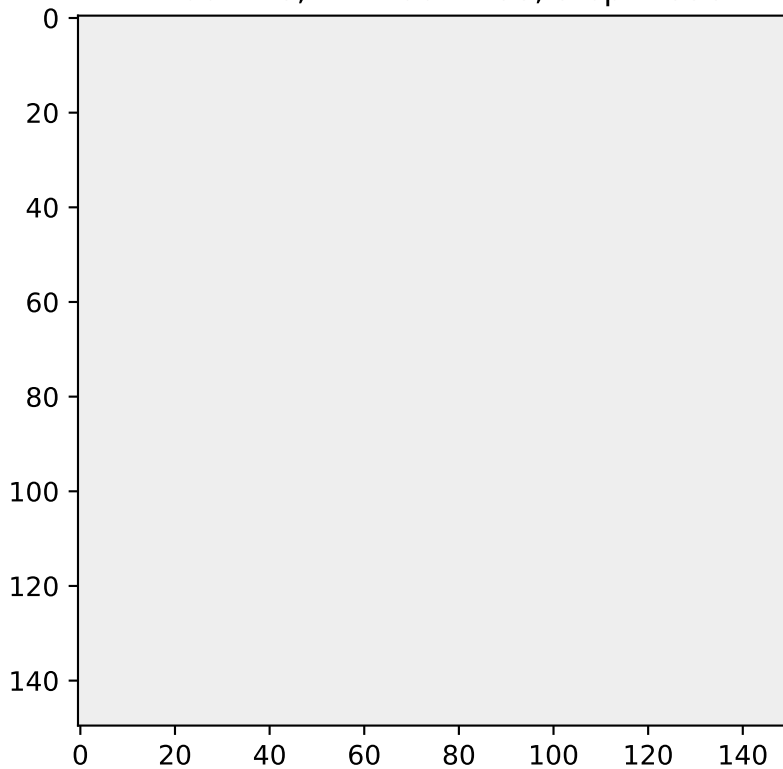
inf dur: 70, imm dur: 100, step: 4000.



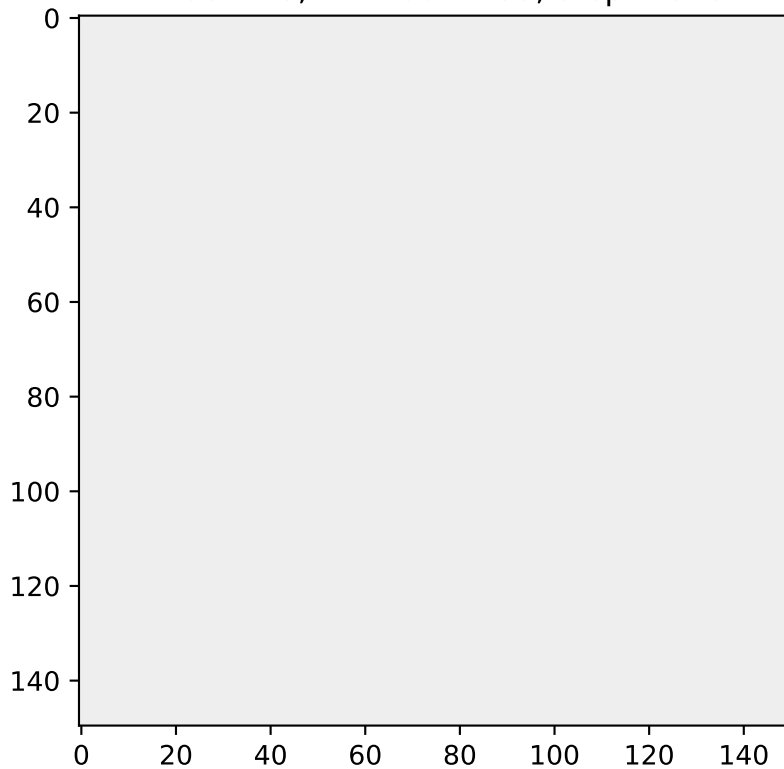
inf dur: 70, imm dur: 100, step: 4025.



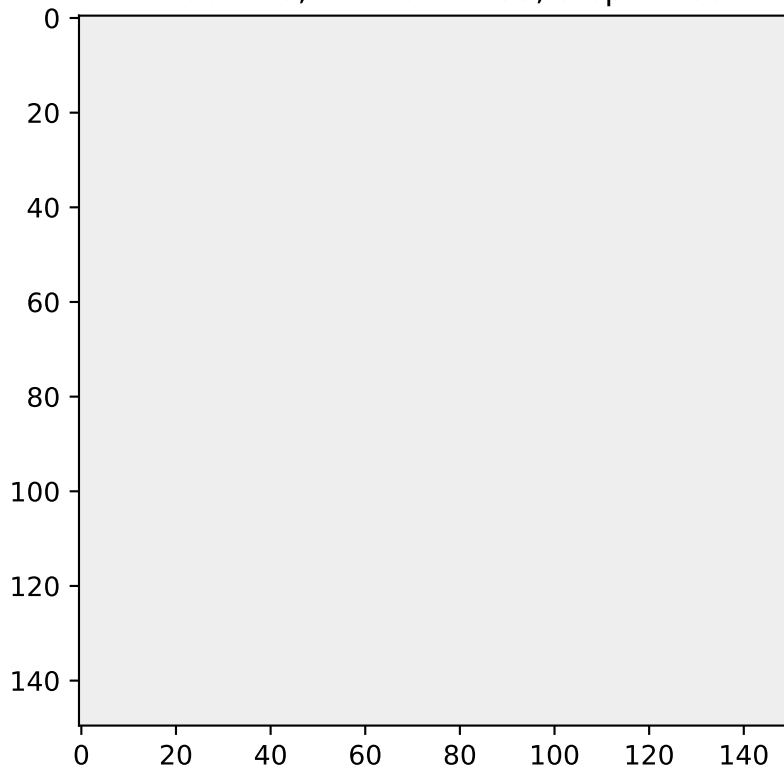
inf dur: 70, imm dur: 100, step: 4050.



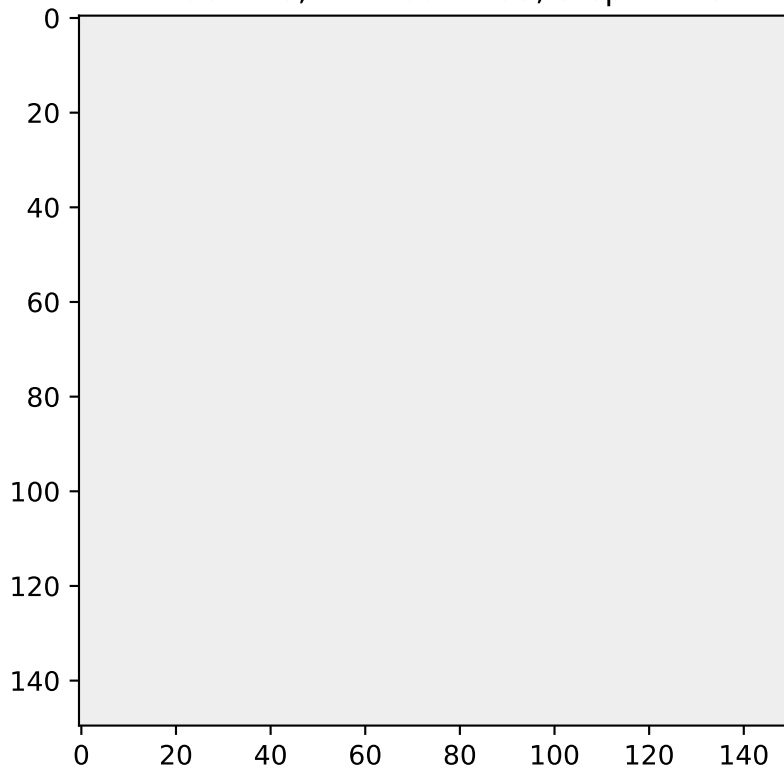
inf dur: 70, imm dur: 100, step: 4075.



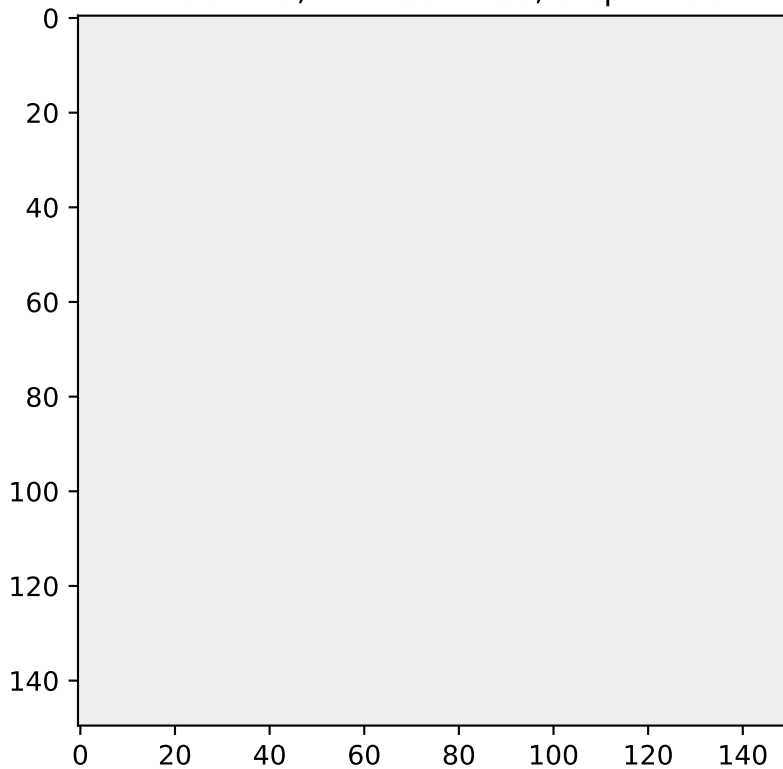
inf dur: 70, imm dur: 100, step: 4100.



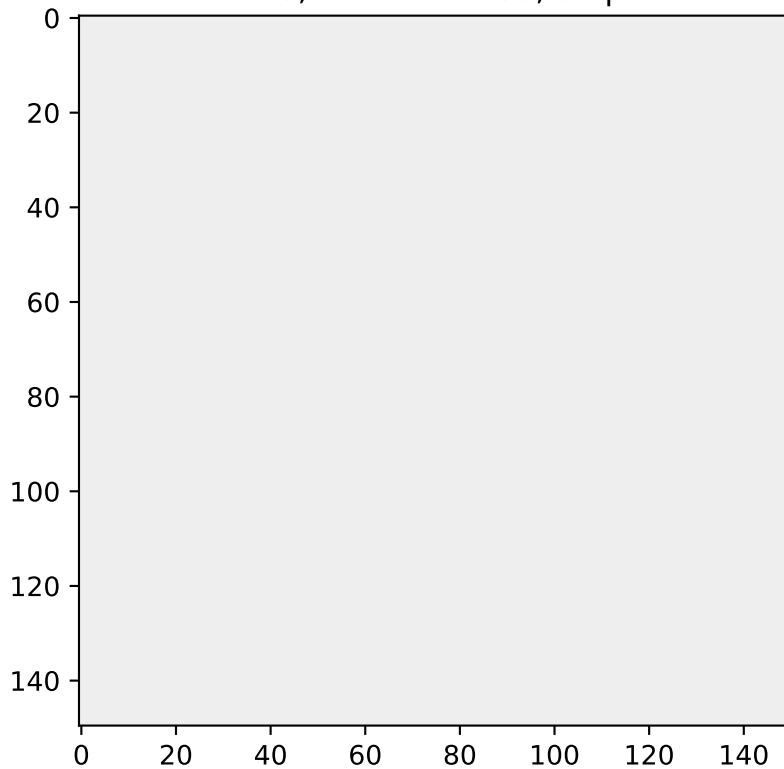
inf dur: 70, imm dur: 100, step: 4125.



inf dur: 70, imm dur: 100, step: 4150.

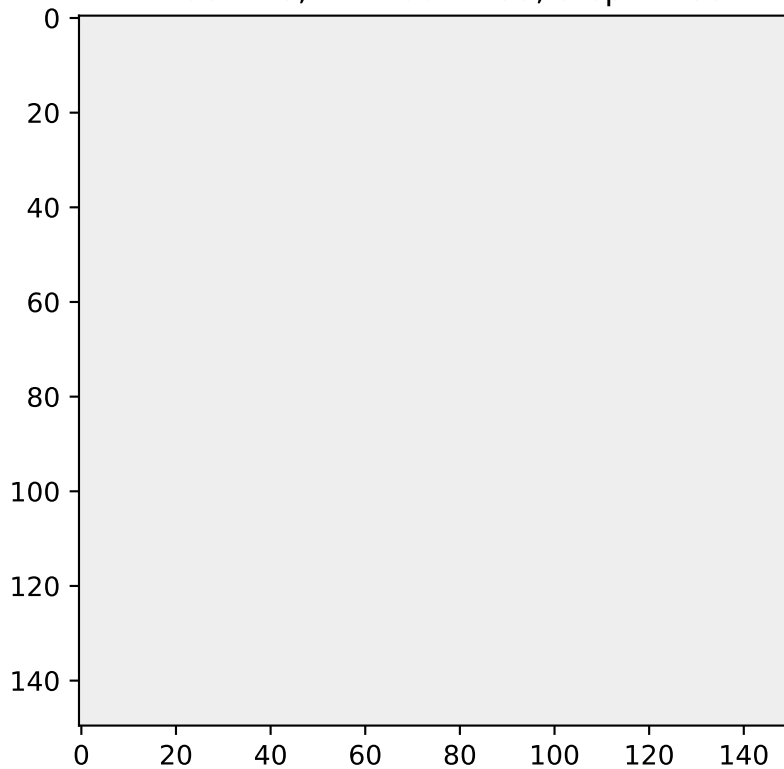


inf dur: 70, imm dur: 100, step: 4175.

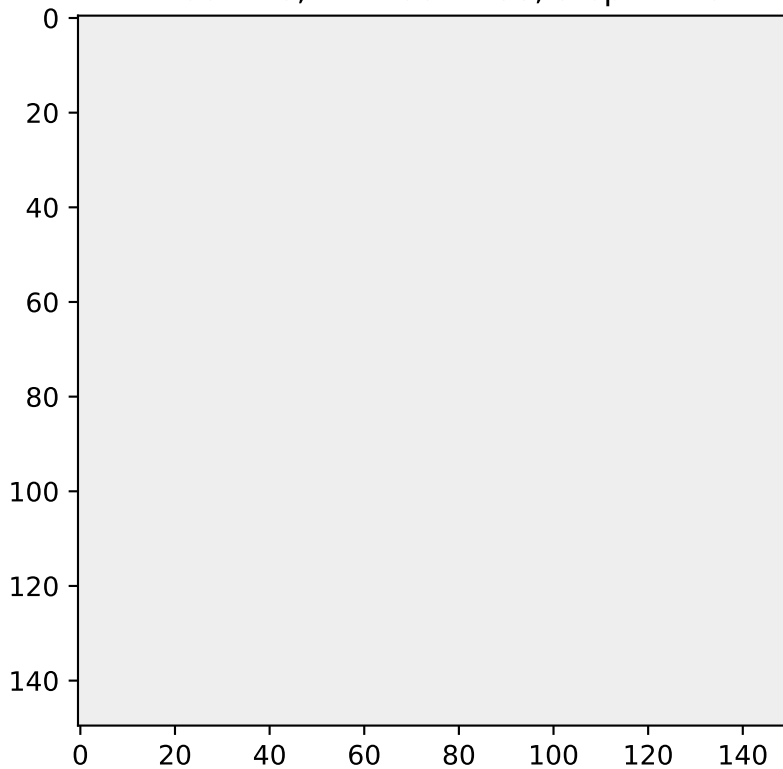




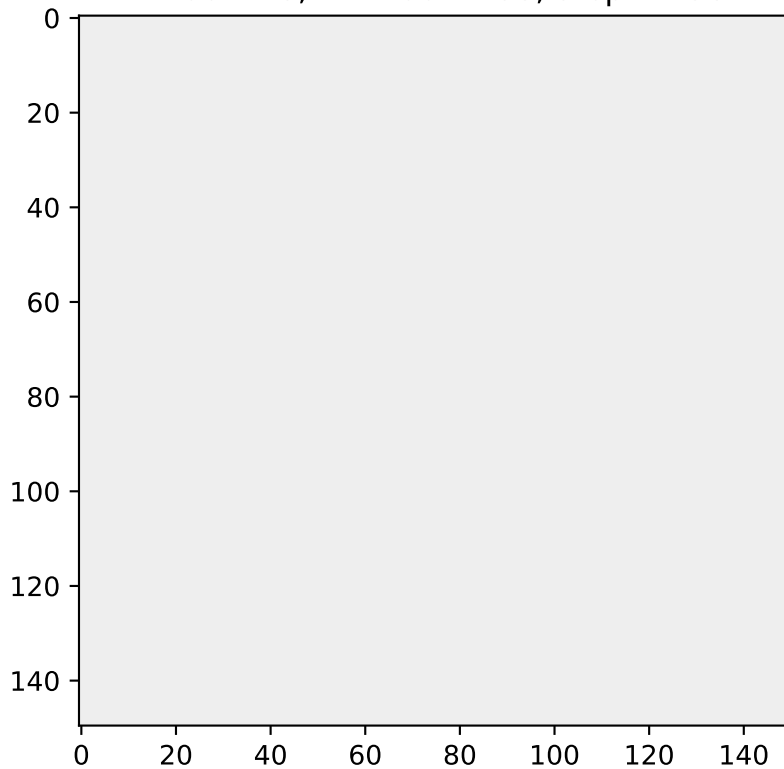
inf dur: 70, imm dur: 100, step: 4200.



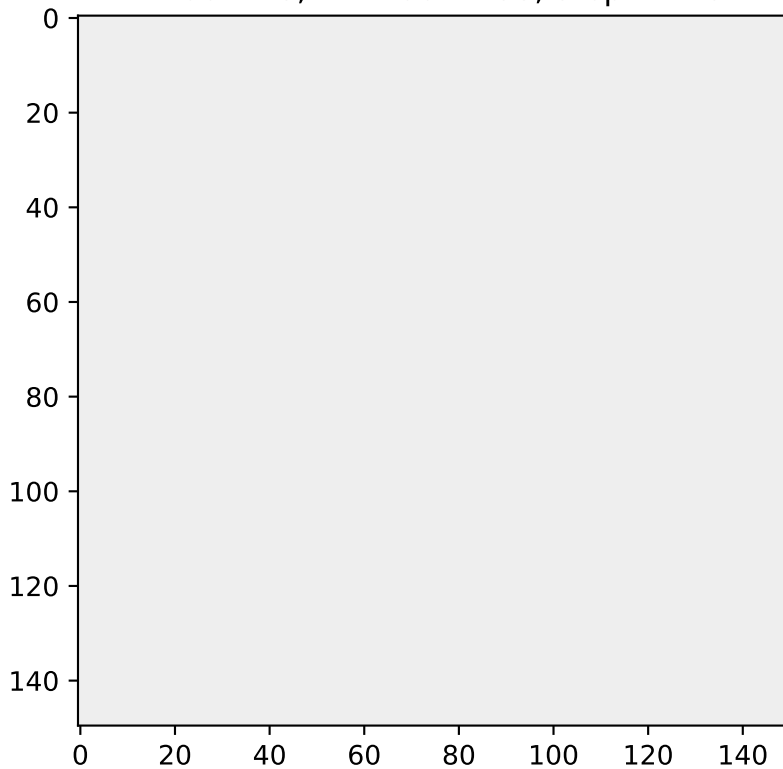
inf dur: 70, imm dur: 100, step: 4225.



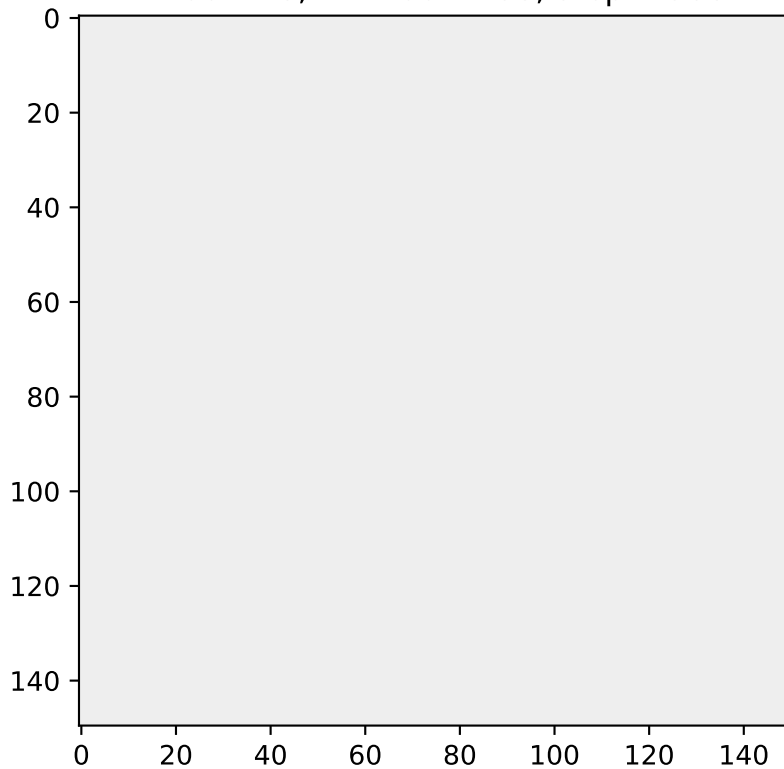
inf dur: 70, imm dur: 100, step: 4250.



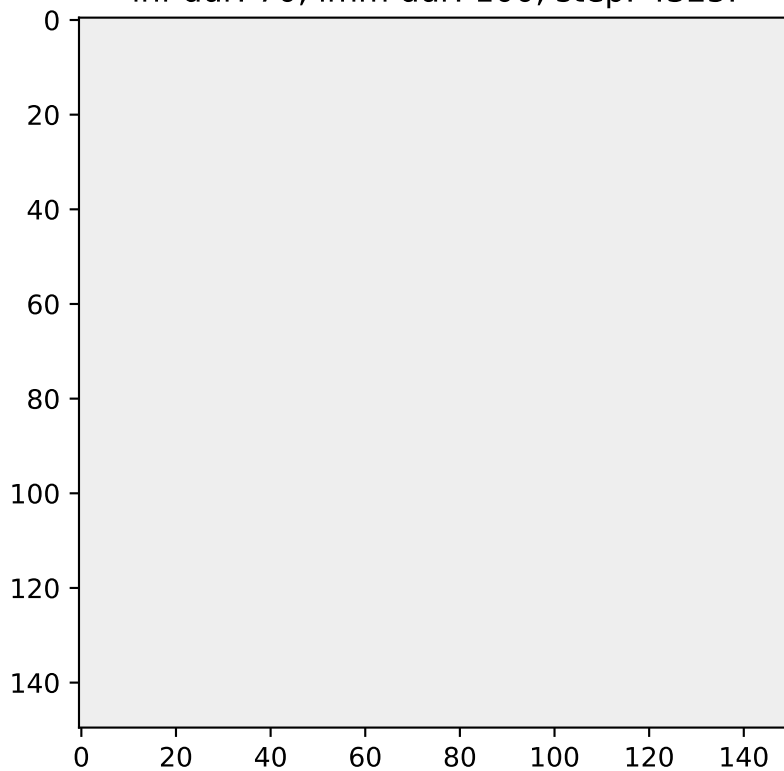
inf dur: 70, imm dur: 100, step: 4275.



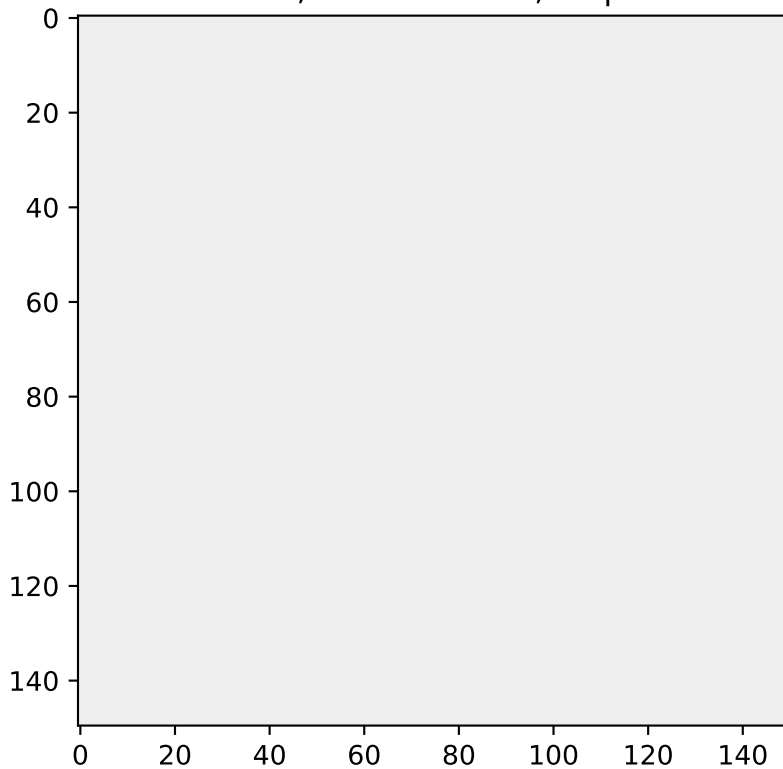
inf dur: 70, imm dur: 100, step: 4300.



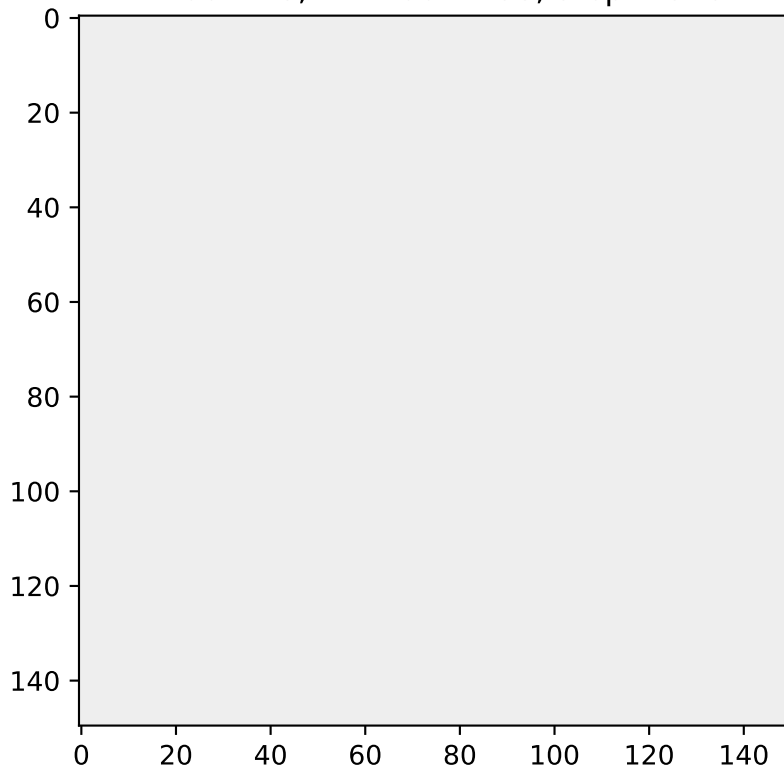
inf dur: 70, imm dur: 100, step: 4325.



inf dur: 70, imm dur: 100, step: 4350.

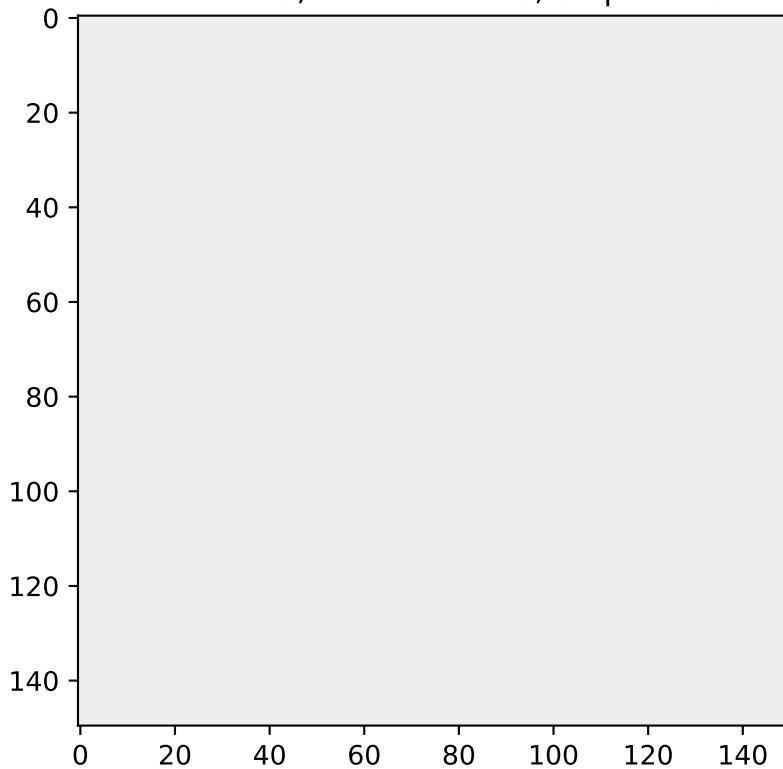


inf dur: 70, imm dur: 100, step: 4375.

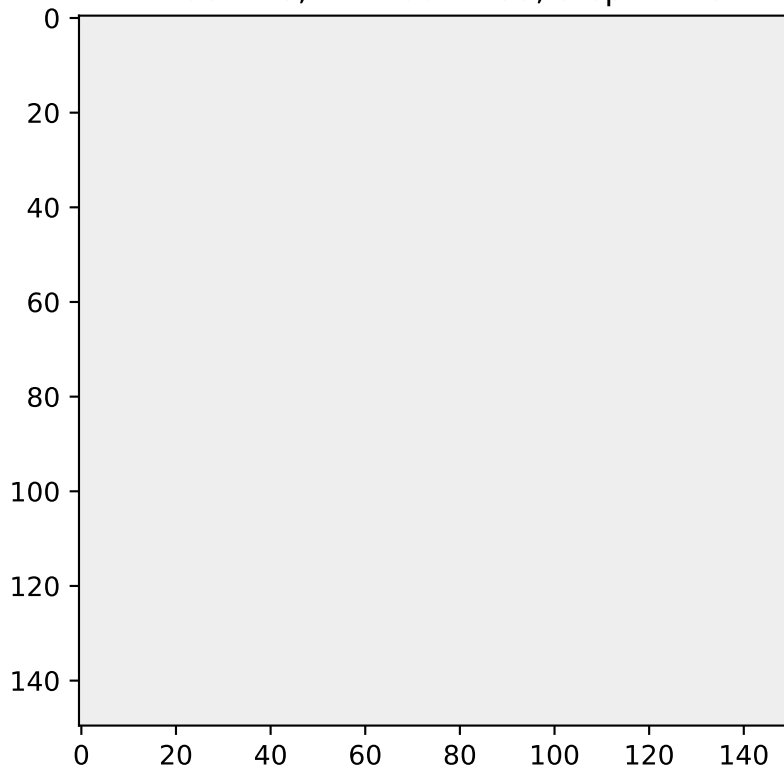




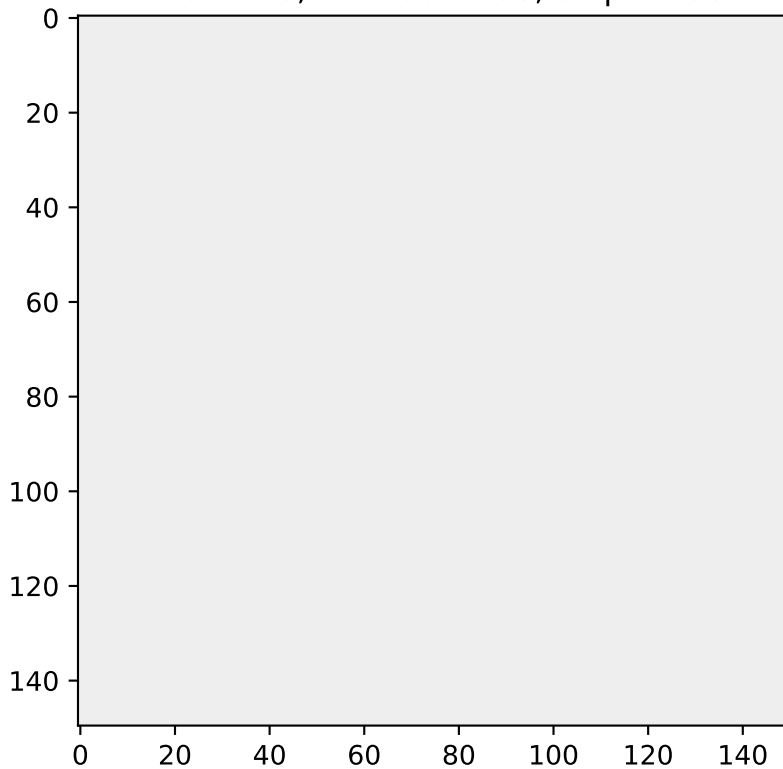
inf dur: 70, imm dur: 100, step: 4400.



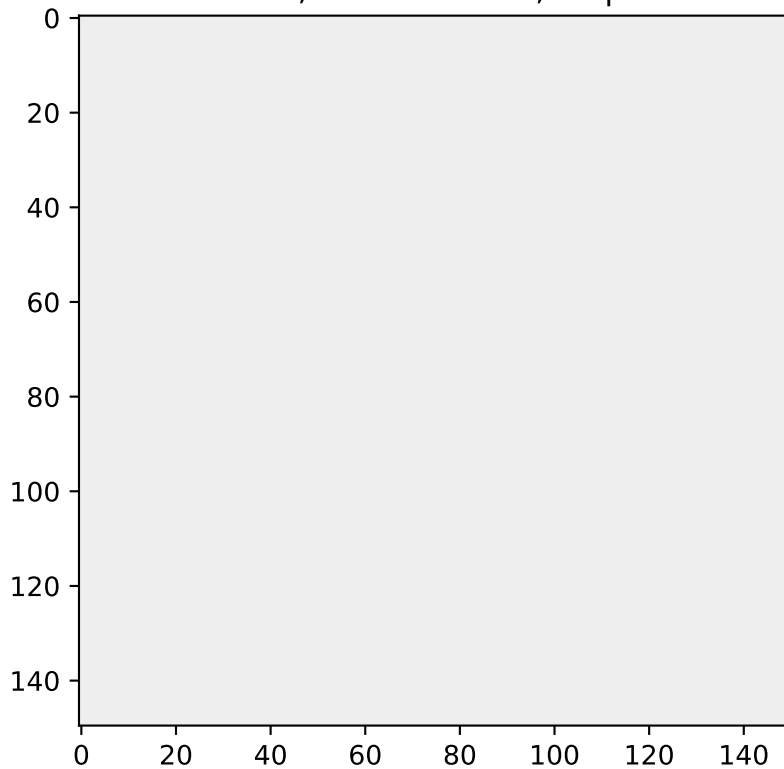
inf dur: 70, imm dur: 100, step: 4425.



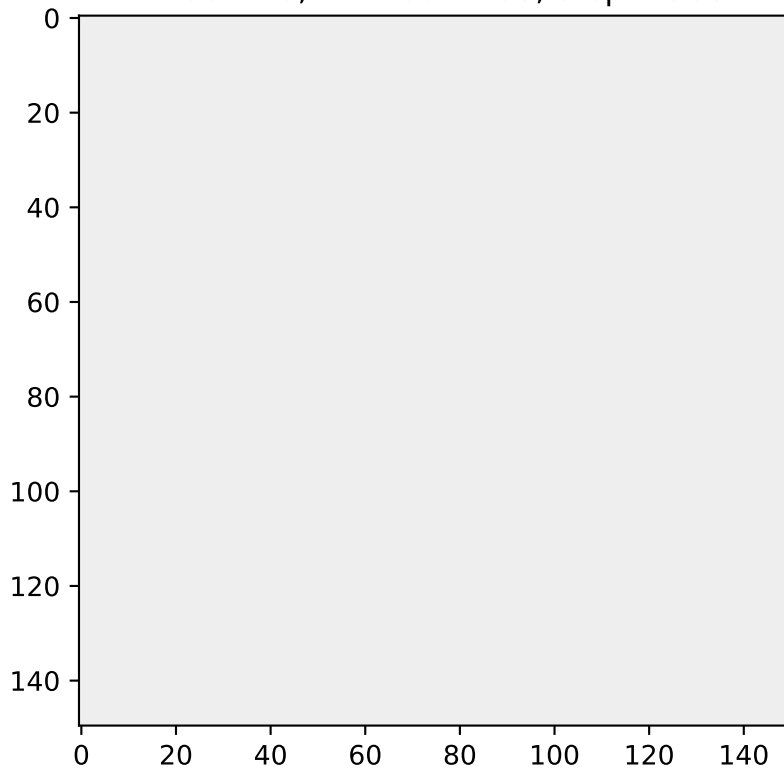
inf dur: 70, imm dur: 100, step: 4450.



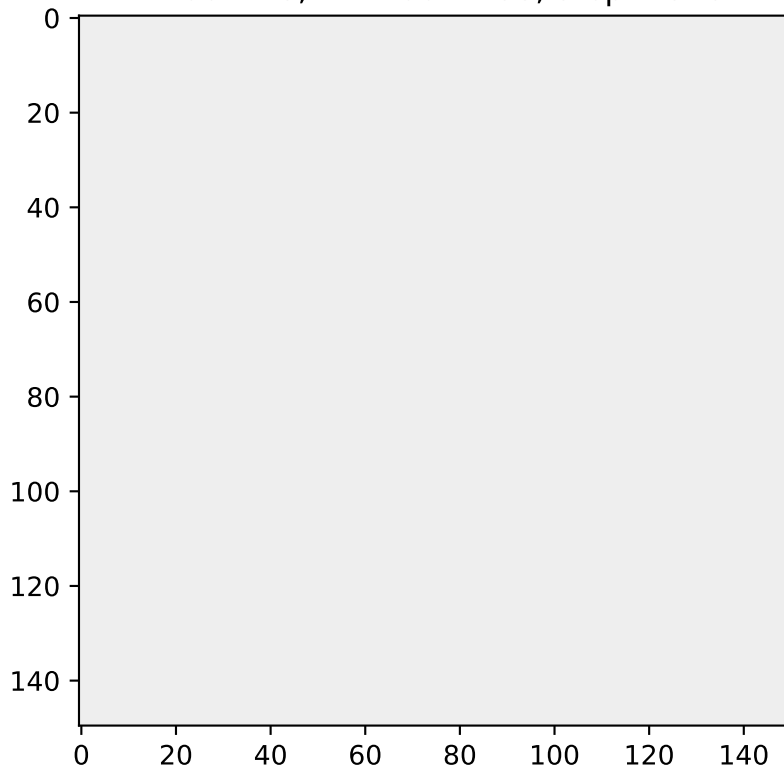
inf dur: 70, imm dur: 100, step: 4475.



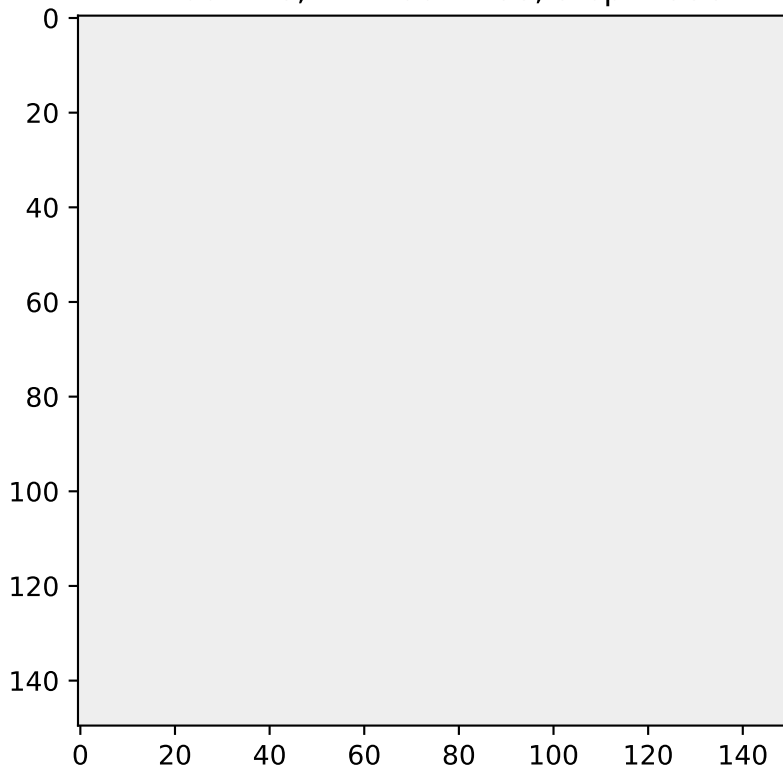
inf dur: 70, imm dur: 100, step: 4500.



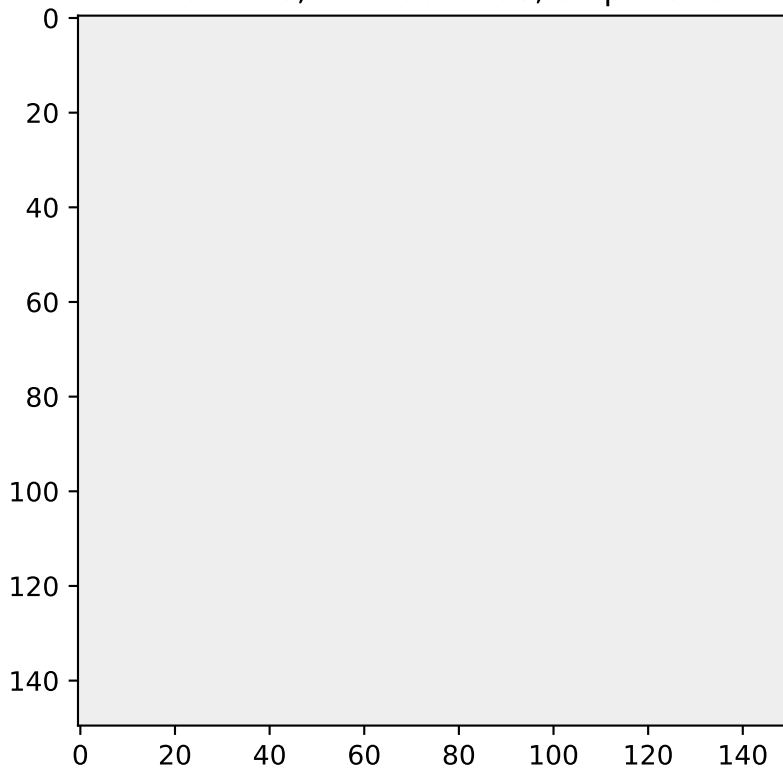
inf dur: 70, imm dur: 100, step: 4525.



inf dur: 70, imm dur: 100, step: 4550.

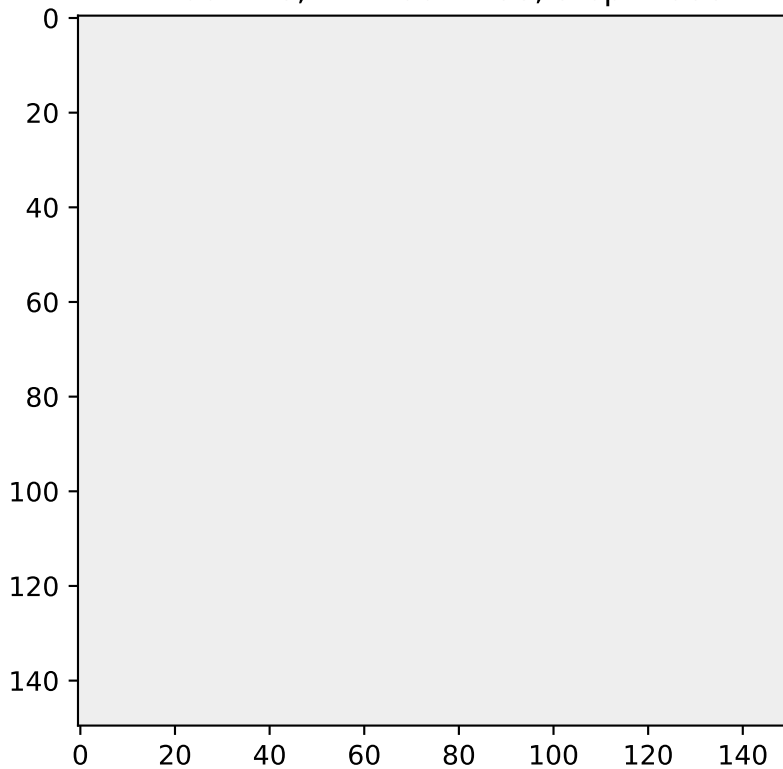


inf dur: 70, imm dur: 100, step: 4575.

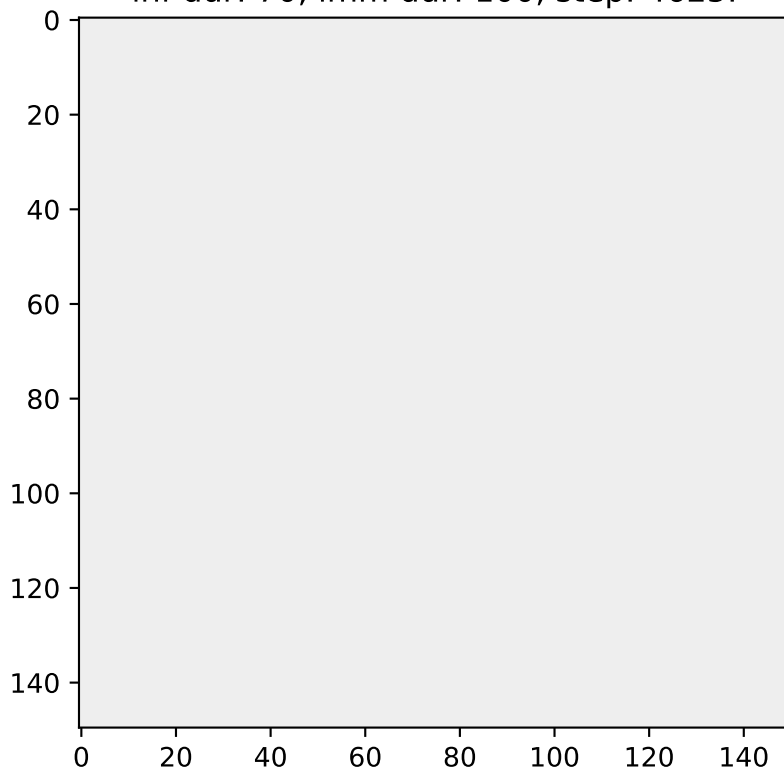




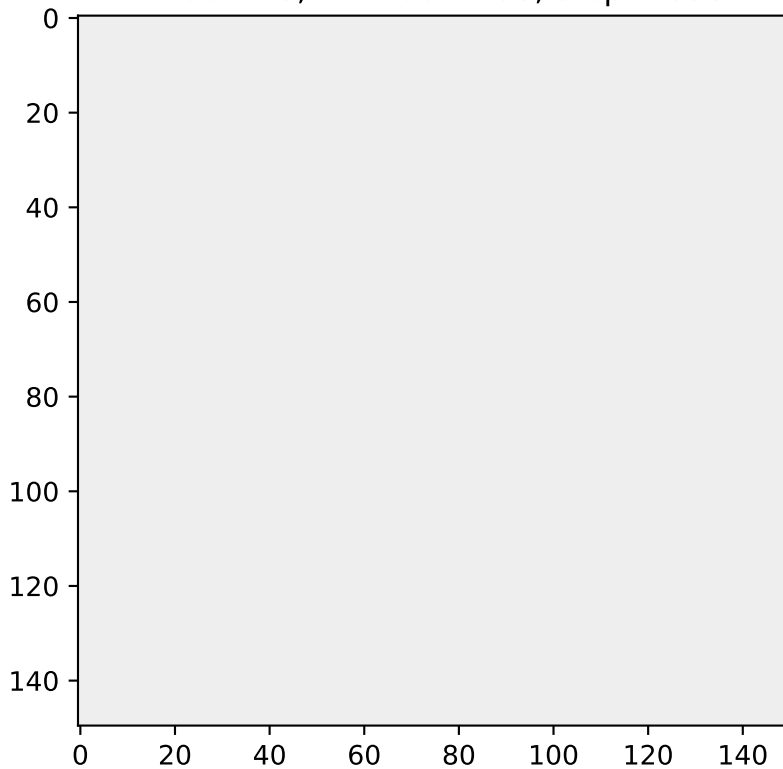
inf dur: 70, imm dur: 100, step: 4600.



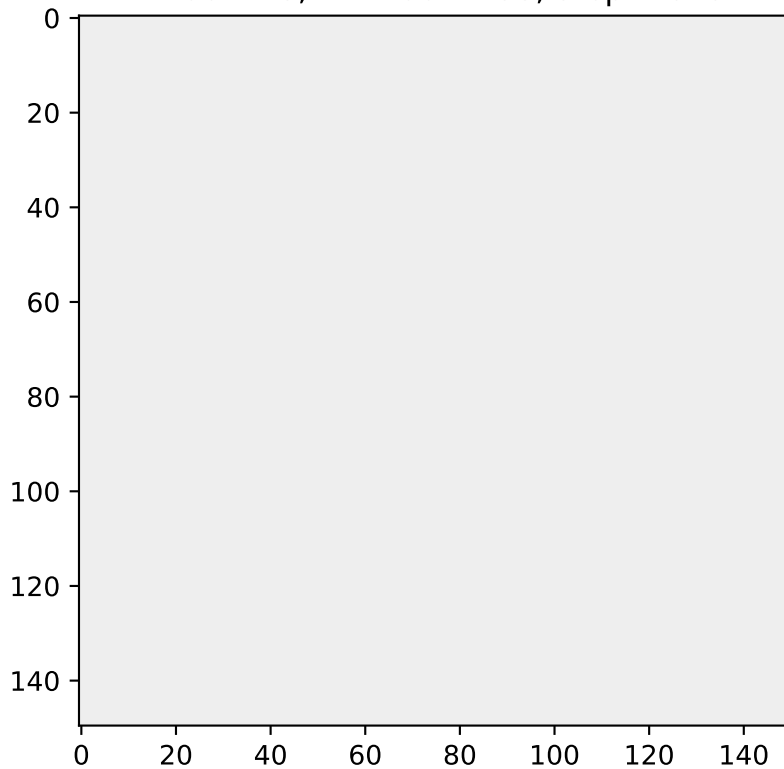
inf dur: 70, imm dur: 100, step: 4625.



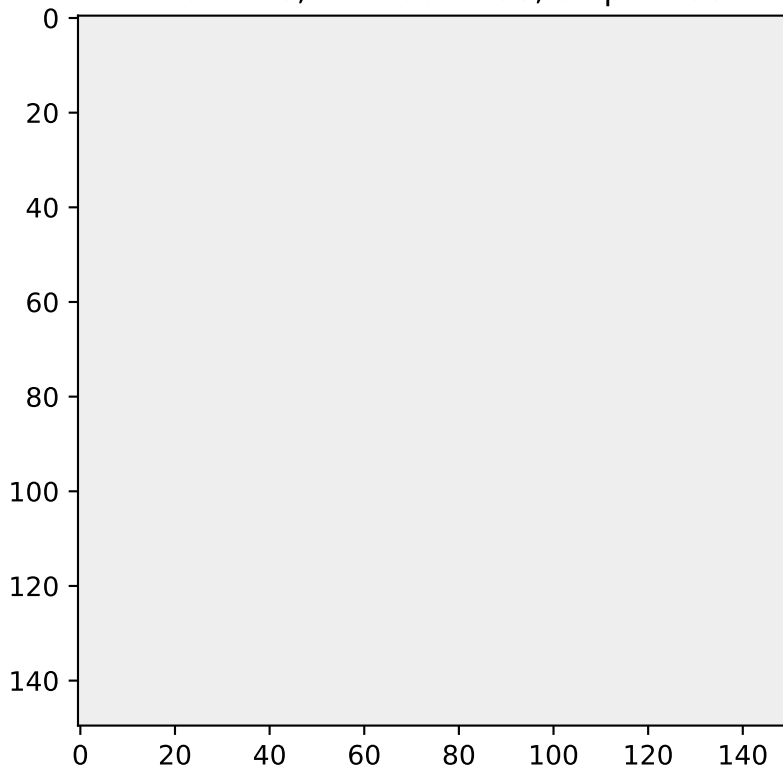
inf dur: 70, imm dur: 100, step: 4650.



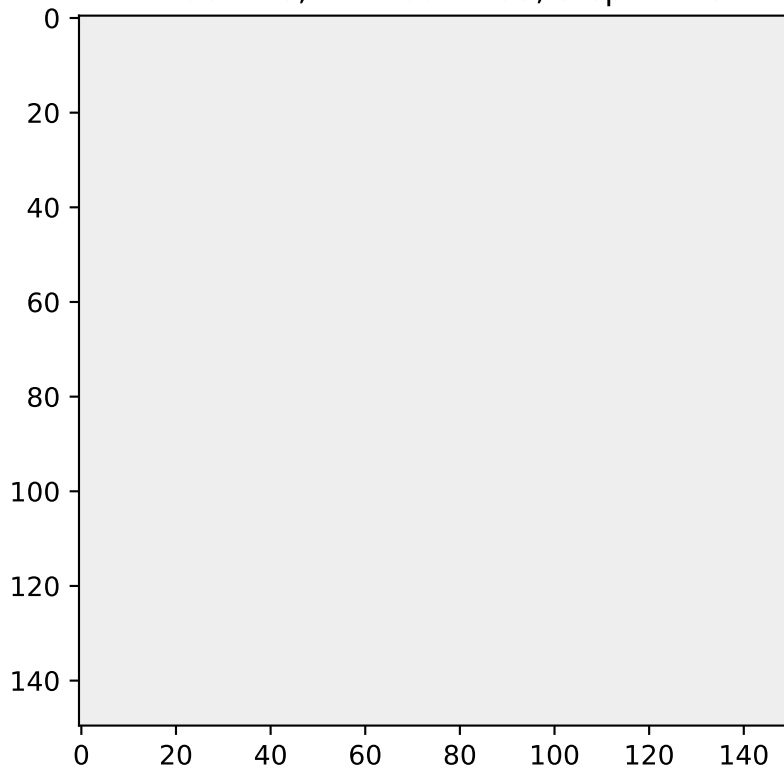
inf dur: 70, imm dur: 100, step: 4675.



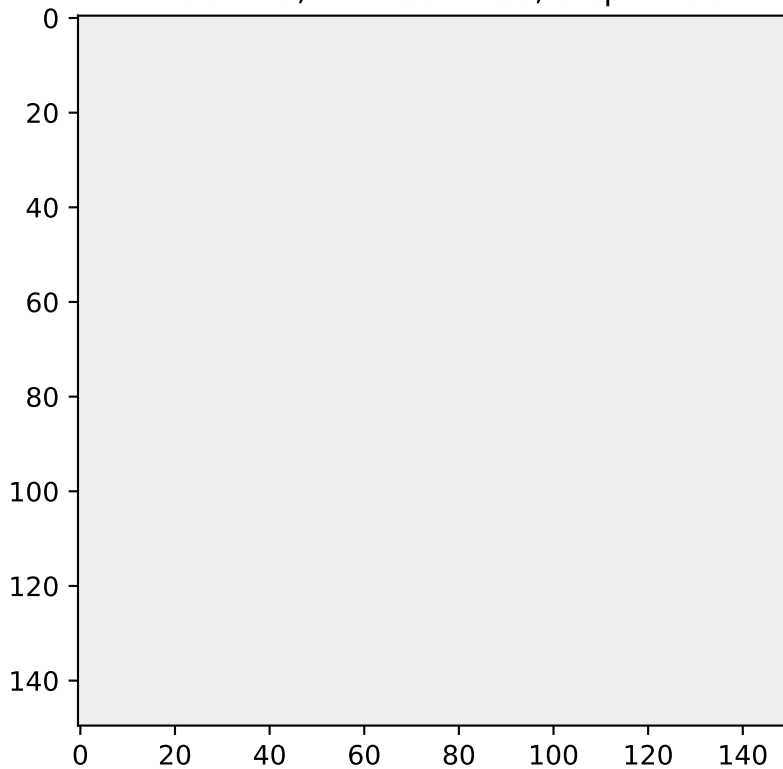
inf dur: 70, imm dur: 100, step: 4700.



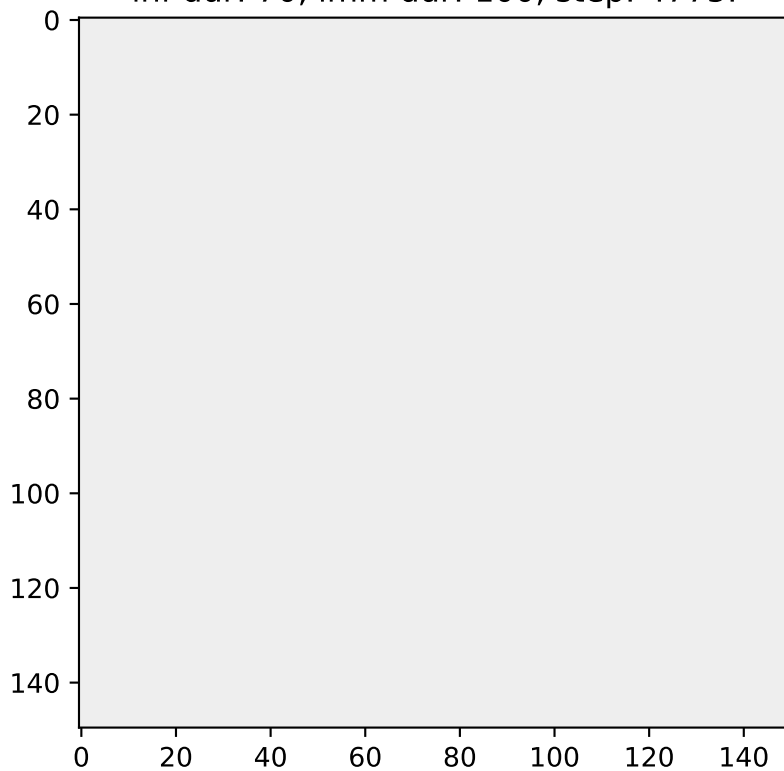
inf dur: 70, imm dur: 100, step: 4725.



inf dur: 70, imm dur: 100, step: 4750.

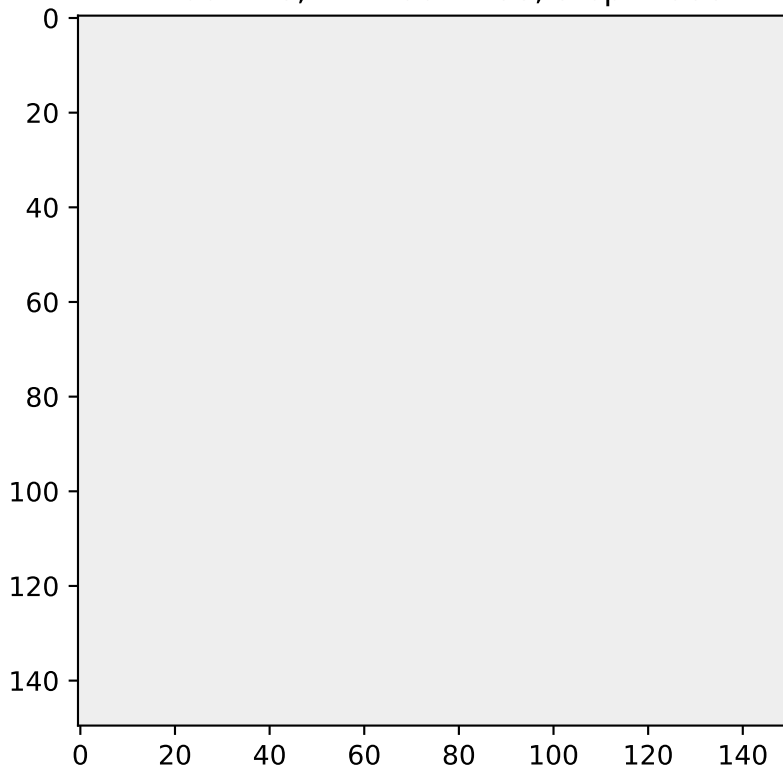


inf dur: 70, imm dur: 100, step: 4775.

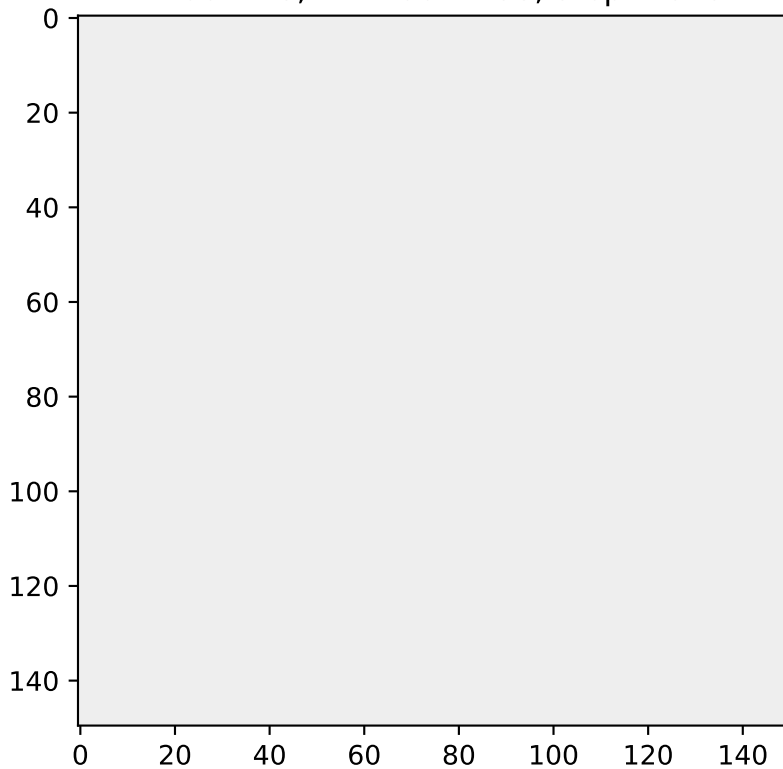




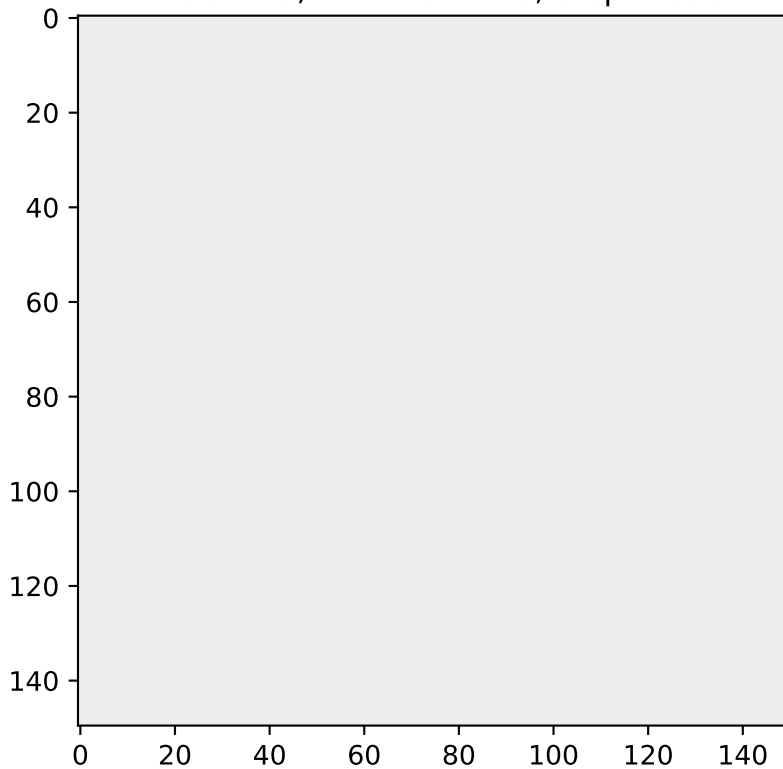
inf dur: 70, imm dur: 100, step: 4800.



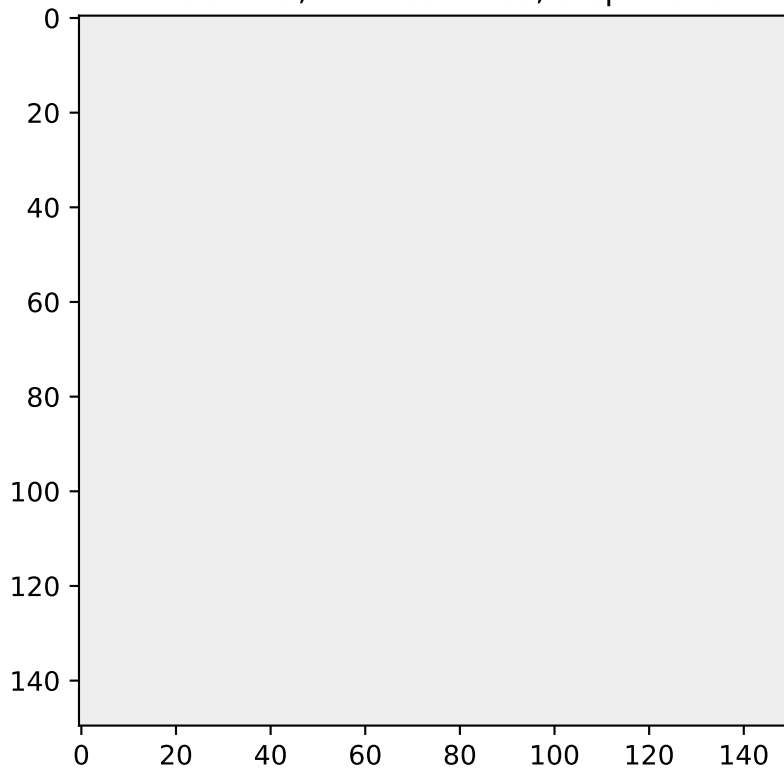
inf dur: 70, imm dur: 100, step: 4825.



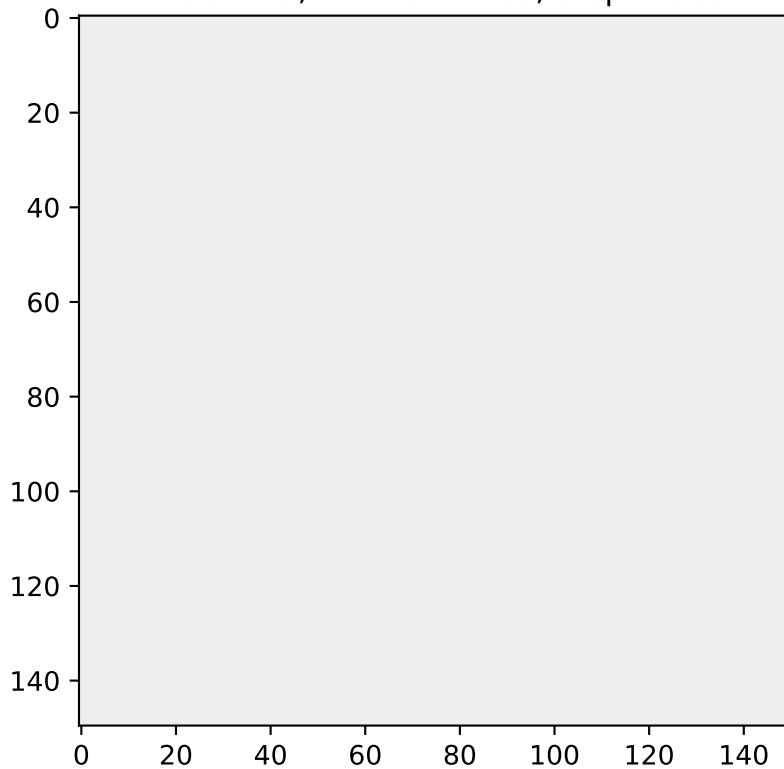
inf dur: 70, imm dur: 100, step: 4850.



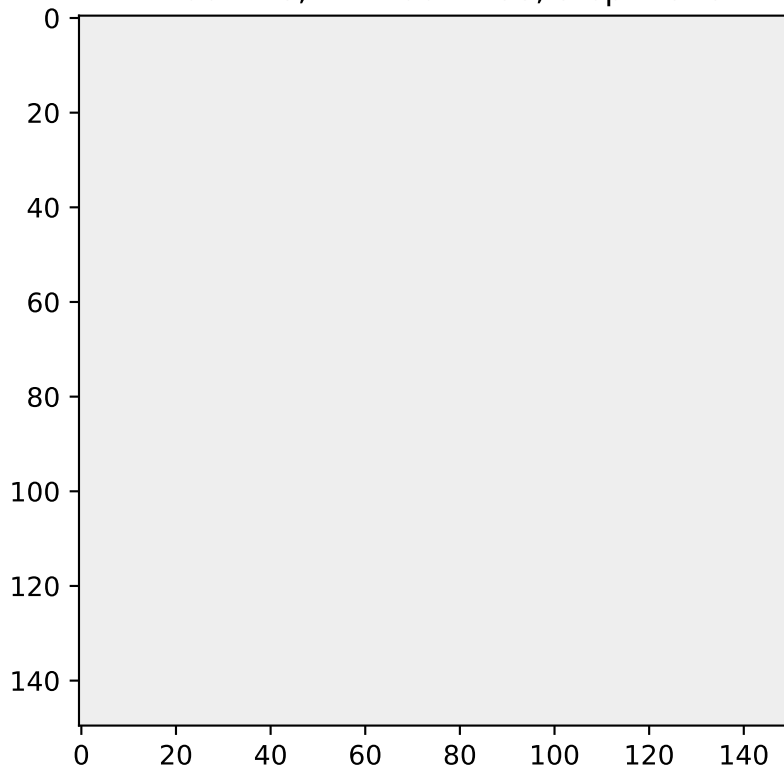
inf dur: 70, imm dur: 100, step: 4875.



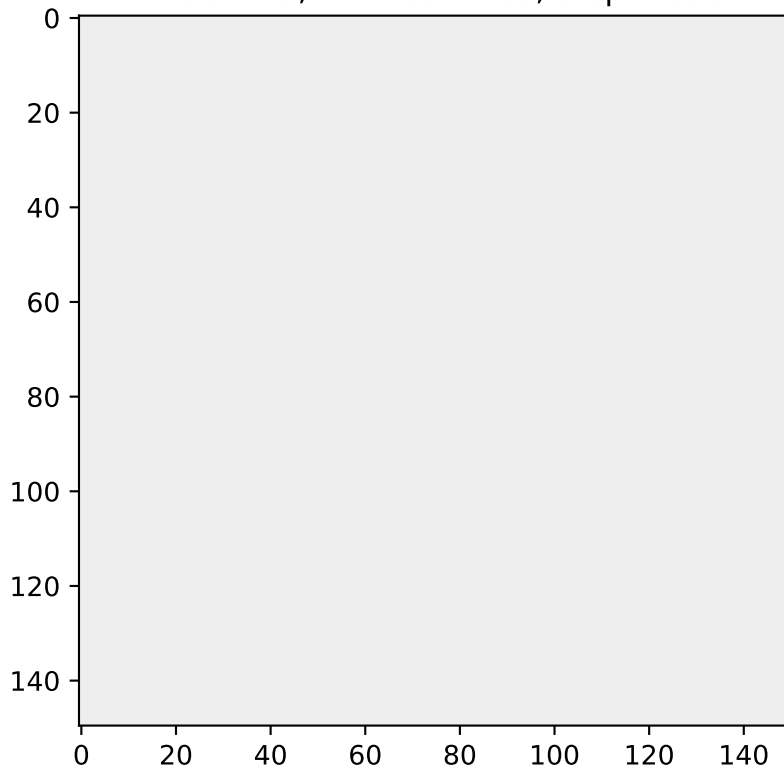
inf dur: 70, imm dur: 100, step: 4900.



inf dur: 70, imm dur: 100, step: 4925.



inf dur: 70, imm dur: 100, step: 4950.



inf dur: 70, imm dur: 100, step: 4975.

