

Lab 12 Evaluation Sheet

Section: _____
 Student Name: _____
 Mg: _____

Lab Exercises

TA Initials Score

you can use the provided golden module
 or completely use your own pipelines and caches
 (highly preferred) to perform this demo.

Software Demonstrations

-*mergesort.asm* cycles: _____ /10

-*example.asm* cycles: _____ /10

Synthesis Demonstrations

-*mergesort.asm* cycles: _____ /03

-*example.asm* cycles: _____ /03

Hardware Demonstrations

-*mergesort.asm* cycles: _____ /03

-*example.asm* cycles: _____ /03

Question

What would the effect of an L2 cache be
 on the dual-core processor?[1]

what would the effect be if the
 caches (L1+L2) were exclusive?[1]

What would the effects be if the caches
 (L1+L2) were inclusive?[1]

you can search inclusive and exclusive online,
 and write your answer in words before demo.

_____ /03

TA Grading script

_____ /65