

Lab 6 Evaluation Sheet

Section: _____

Student Name: _____

Mg: _____

Lab Exercises

TA Initials

Score

Updated CPU design schematic (Modular)
relatively detail, including major signals and their names

_____ /01

The following programs can be inserted with nops
if your forwarding unit is not ready at this point.

Pre-synthesis Tests*-test.rtype.asm*

_____ /02

-test.loadstore.asm

_____ /02

-test.ldst1.asm

_____ /01

-test.ldst2.asm

_____ /01

-test.branchjump1.asm

_____ /02

TA Grading script of submitted design

_____ /01