

Practical Second-Order CPA Attack on Ascon with Proper Selection Function

Viet-Sang Nguyen

joint work with Vincent Grosso and Pierre-Louis Cayrel

CASCADE Conference

Saint-Etienne, 2 April, 2025



PROPHY ANR-22-CE39-0008-01

NIST

2018



Lightweight cryptography competition

NIST

2018



Lightweight cryptography competition

2023



Selected Ascon

NIST



2018



Lightweight cryptography competition

2023



Selected Ascon

NIST



2018



Lightweight cryptography competition

2023



Selected Ascon



Possible attacks



Secure implementations

In this talk



Possible attacks

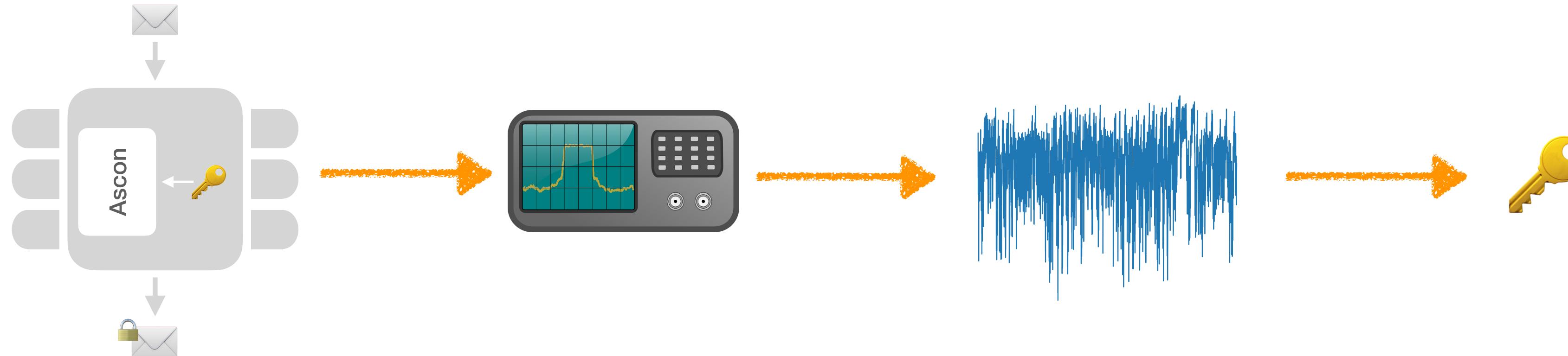
In this talk



Possible attacks

Correlation Power Analysis (CPA) attack

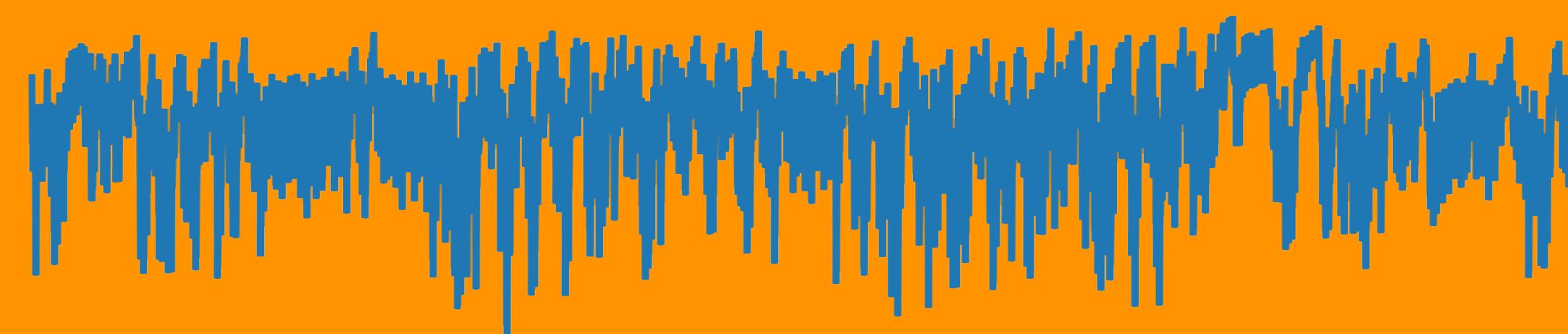
In this talk



Possible attacks

Correlation Power Analysis (CPA) attack

CPA attack



CPA attack

CPA attack

- ◆ Choose attack point: intermediate variable ν

CPA attack

- ◆ Choose attack point: intermediate variable v

Selection function: $v = f(d, k)$

known non-constant data  part of the key

CPA attack

- ◆ Choose attack point: intermediate variable v

Selection function: $v = f(d, k)$

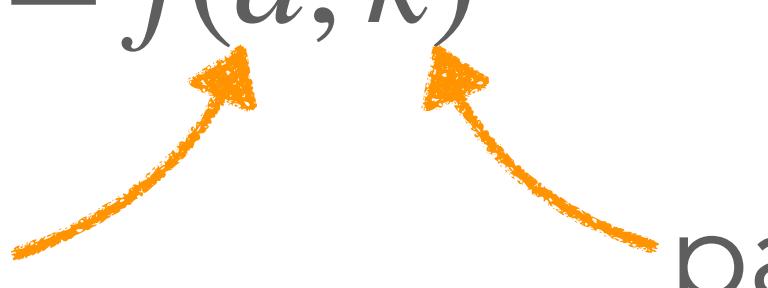
known non-constant data  part of the key

Well-known CPA on AES: $v = \text{Sbox}(\text{plaintext}, \text{key})$

CPA attack

- ◆ Choose attack point: intermediate variable v

Selection function: $v = f(d, k)$

known non-constant data  part of the key

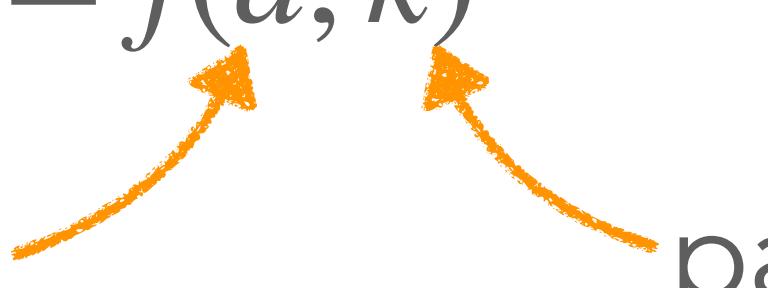
Well-known CPA on AES: $v = \text{Sbox}(\text{plaintext}, \text{key})$

- ◆ Choose leakage model for v

CPA attack

- ◆ Choose attack point: intermediate variable v

Selection function: $v = f(d, k)$

known non-constant data  part of the key

Well-known CPA on AES: $v = \text{Sbox}(\text{plaintext}, \text{key})$

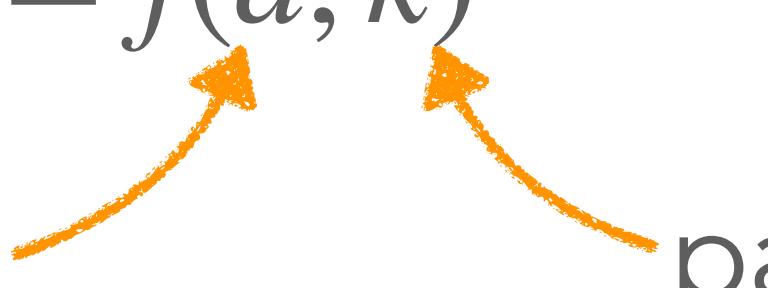
- ◆ Choose leakage model for v

This work: Hamming weight

CPA attack

- ◆ Choose attack point: intermediate variable v

Selection function: $v = f(d, k)$

known non-constant data  part of the key

Well-known CPA on AES: $v = \text{Sbox}(\text{plaintext}, \text{key})$

- ◆ Choose leakage model for v

This work: Hamming weight

Hypothetical power consumption: $h = \text{HW}(v) = \text{HW}(f(d, k))$

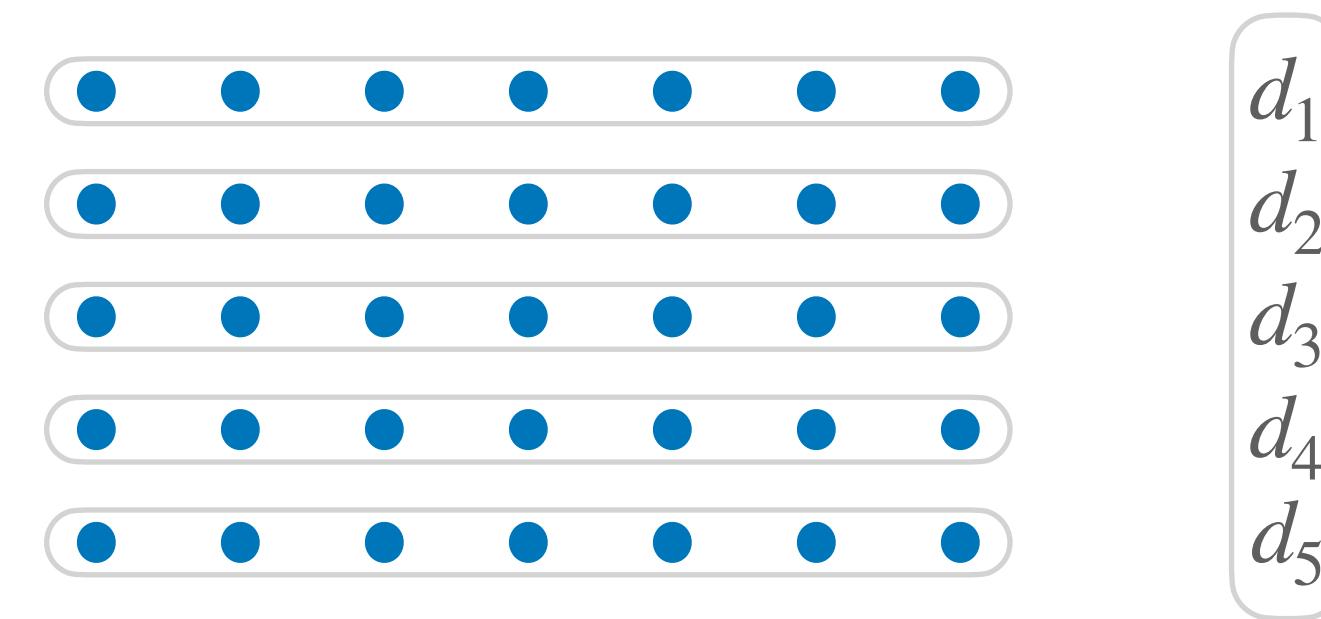
CPA attack

CPA attack

- ◆ Measure power consumption traces
and record d

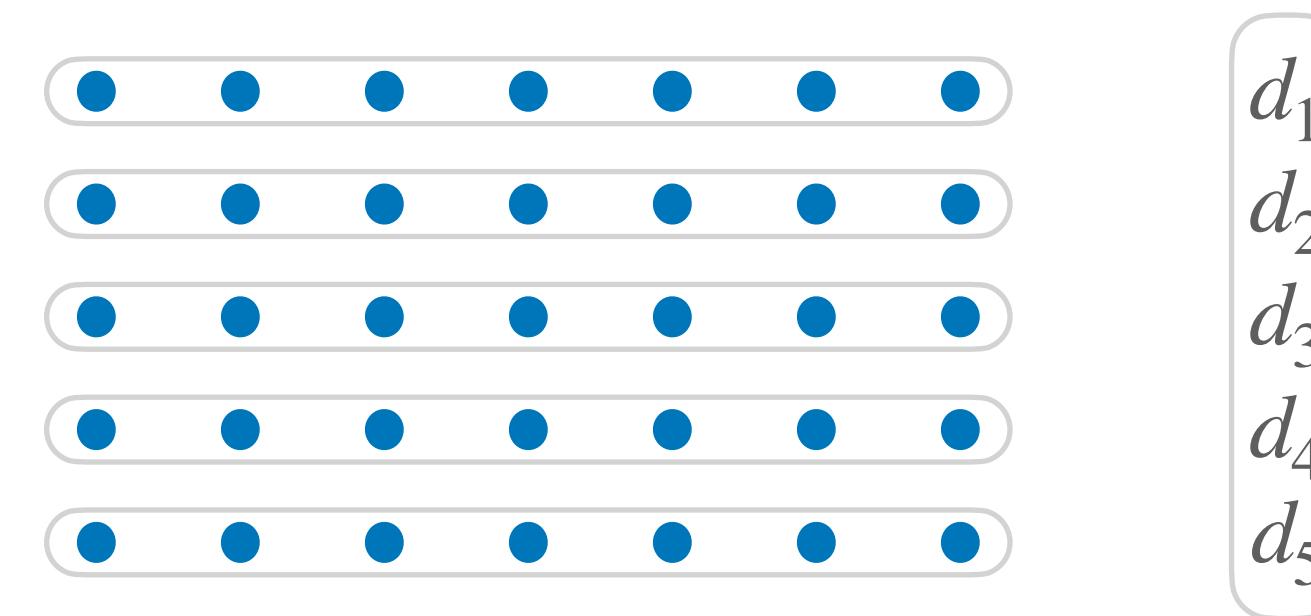
CPA attack

- ◆ Measure power consumption traces
and record d



CPA attack

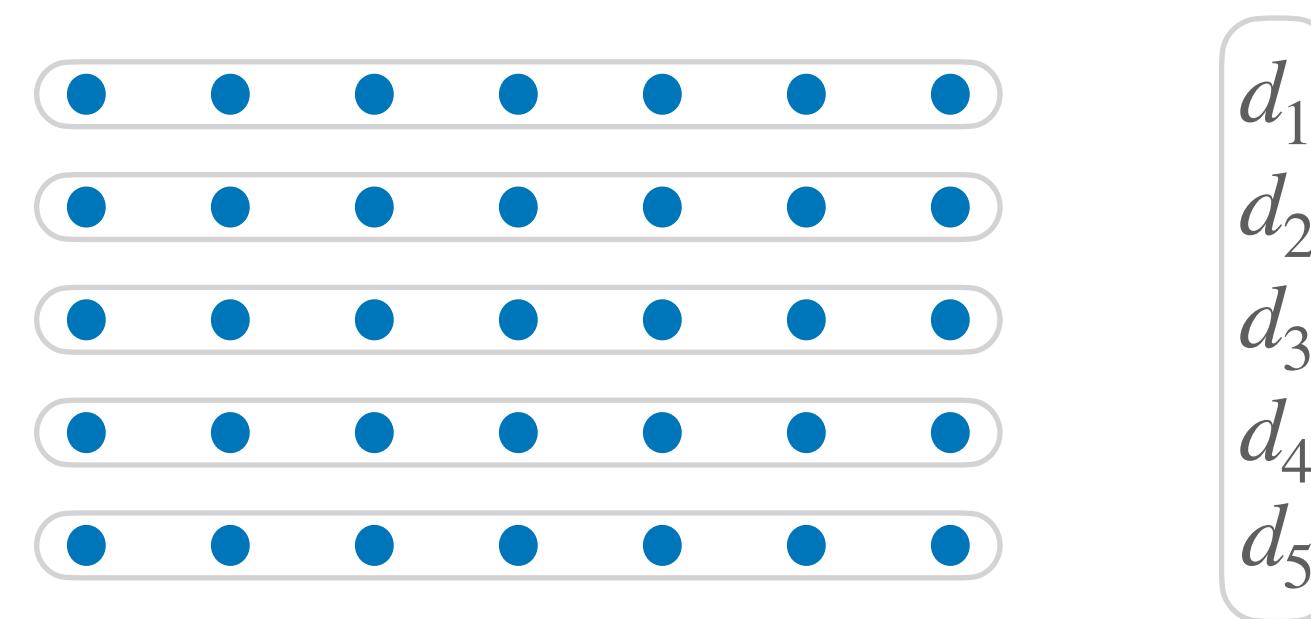
- ◆ Measure power consumption traces and record d
- ◆ Compute hypothetical power consumption



CPA attack

- ◆ Measure power consumption traces and record d
- ◆ Compute hypothetical power consumption

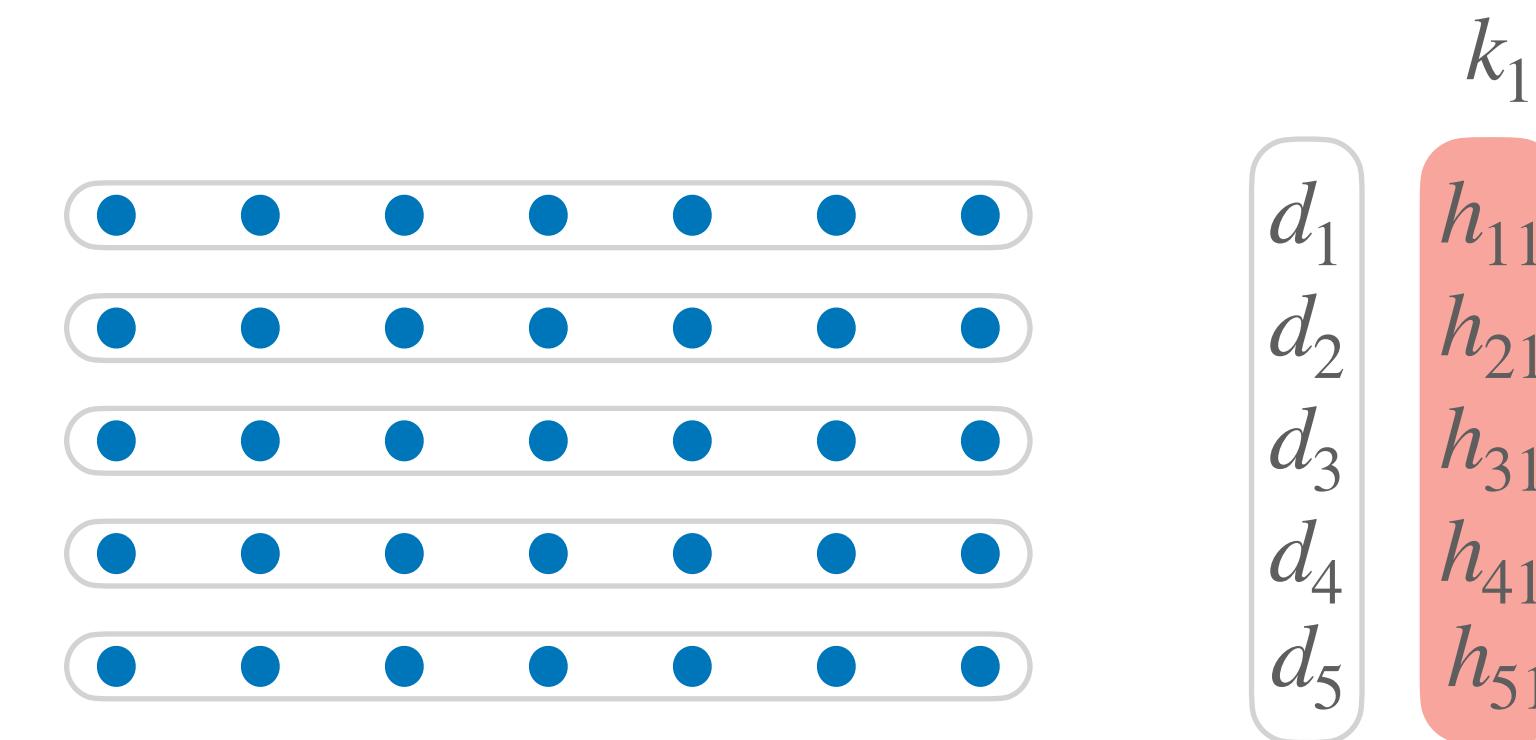
$$h = \text{HW}(f(d, k))$$



CPA attack

- ◆ Measure power consumption traces and record d
- ◆ Compute hypothetical power consumption

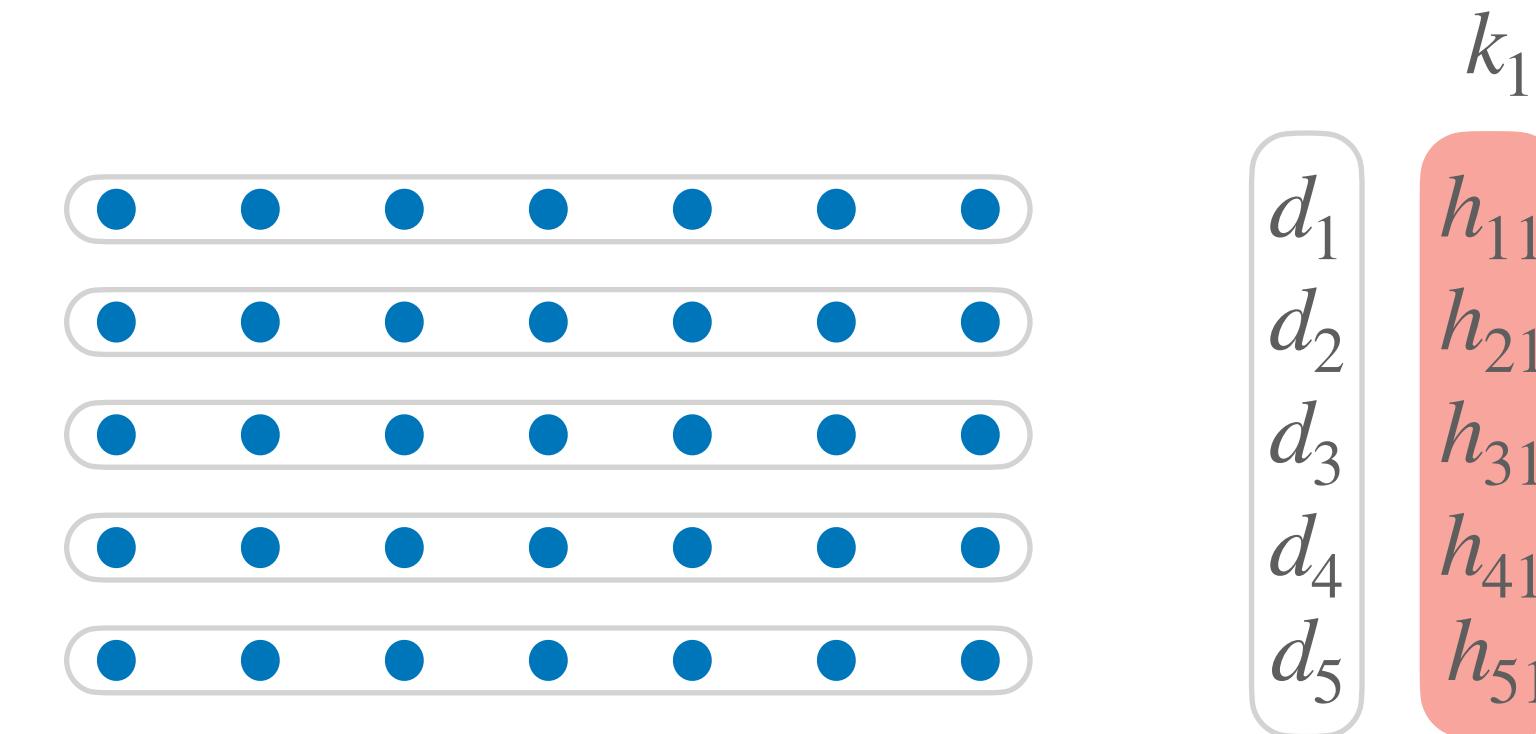
$$h = \text{HW}(f(d, k))$$



CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$

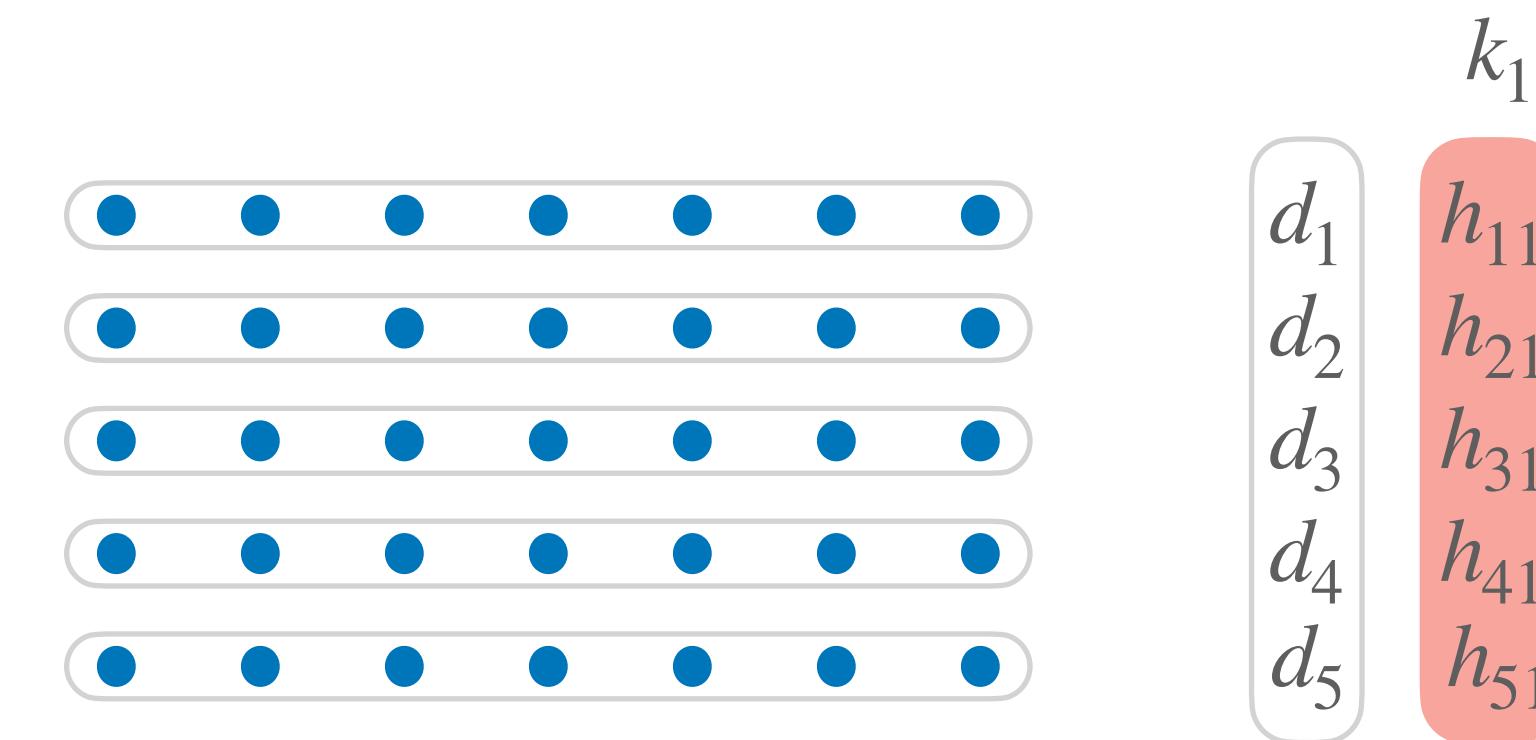


- ◆ Compute hypothetical power consumption
- ◆ Compare with measured power consumption

CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$



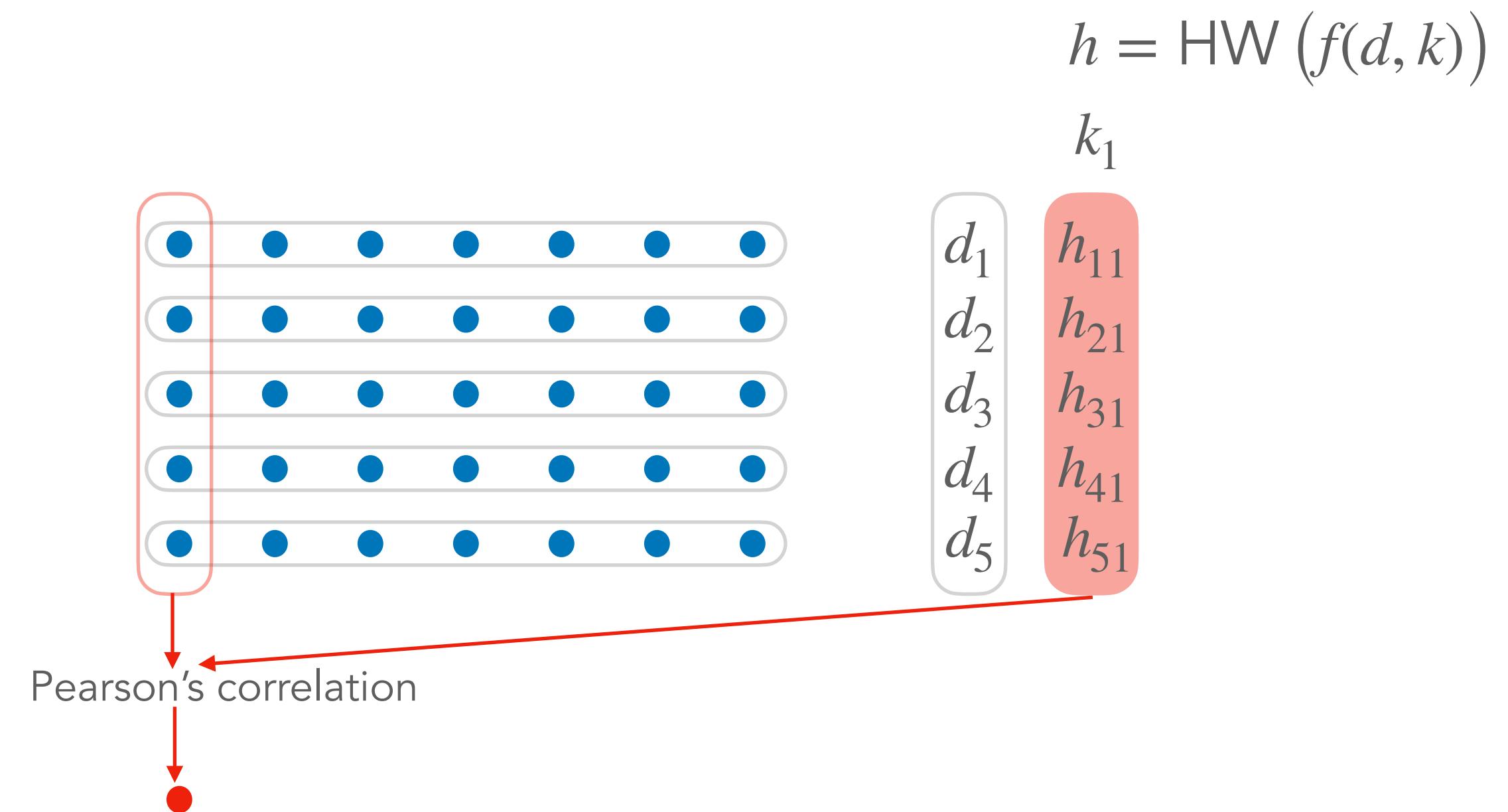
- ◆ Compute hypothetical power consumption

- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient

CPA attack

- ◆ Measure power consumption traces and record d
- ◆ Compute hypothetical power consumption
- ◆ Compare with measured power consumption

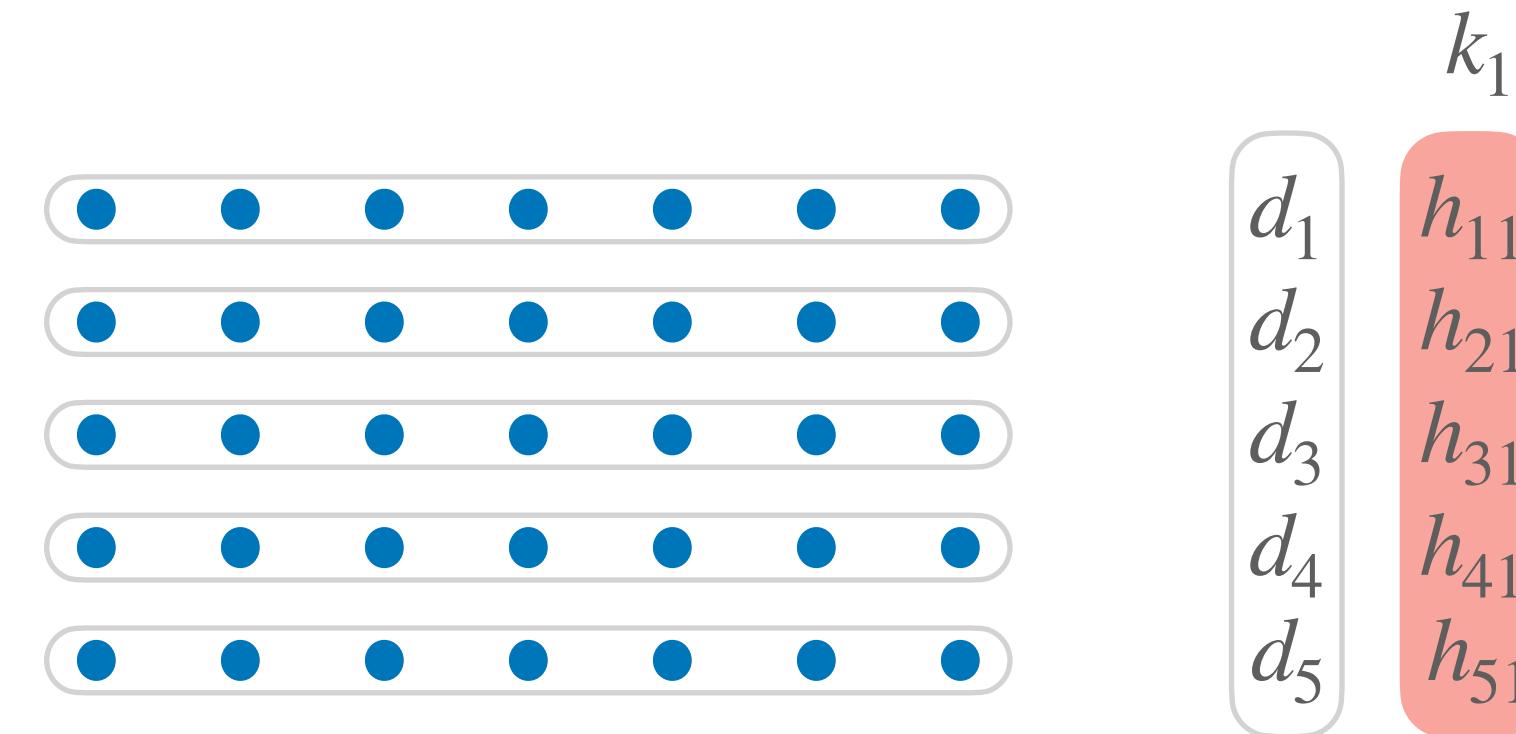


Linearity relationship
with Pearson's correlation coefficient

CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$



- ◆ Compute hypothetical power consumption

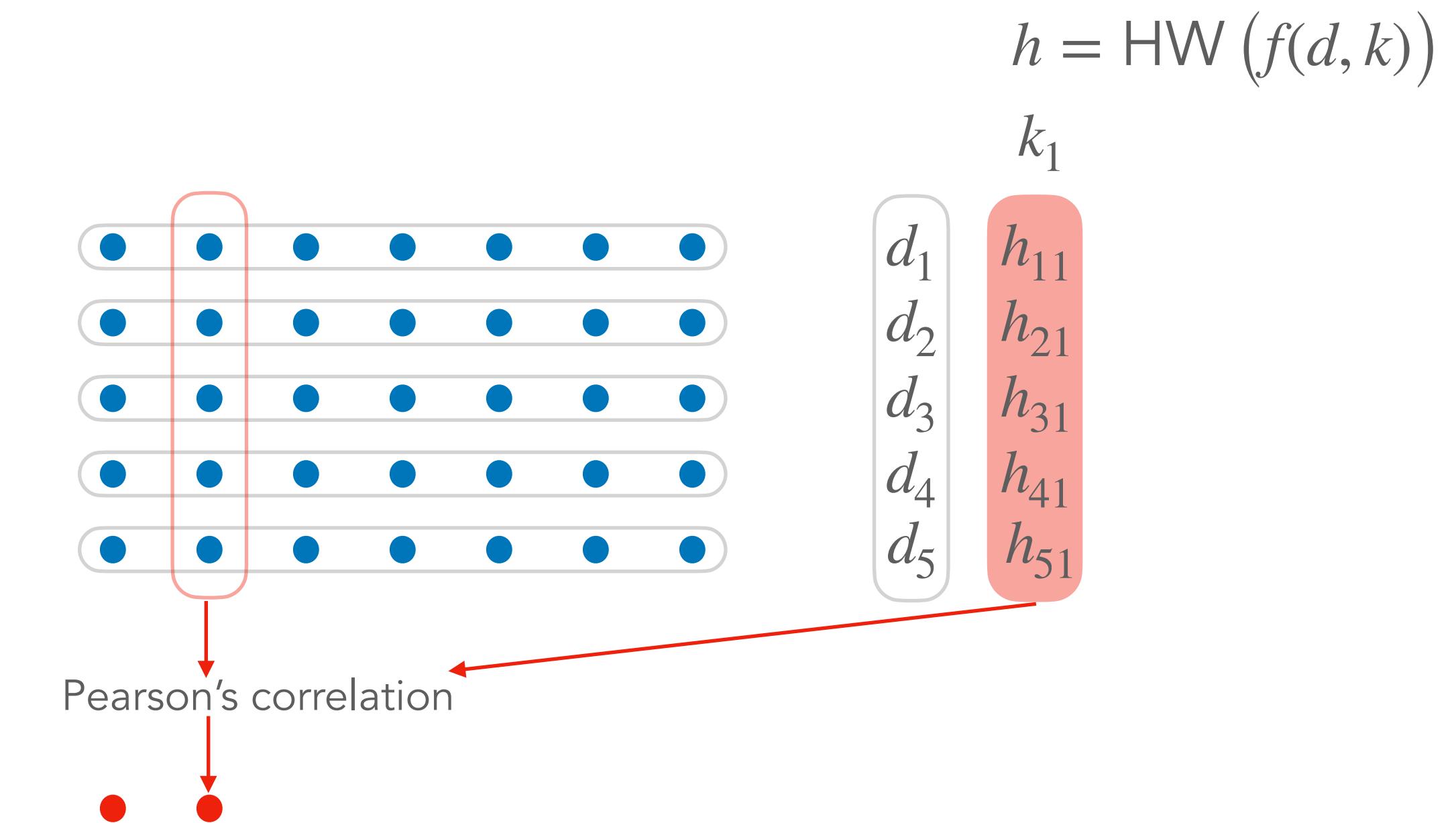
- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient

•

CPA attack

- ◆ Measure power consumption traces and record d
- ◆ Compute hypothetical power consumption
- ◆ Compare with measured power consumption

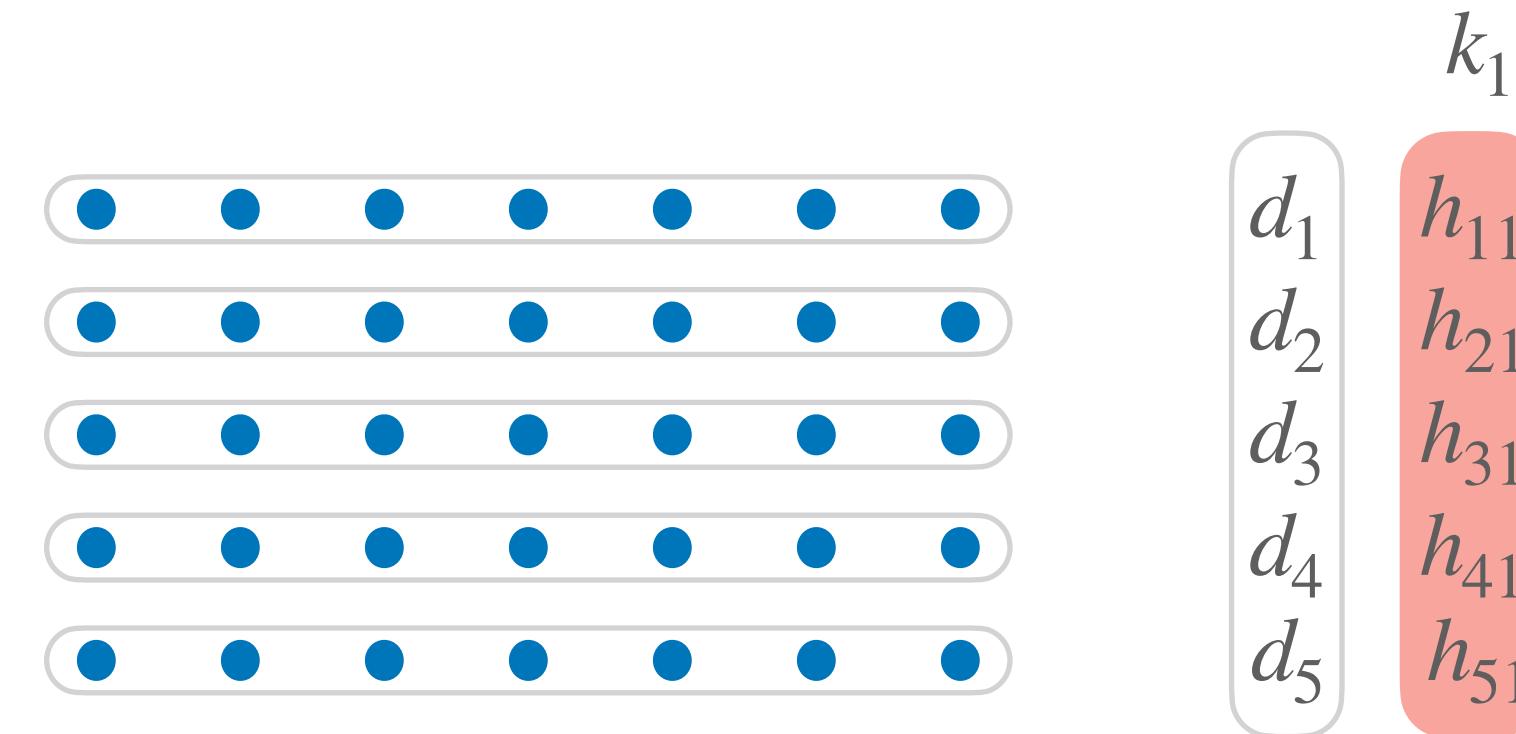


Linearity relationship
with Pearson's correlation coefficient

CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$



- ◆ Compute hypothetical power consumption

- ◆ Compare with measured power consumption

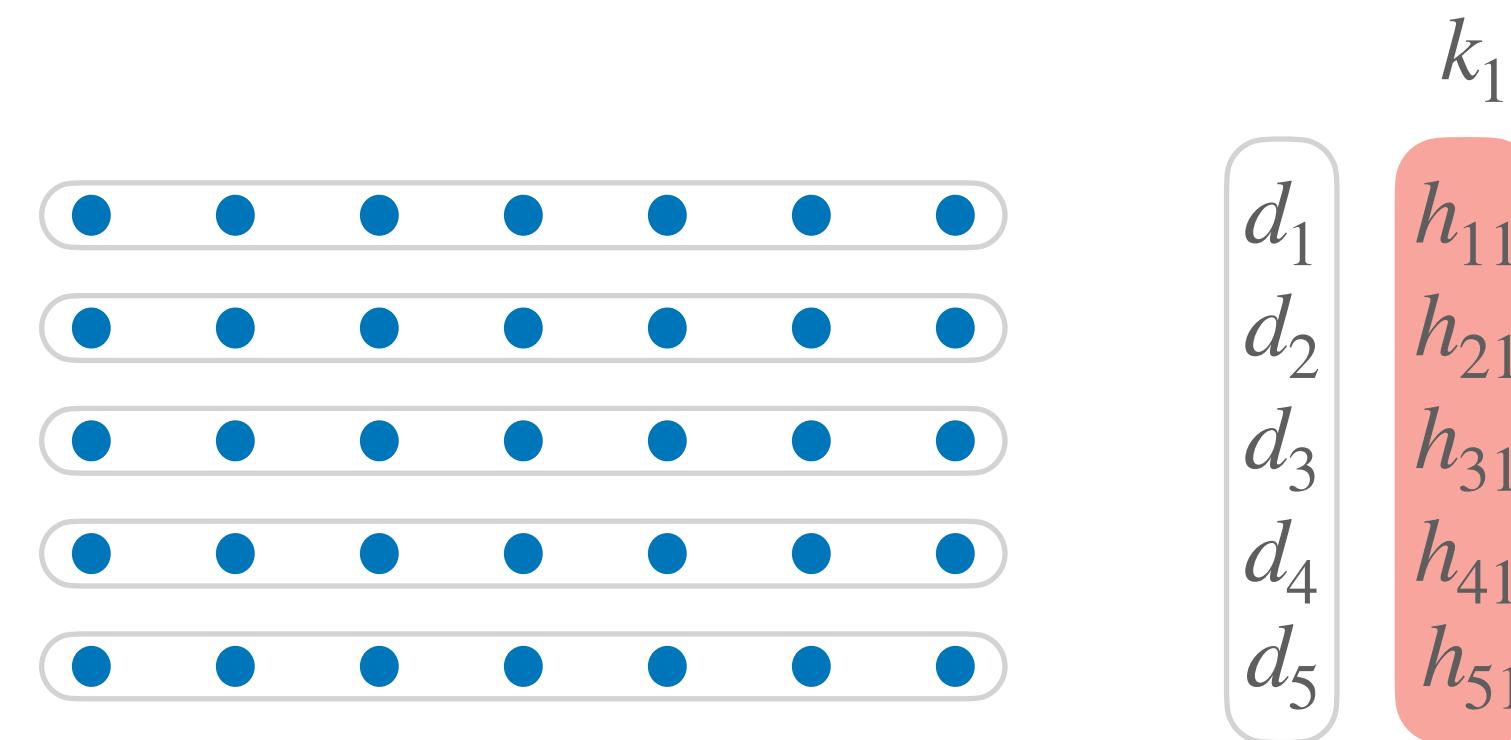


Linearity relationship
with Pearson's correlation coefficient

CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$



- ◆ Compute hypothetical power consumption

- ◆ Compare with measured power consumption

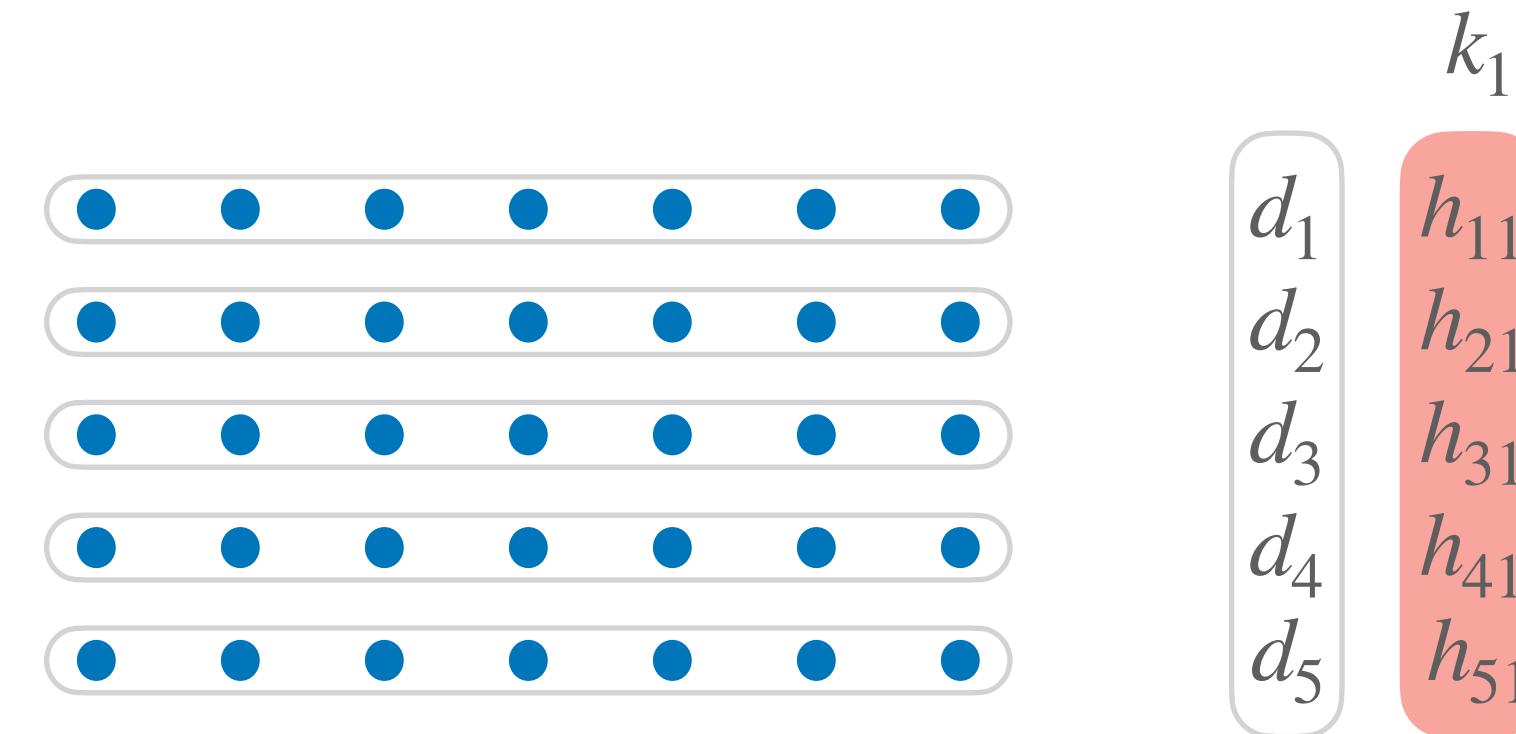


Linearity relationship
with Pearson's correlation coefficient

CPA attack

- ◆ Measure power consumption traces and record d

$$h = \text{HW}(f(d, k))$$



- ◆ Compute hypothetical power consumption

- ◆ Compare with measured power consumption

k_1 ● ● ● ● ● ● ●

Linearity relationship
with Pearson's correlation coefficient

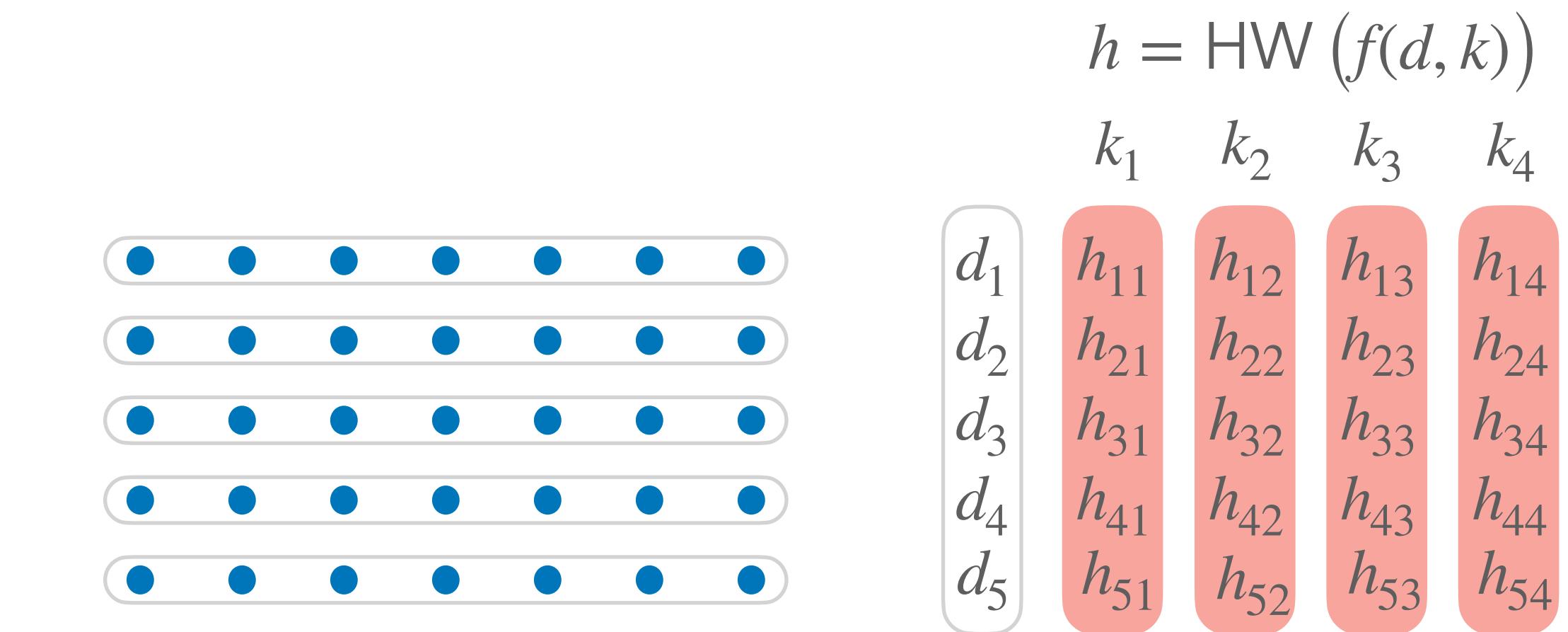
CPA attack

- ◆ Measure power consumption traces and record d

- ◆ Compute hypothetical power consumption

- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient



k_1

CPA attack

- ◆ Measure power consumption traces and record d

- ◆ Compute hypothetical power consumption

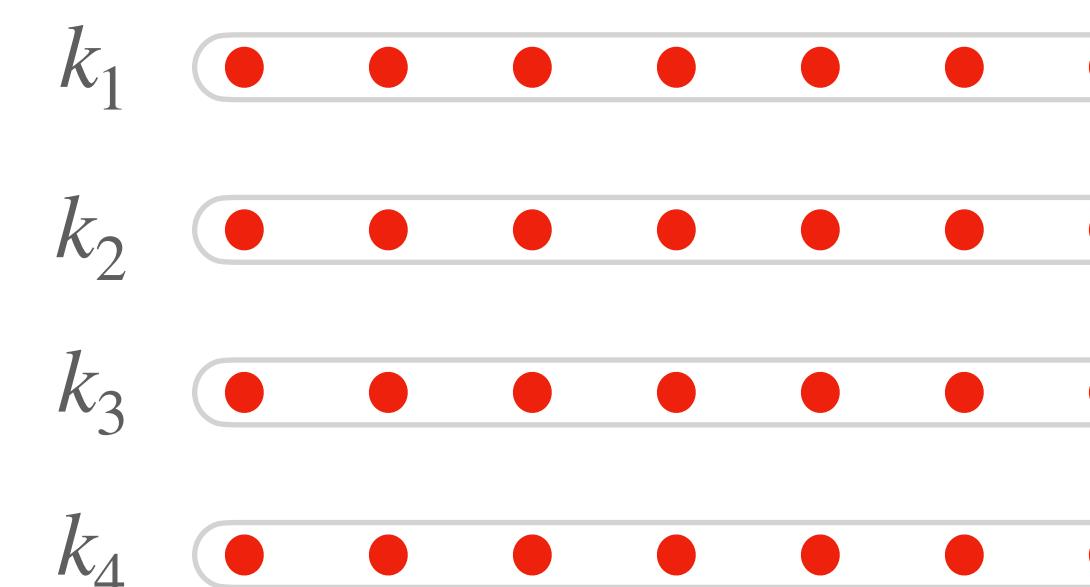
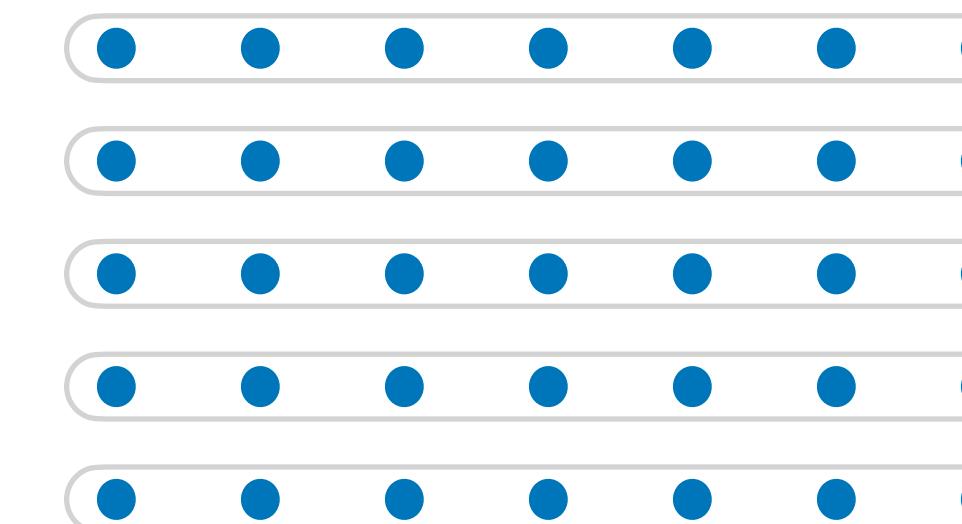
- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient

$$h = \text{HW}(f(d, k))$$

$$k_1 \quad k_2 \quad k_3 \quad k_4$$

d_1	h_{11}	h_{12}	h_{13}	h_{14}
d_2	h_{21}	h_{22}	h_{23}	h_{24}
d_3	h_{31}	h_{32}	h_{33}	h_{34}
d_4	h_{41}	h_{42}	h_{43}	h_{44}
d_5	h_{51}	h_{52}	h_{53}	h_{54}



CPA attack

- ◆ Measure power consumption traces and record d

- ◆ Compute hypothetical power consumption

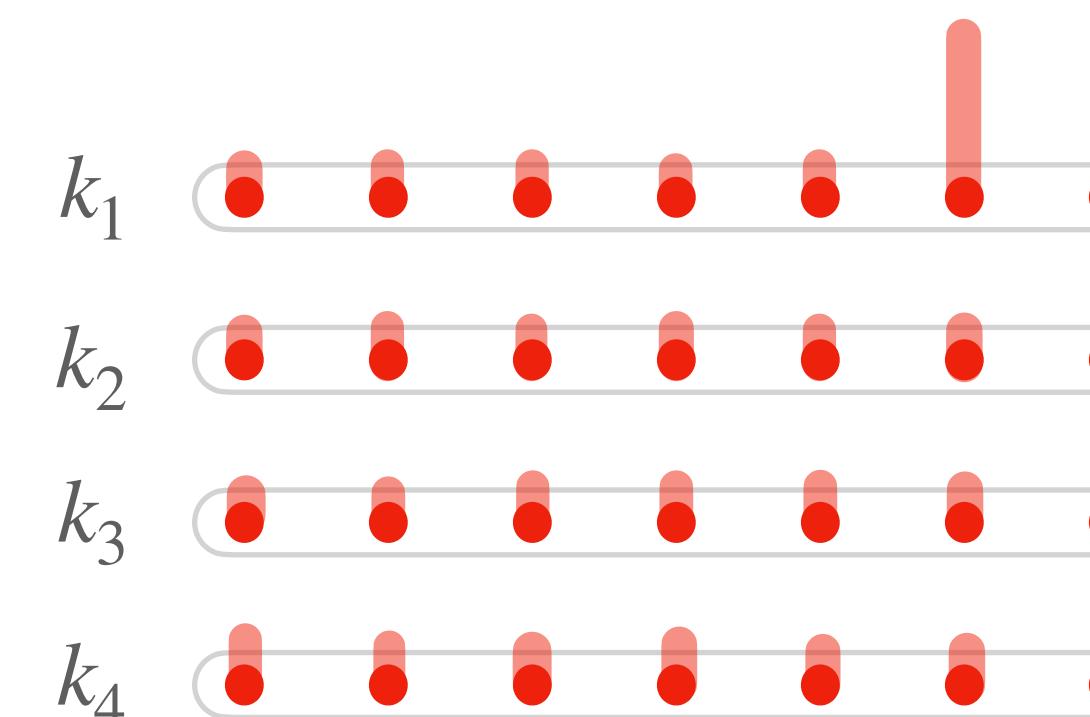
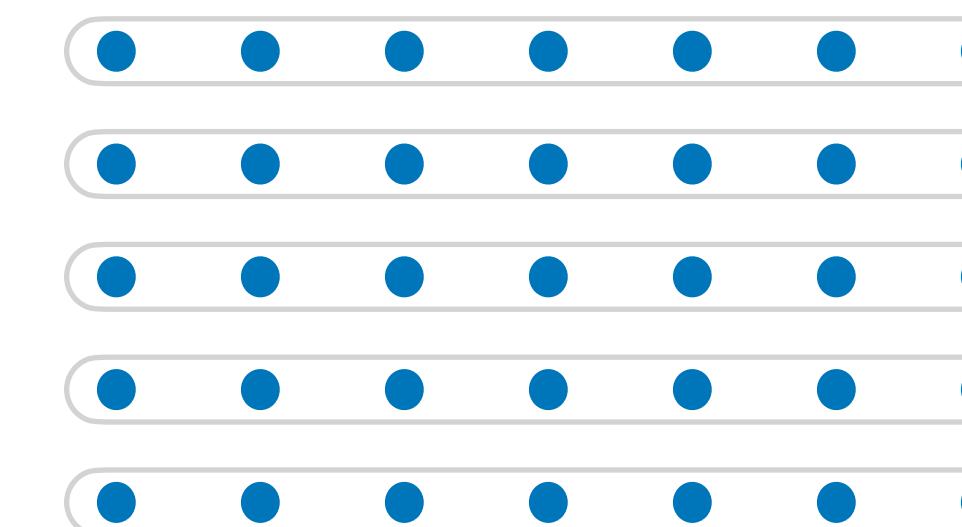
- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient

$$h = \text{HW}(f(d, k))$$

$$k_1 \quad k_2 \quad k_3 \quad k_4$$

d_1	h_{11}	h_{12}	h_{13}	h_{14}
d_2	h_{21}	h_{22}	h_{23}	h_{24}
d_3	h_{31}	h_{32}	h_{33}	h_{34}
d_4	h_{41}	h_{42}	h_{43}	h_{44}
d_5	h_{51}	h_{52}	h_{53}	h_{54}



CPA attack

- ◆ Measure power consumption traces and record d

- ◆ Compute hypothetical power consumption

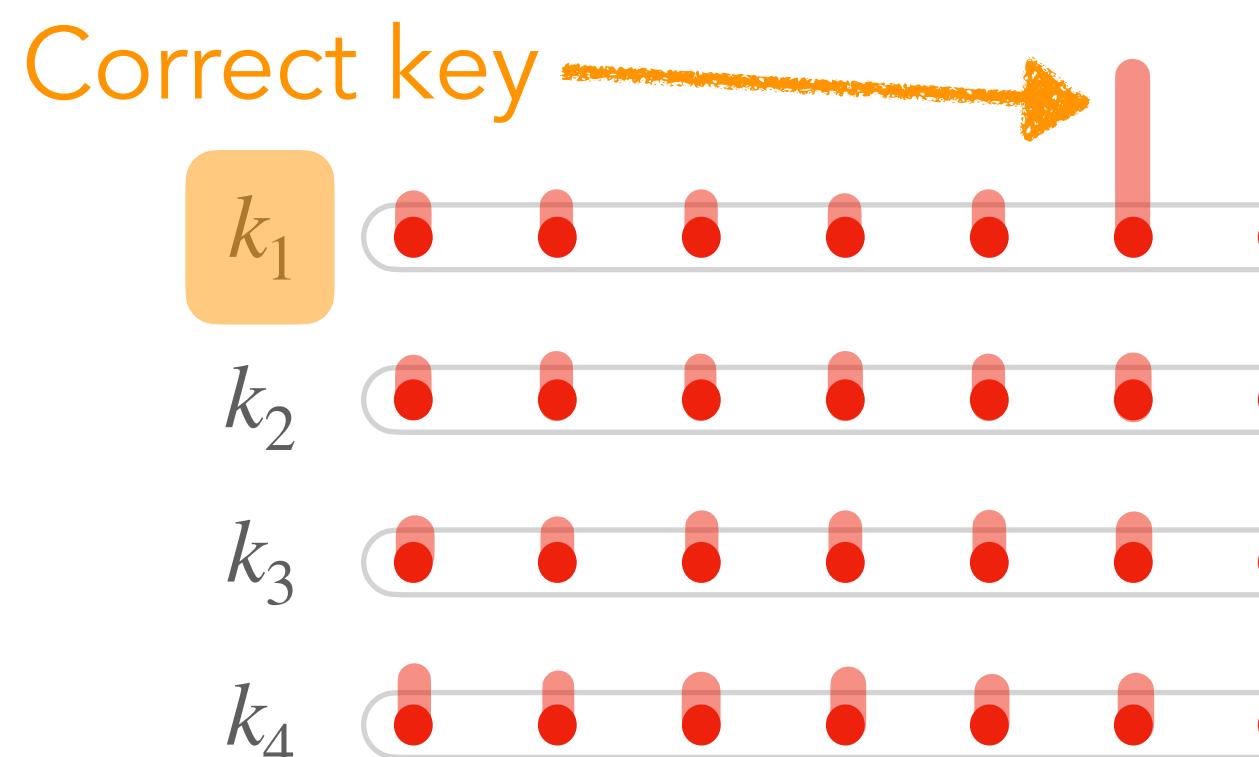
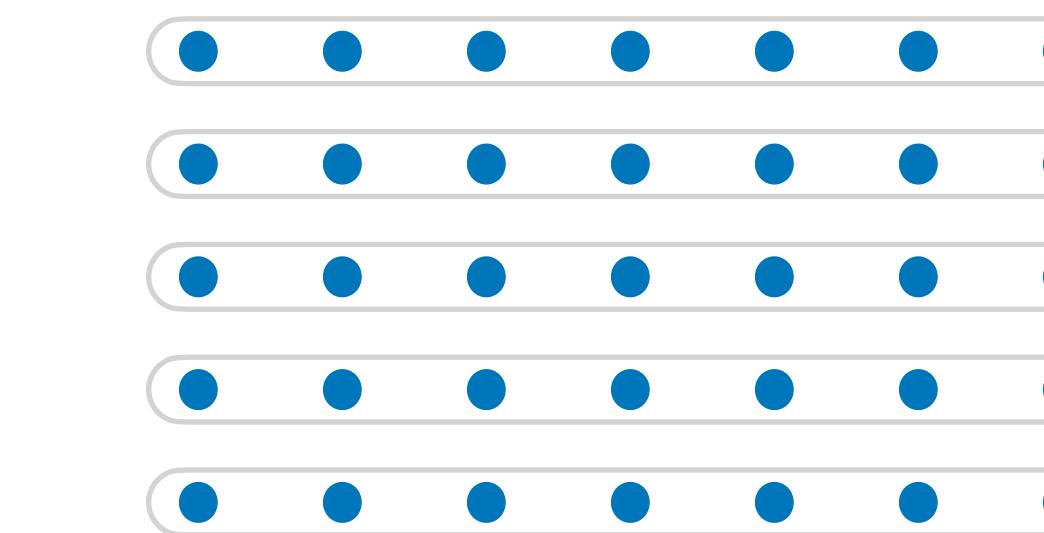
- ◆ Compare with measured power consumption

Linearity relationship with Pearson's correlation coefficient

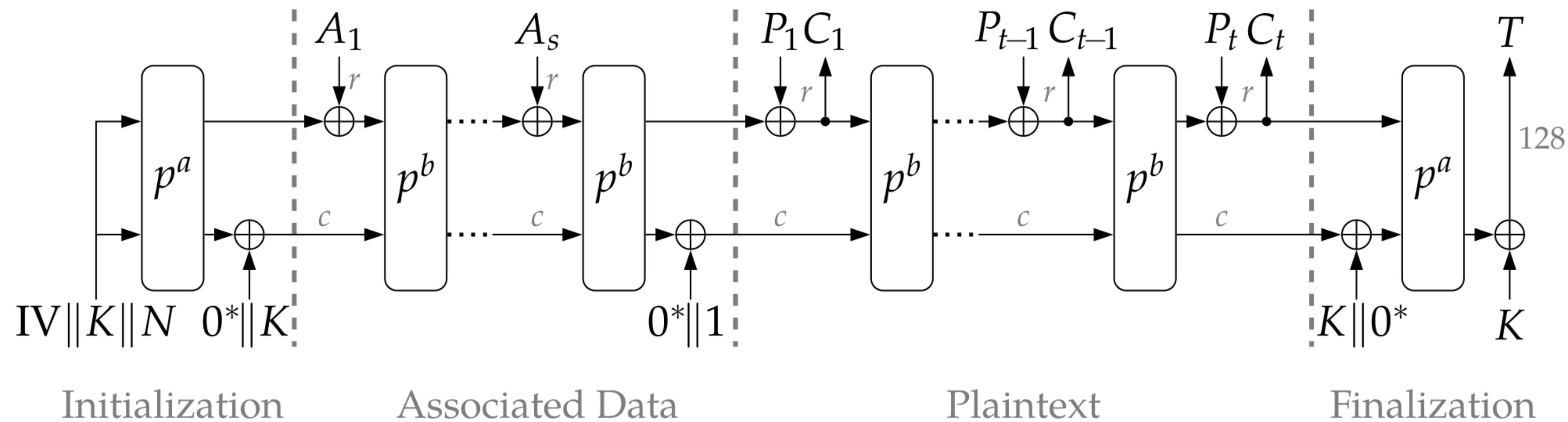
$$h = \text{HW}(f(d, k))$$

$$k_1 \quad k_2 \quad k_3 \quad k_4$$

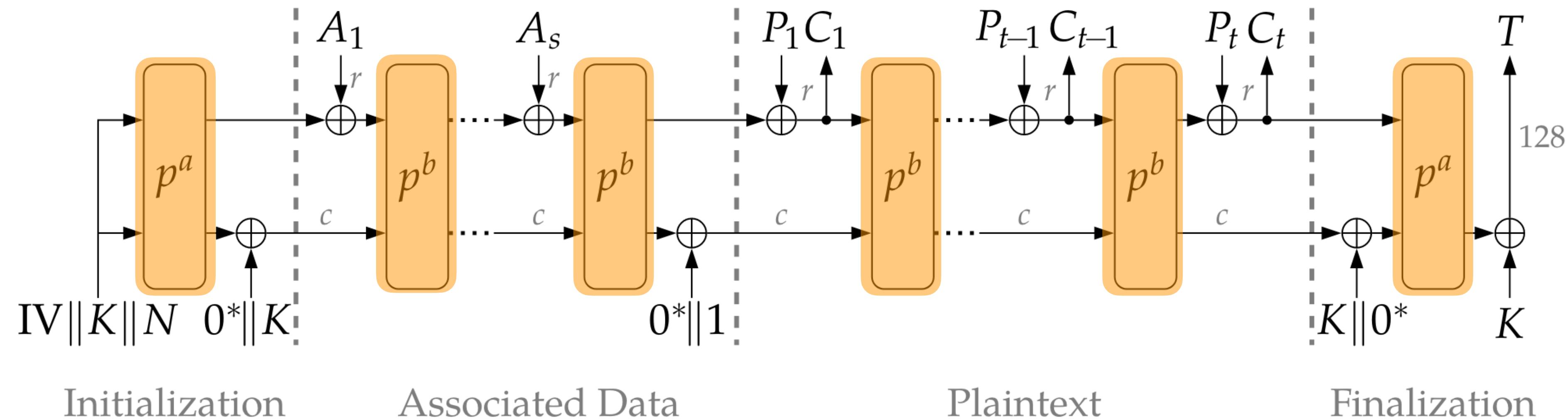
d_1	h_{11}	h_{12}	h_{13}	h_{14}
d_2	h_{21}	h_{22}	h_{23}	h_{24}
d_3	h_{31}	h_{32}	h_{33}	h_{34}
d_4	h_{41}	h_{42}	h_{43}	h_{44}
d_5	h_{51}	h_{52}	h_{53}	h_{54}



Ascon

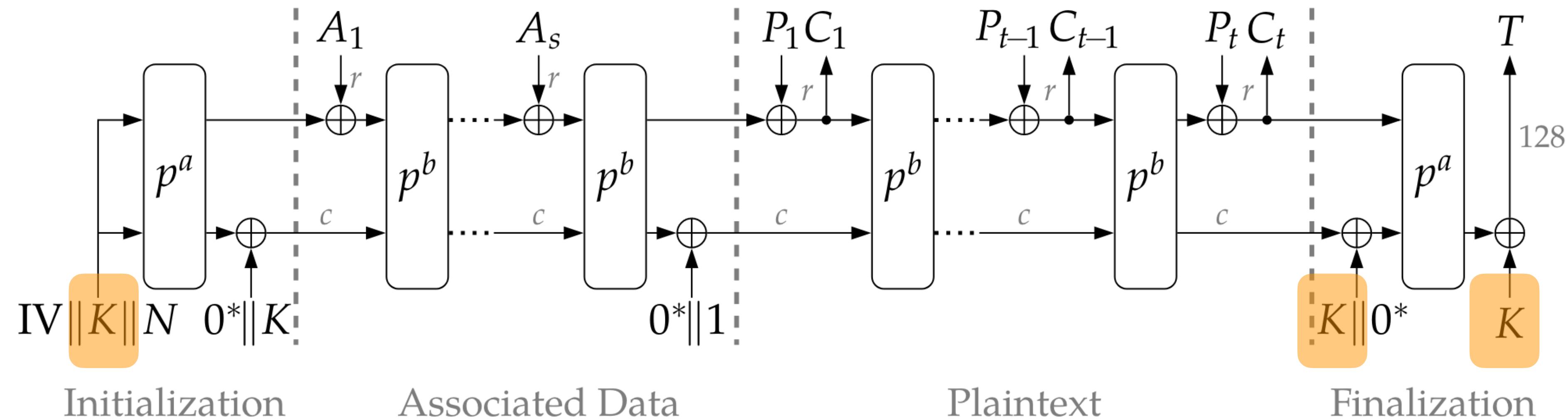


Permutation blocks

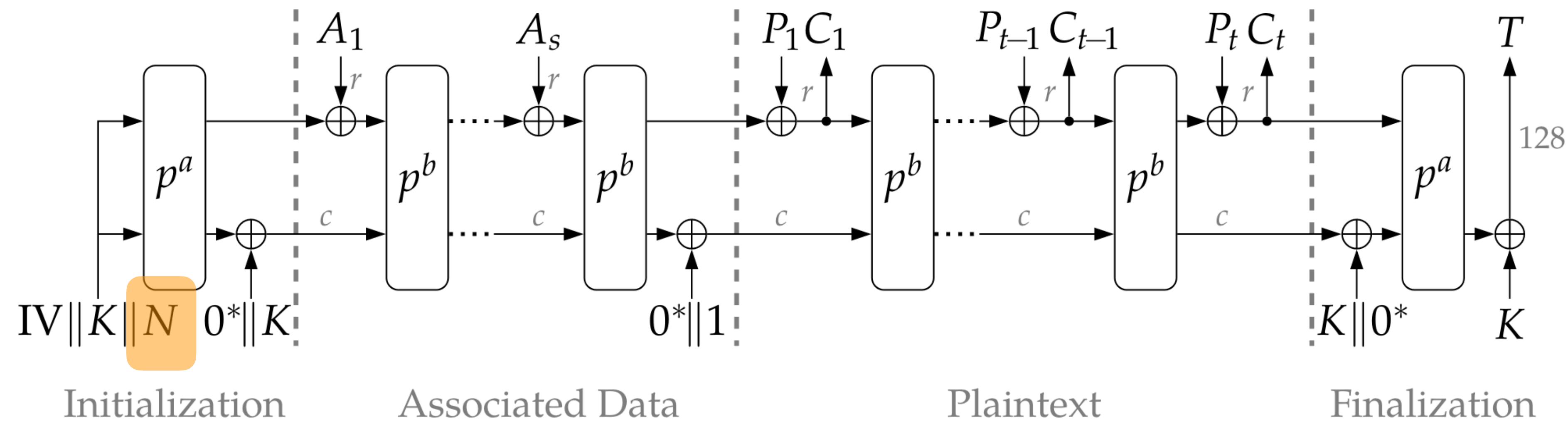


- ▶ p^a : 12 rounds
- ▶ p^b : 6 rounds

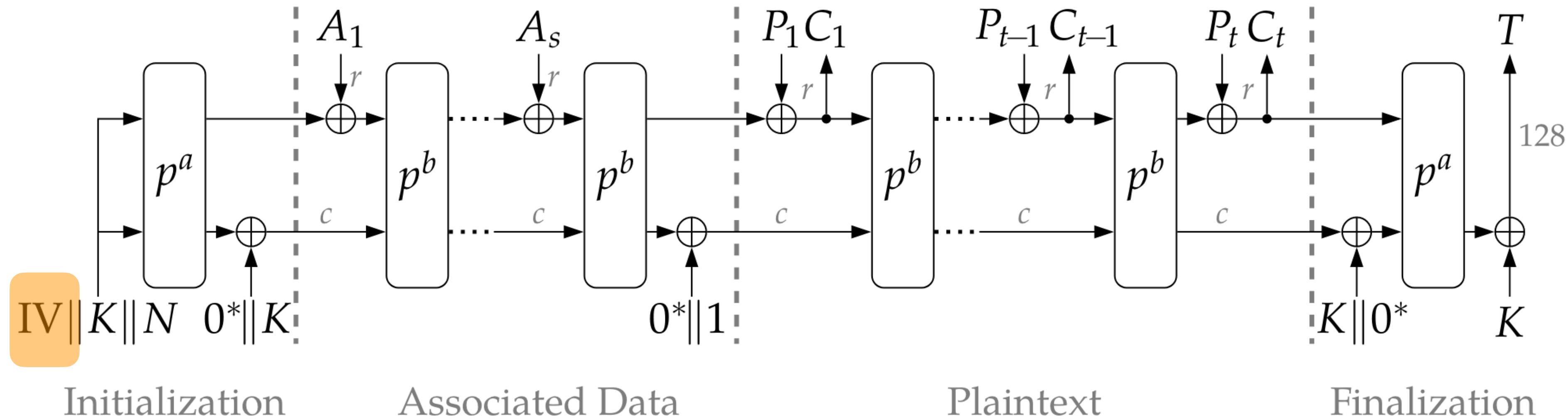
Key (128 bits)



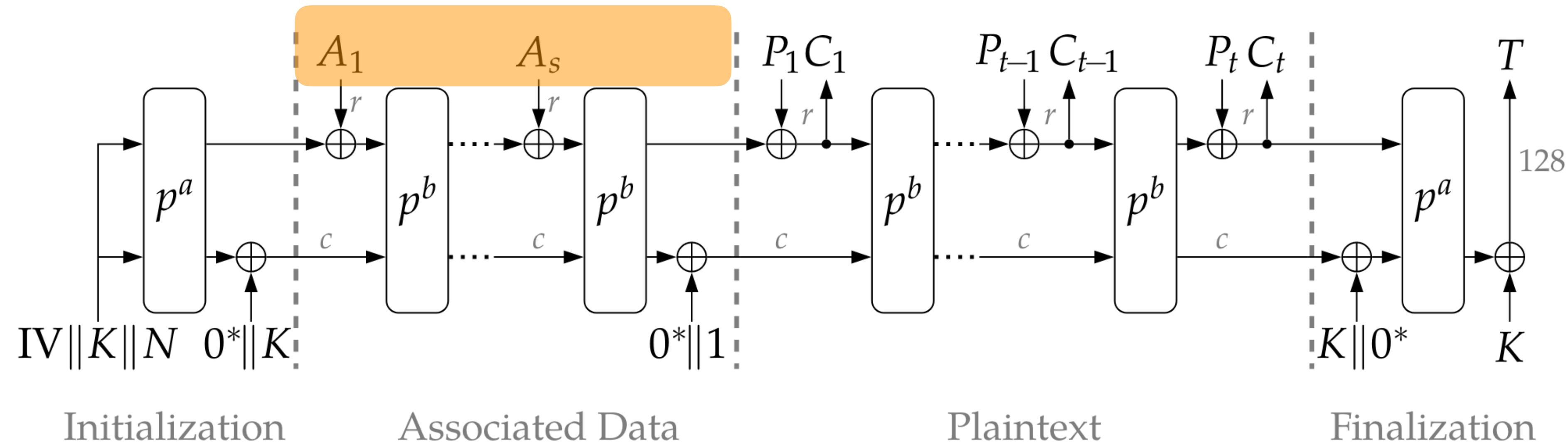
Nonce (128 bits)



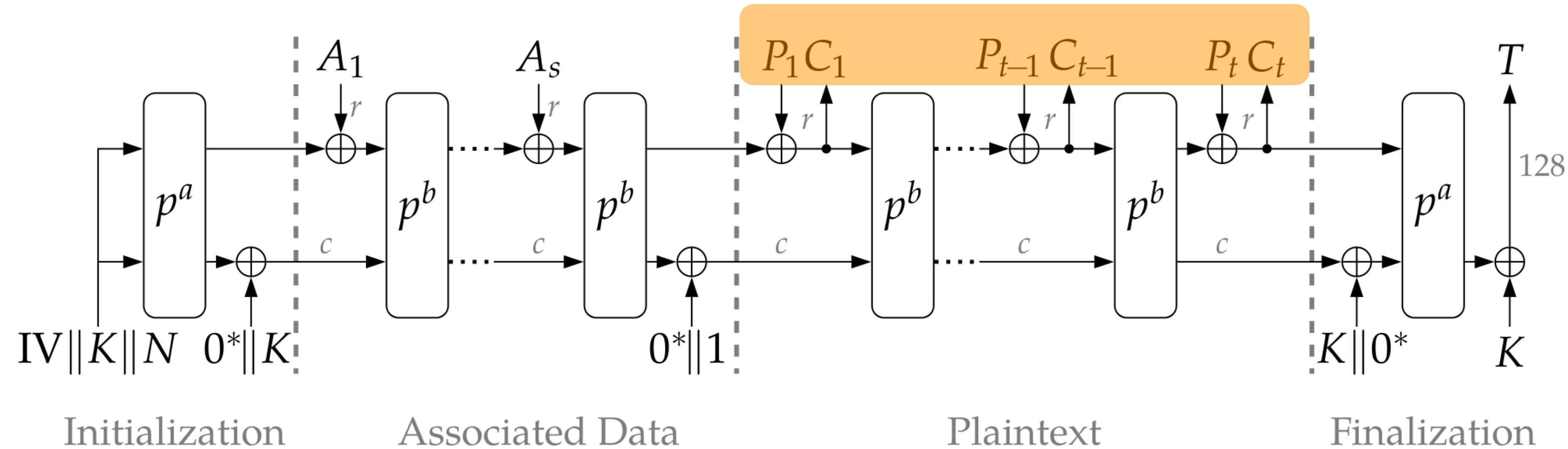
Initialization vector



Associated data



Plaintext / Ciphertext



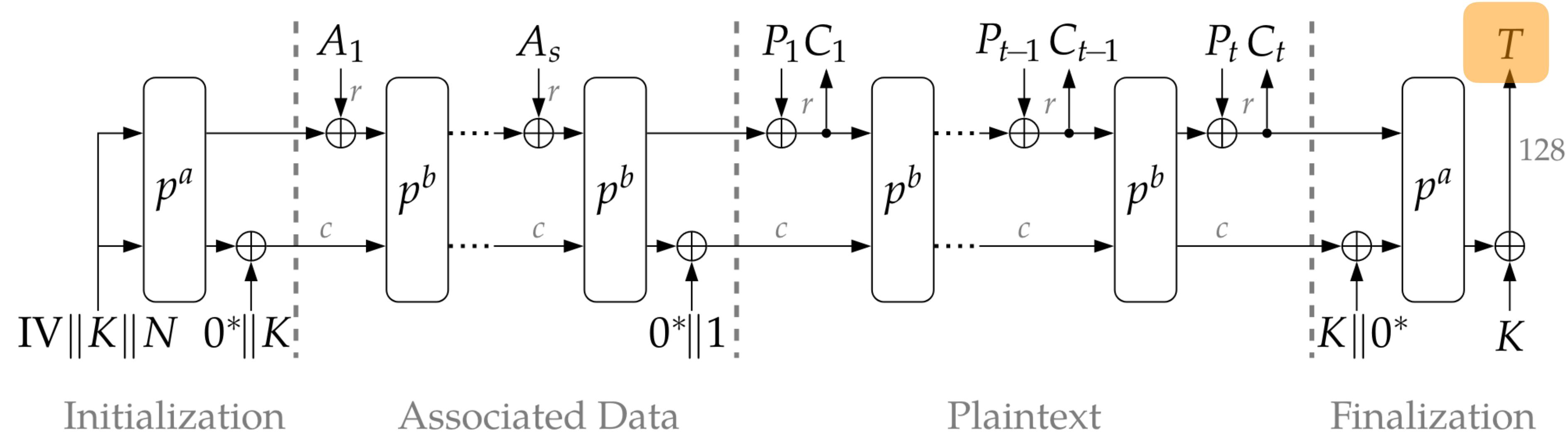
Initialization

Associated Data

Plaintext

Finalization

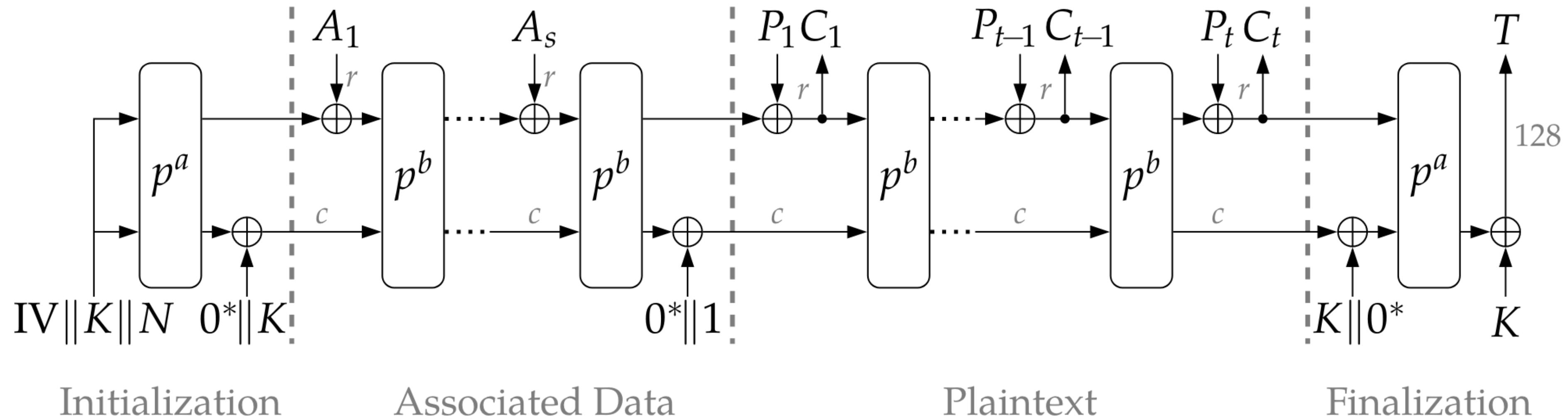
Verification Tag



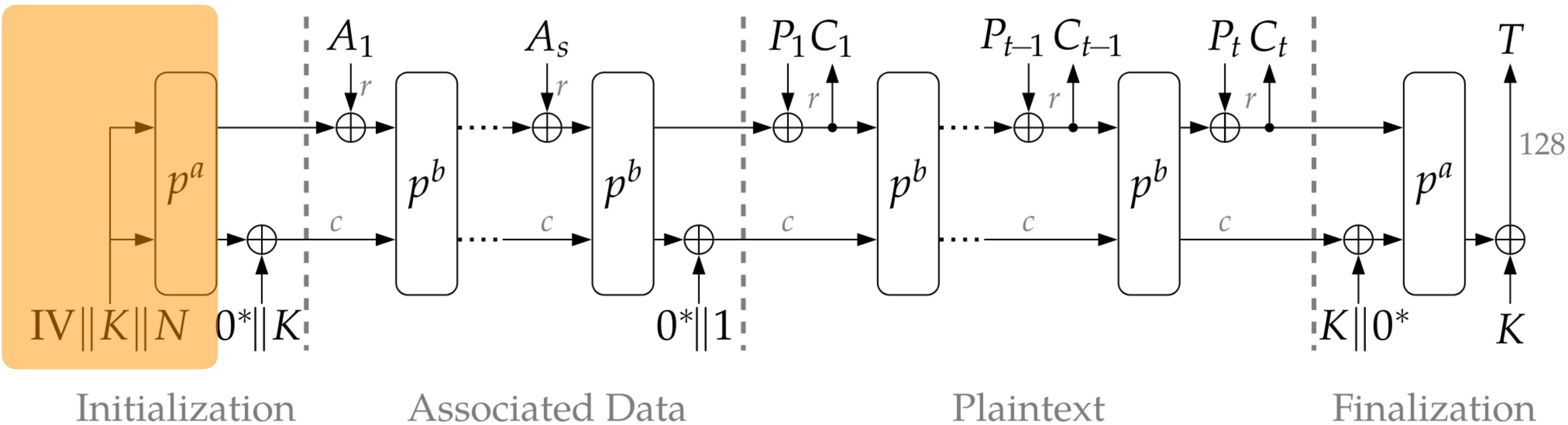


Existing CPA attacks on Ascon

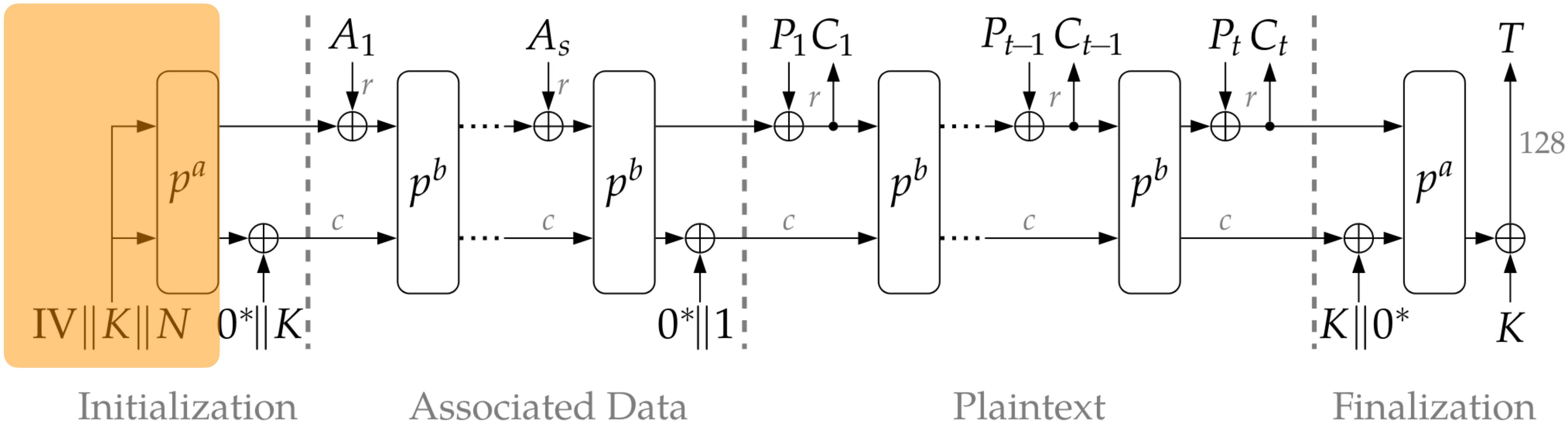
Selection function $v = f(d, k)$



Selection function $v = f(d, k)$



Selection function $v = f(d, k)$



Choose $v = f(\text{nonce}, \text{key})$

intermediate variable v in the *first round*

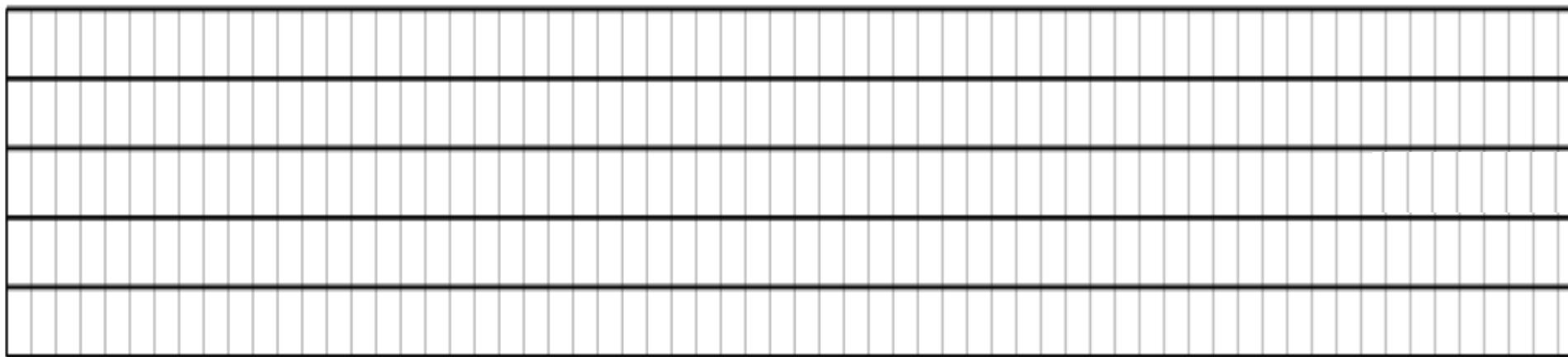
round computation

Round computation



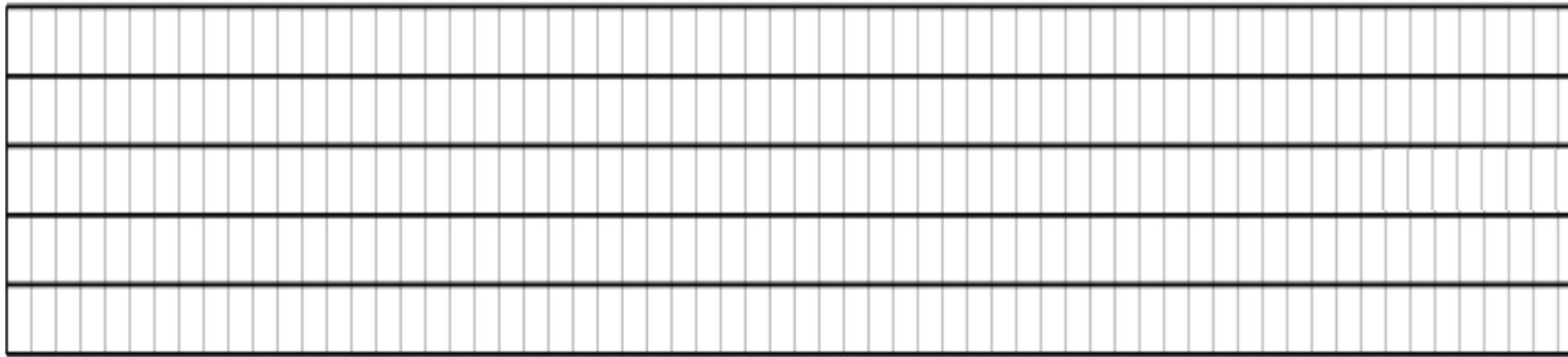
Round computation

On 320-bit state = $5 \times 64\text{-bit words}$



Round computation

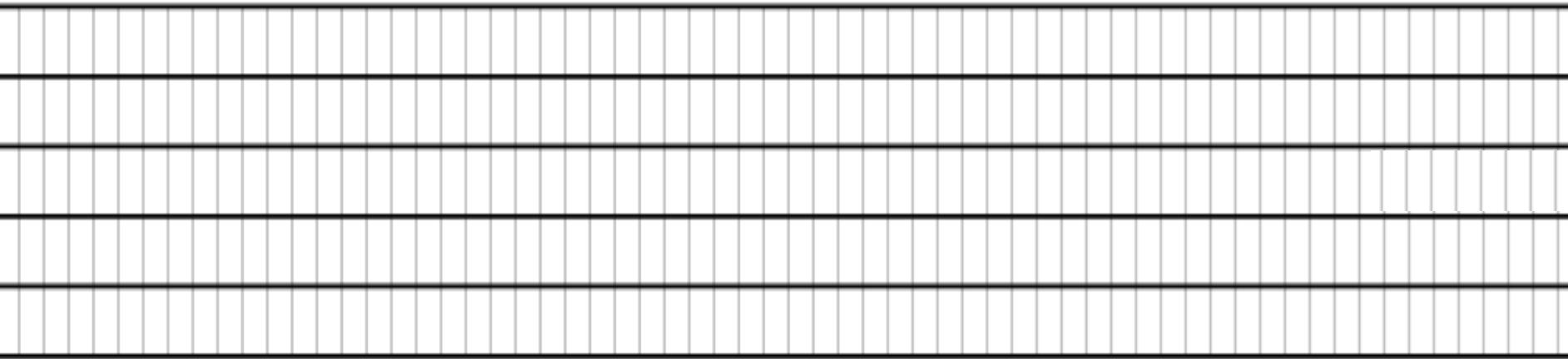
On 320-bit state = $5 \times 64\text{-bit words}$



Input of the first round:

Round computation

On 320-bit state = $5 \times 64\text{-bit words}$

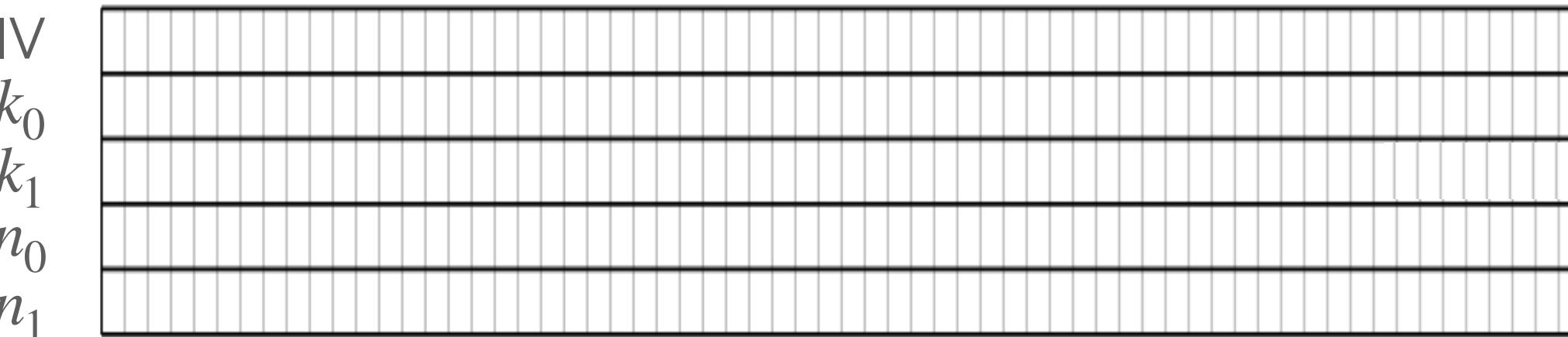


Input of the first round:

- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV

Round computation

On 320-bit state = $5 \times 64\text{-bit words}$

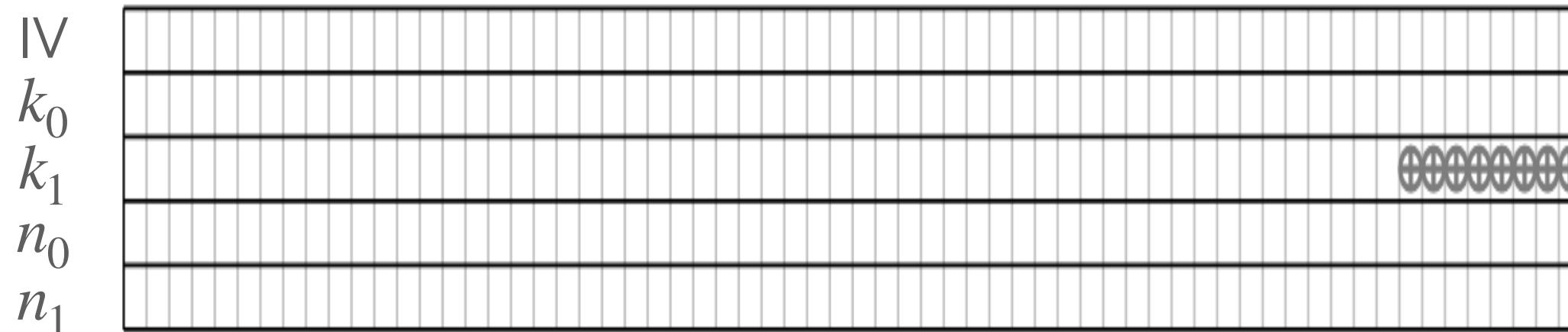


Input of the first round:

- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV

Round computation

On 320-bit state = $5 \times 64\text{-bit words}$



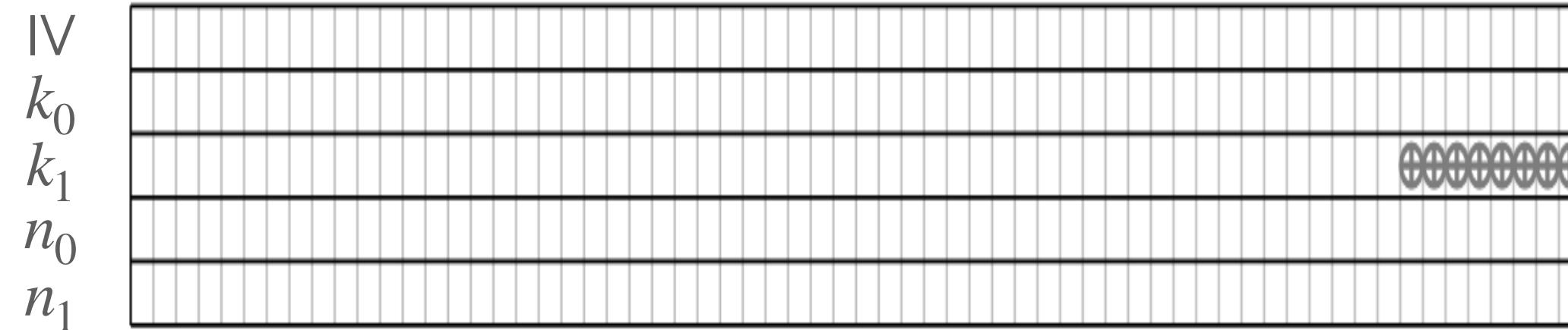
Input of the first round:

- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV

(1) Round constant addition

Round computation

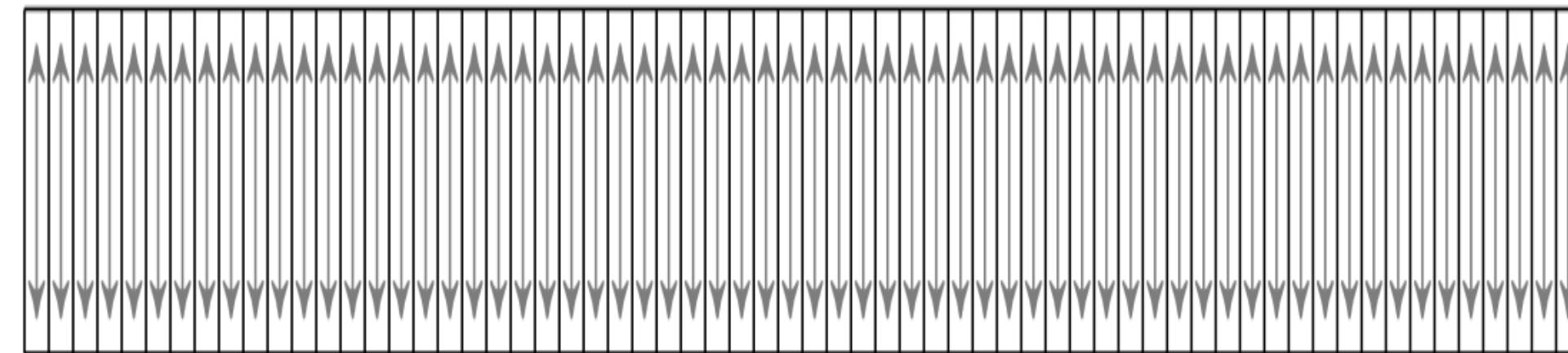
On 320-bit state = 5×64 -bit words



(1) Round constant addition

Input of the first round:

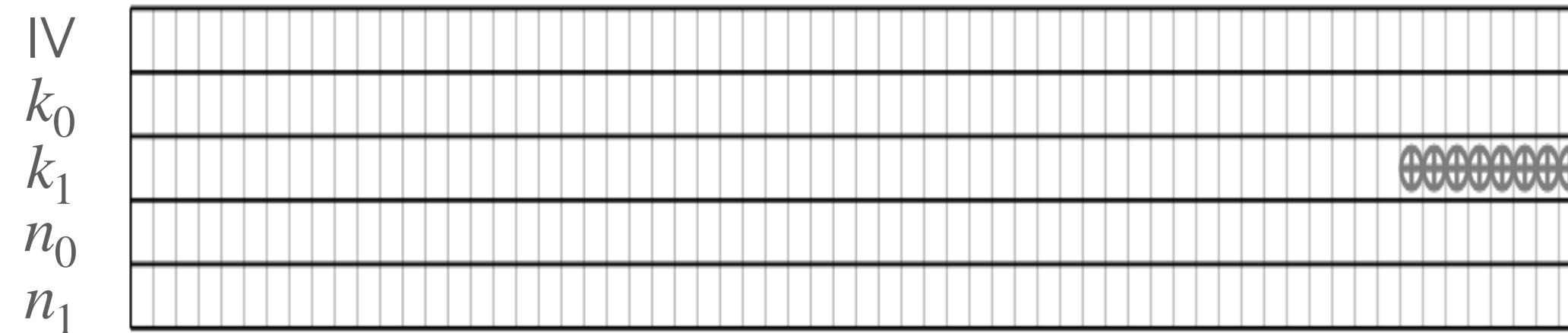
- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV



(2) Substitution *(vertical)*

Round computation

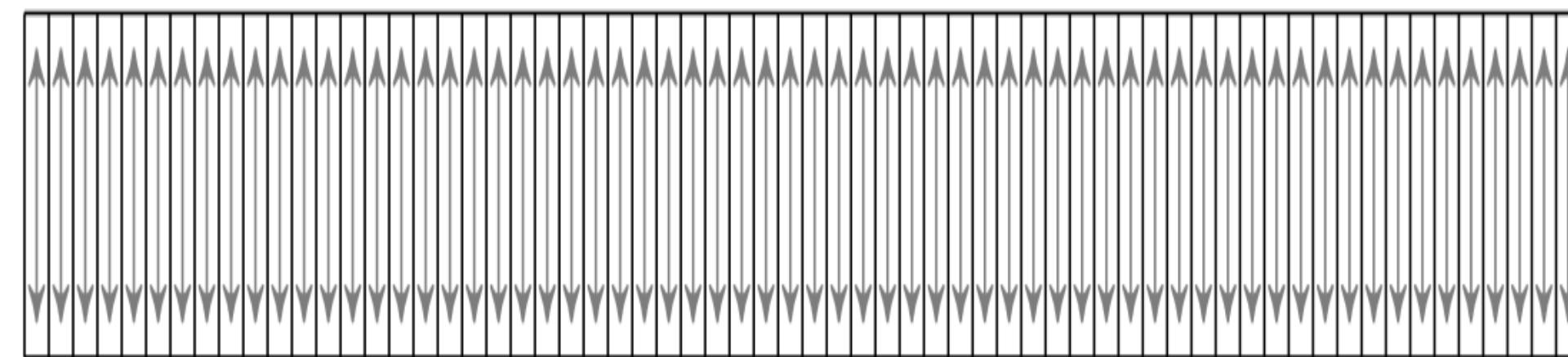
On 320-bit state = 5×64 -bit words



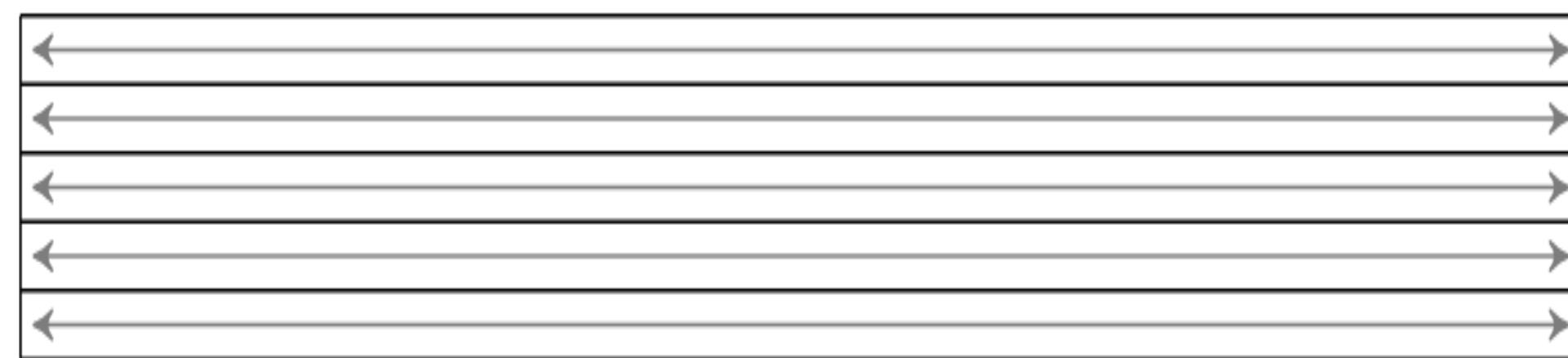
(1) Round constant addition

Input of the first round:

- 128-bit key : $K = (k_0, k_1)$
- 128-bit nonce : $N = (n_0, n_1)$
- 64-bit init. vector : IV



(2) Substitution (*vertical*)

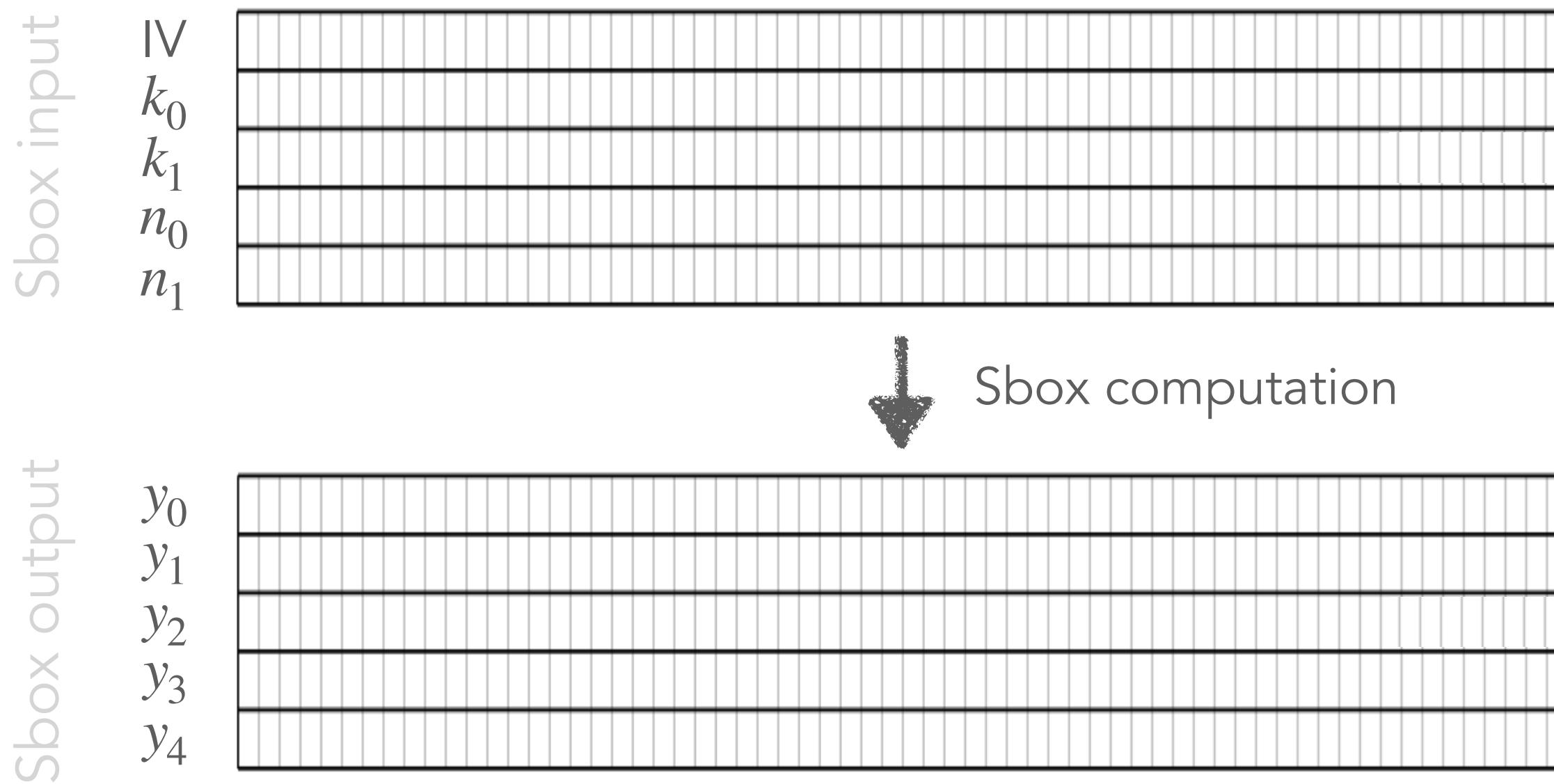


(3) Linear diffusion (*horizontal*)

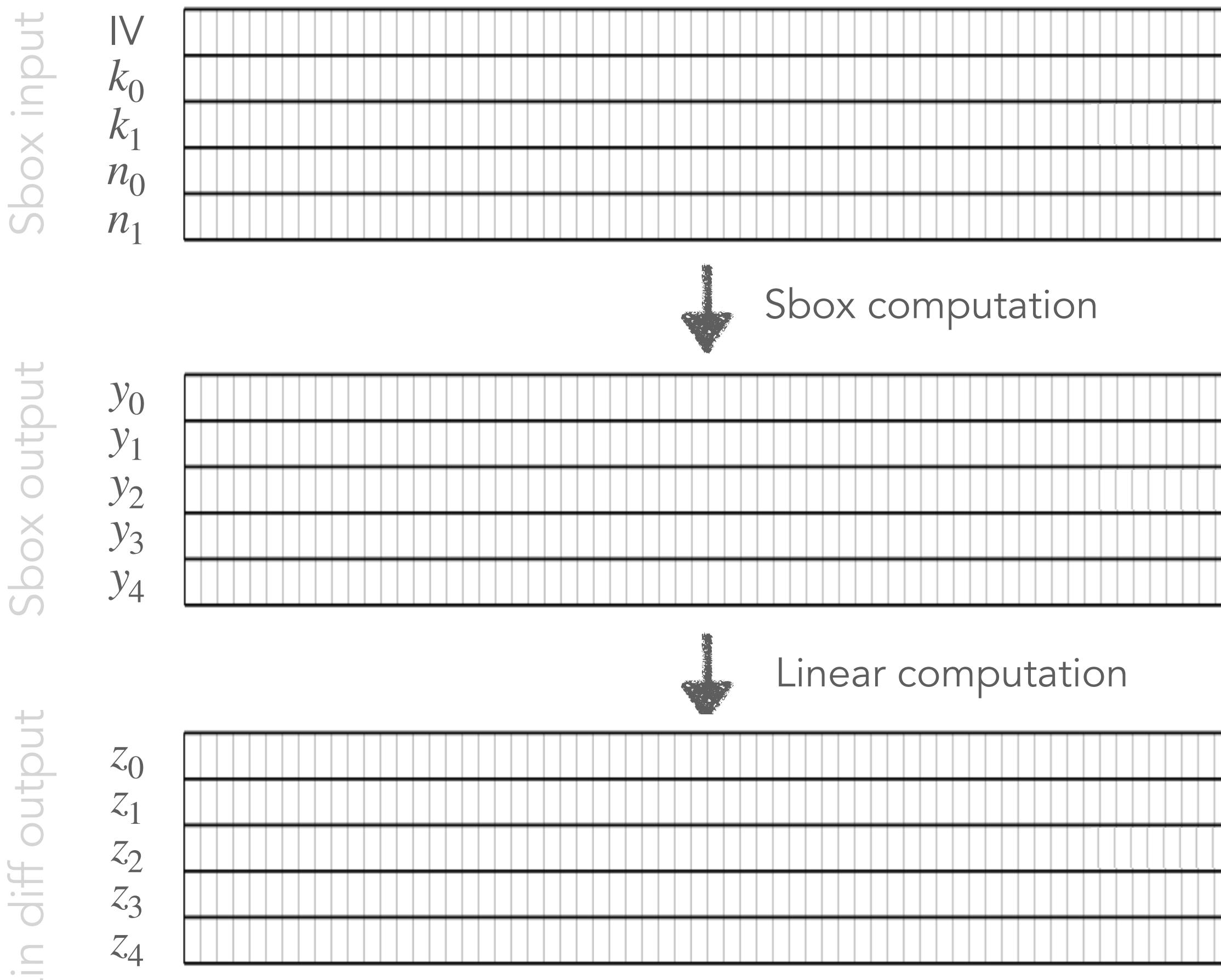
State changes

Sbox input

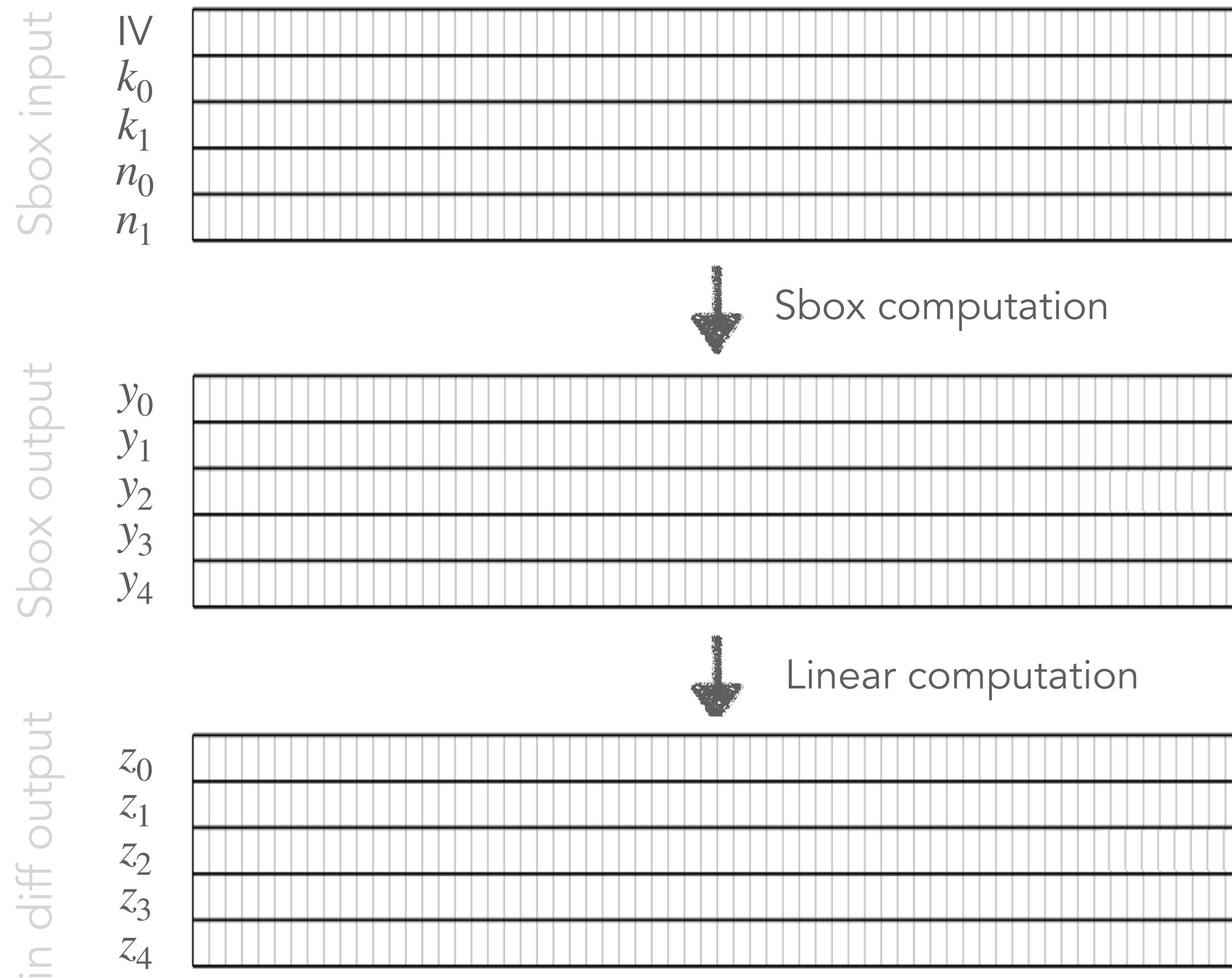
State changes



State changes



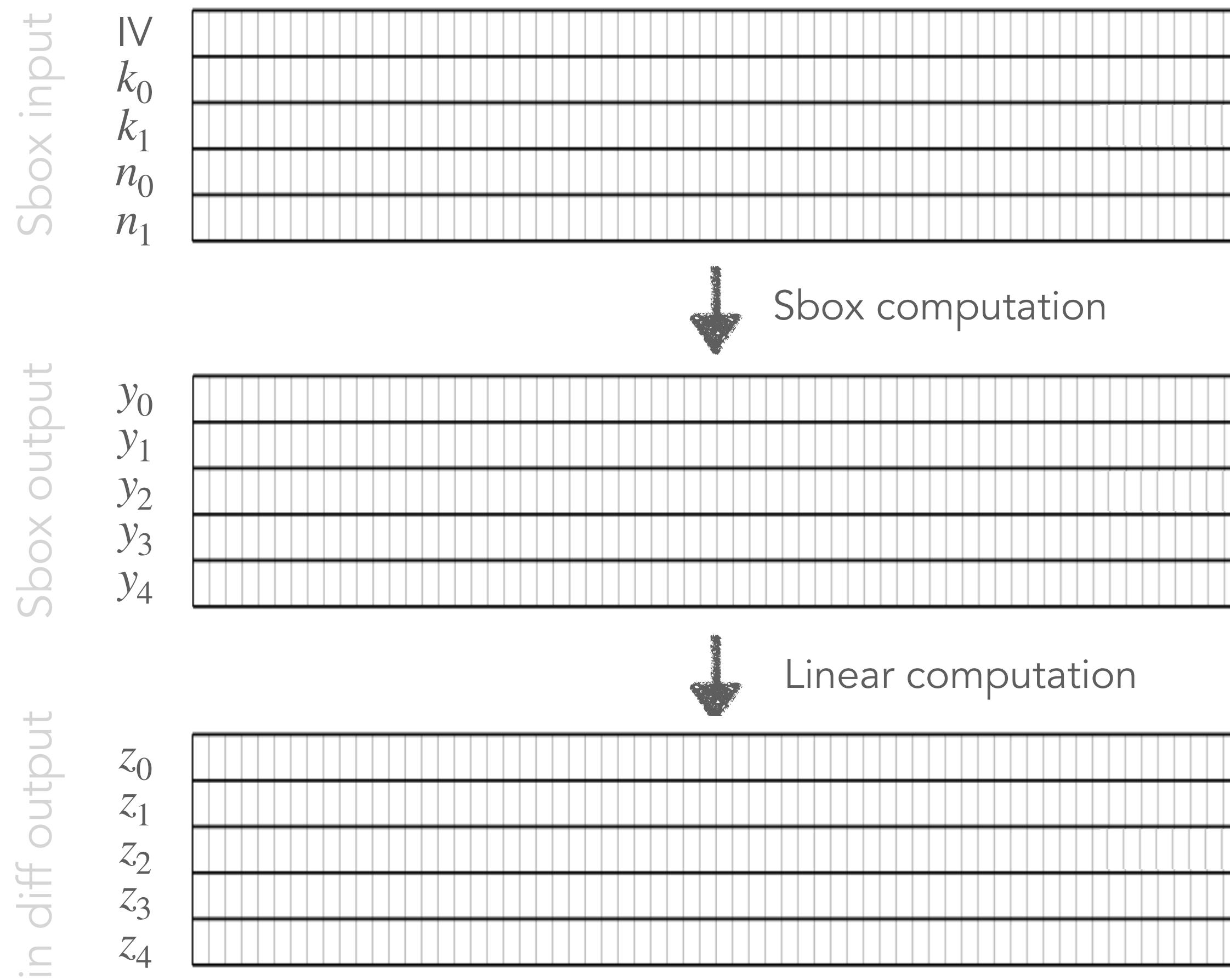
Previous CPA (1)



Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

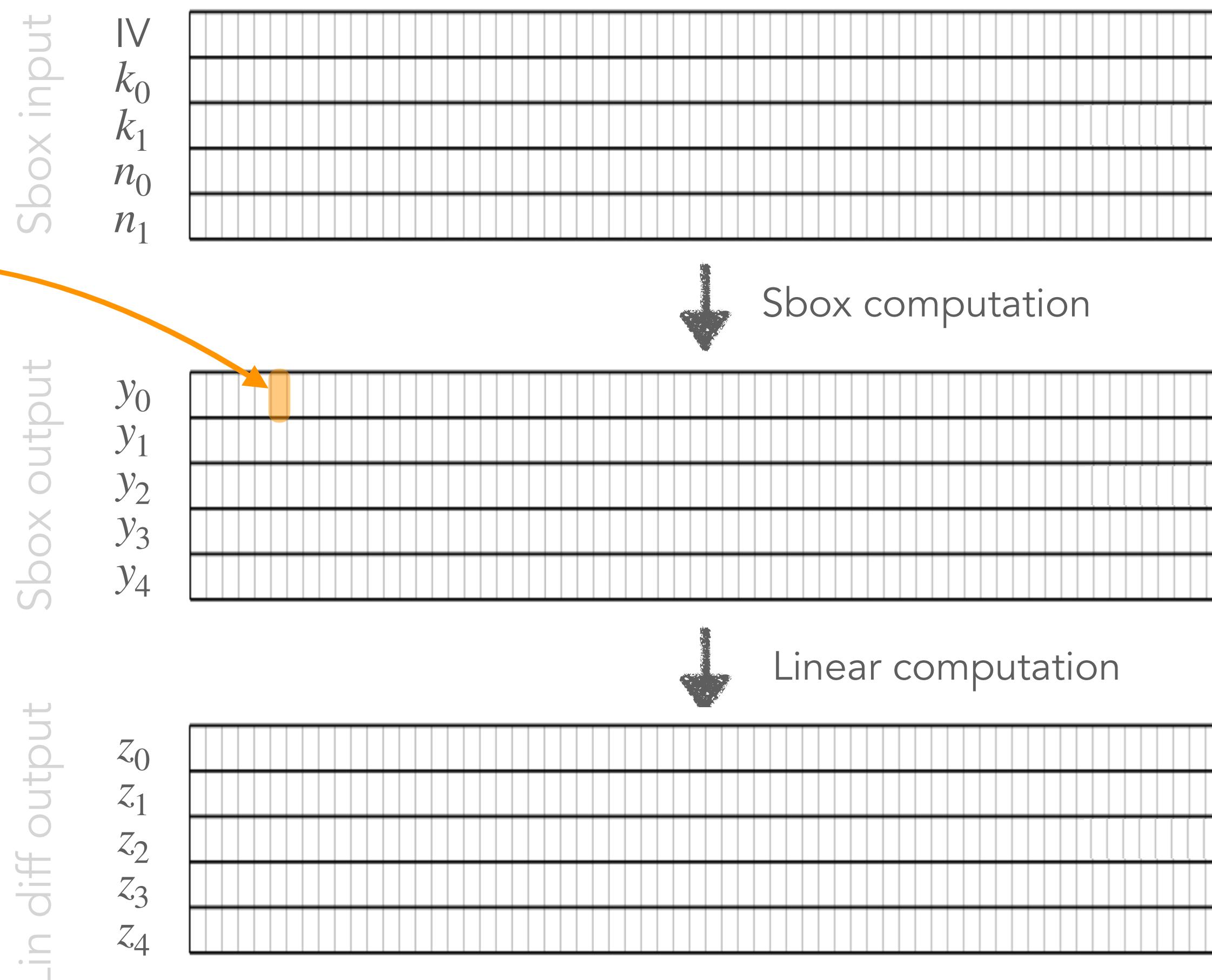


Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j

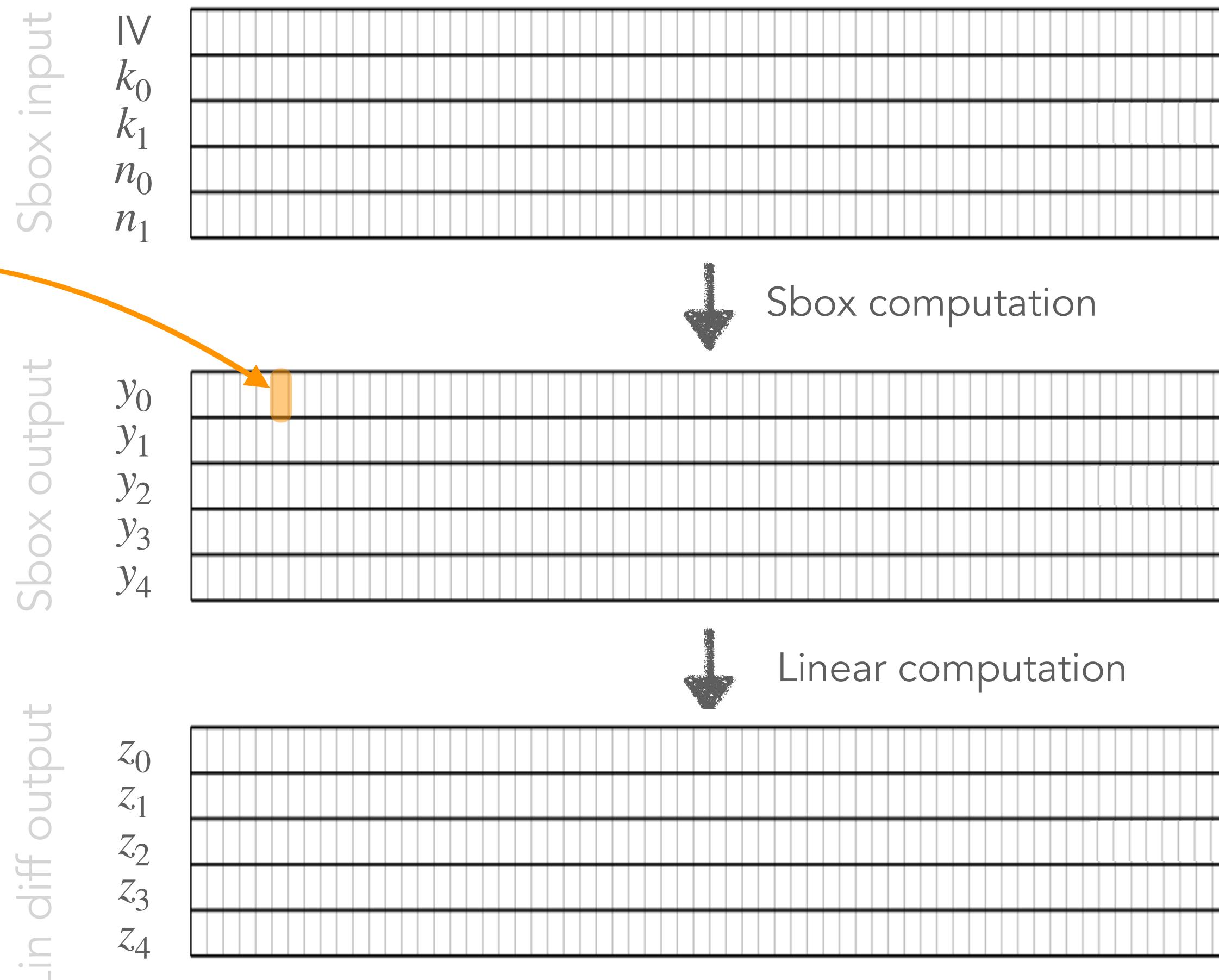


Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j
→ Failed (with 40K traces)

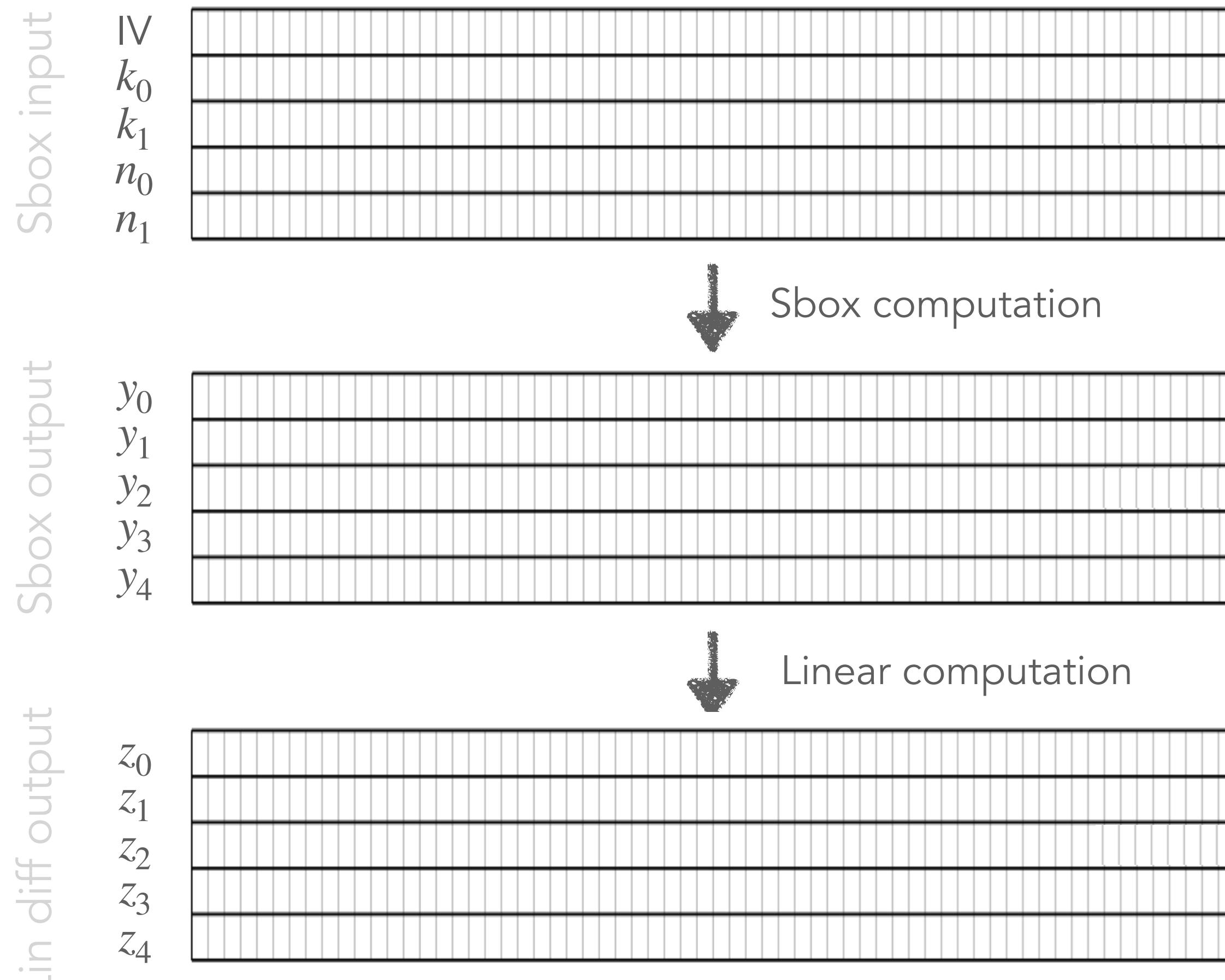


Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j
→ Failed (with 40K traces)

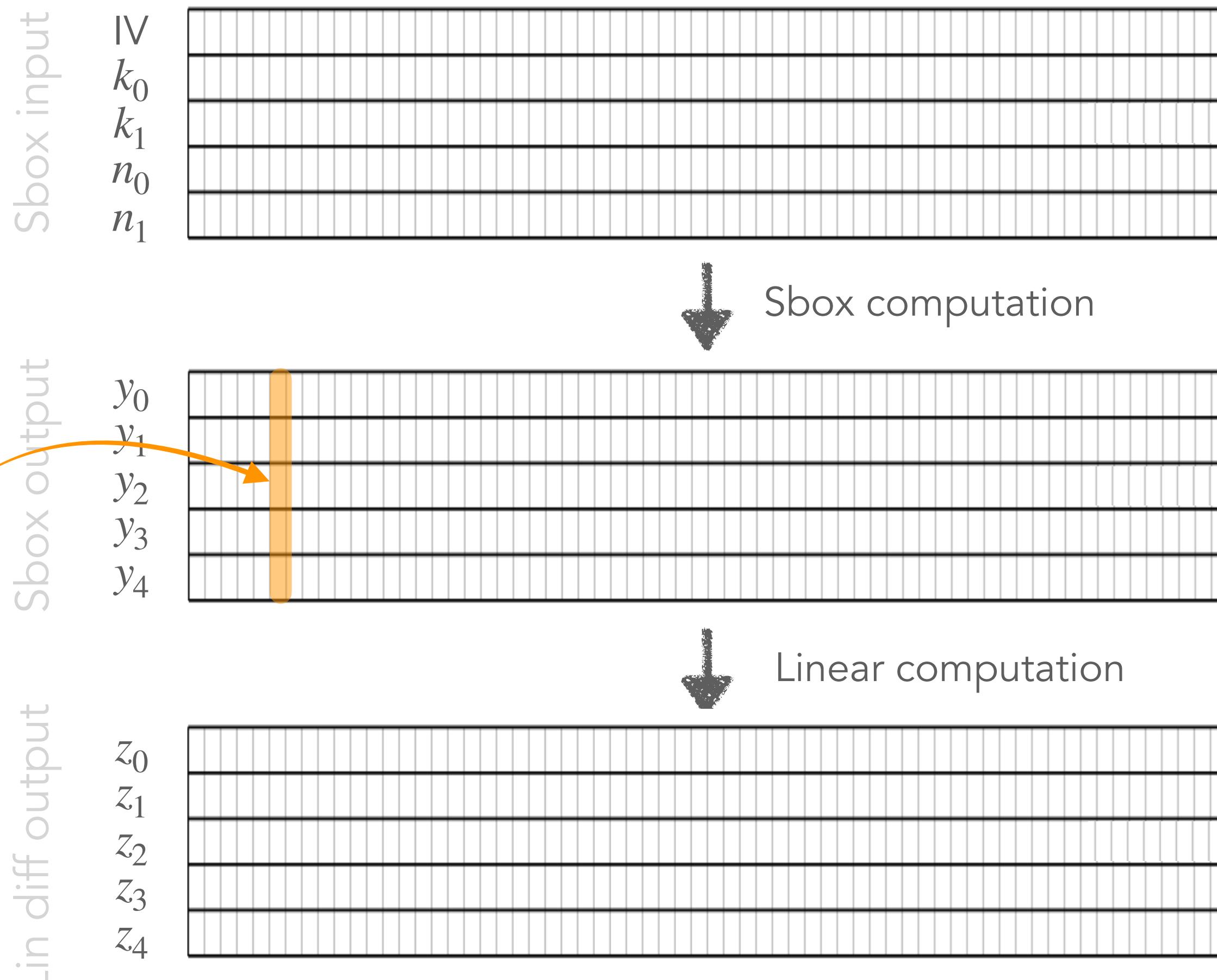


Previous CPA (1)

Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j
→ Failed (with 40K traces)
- Intermediate variable: $(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$
HW of Sbox output

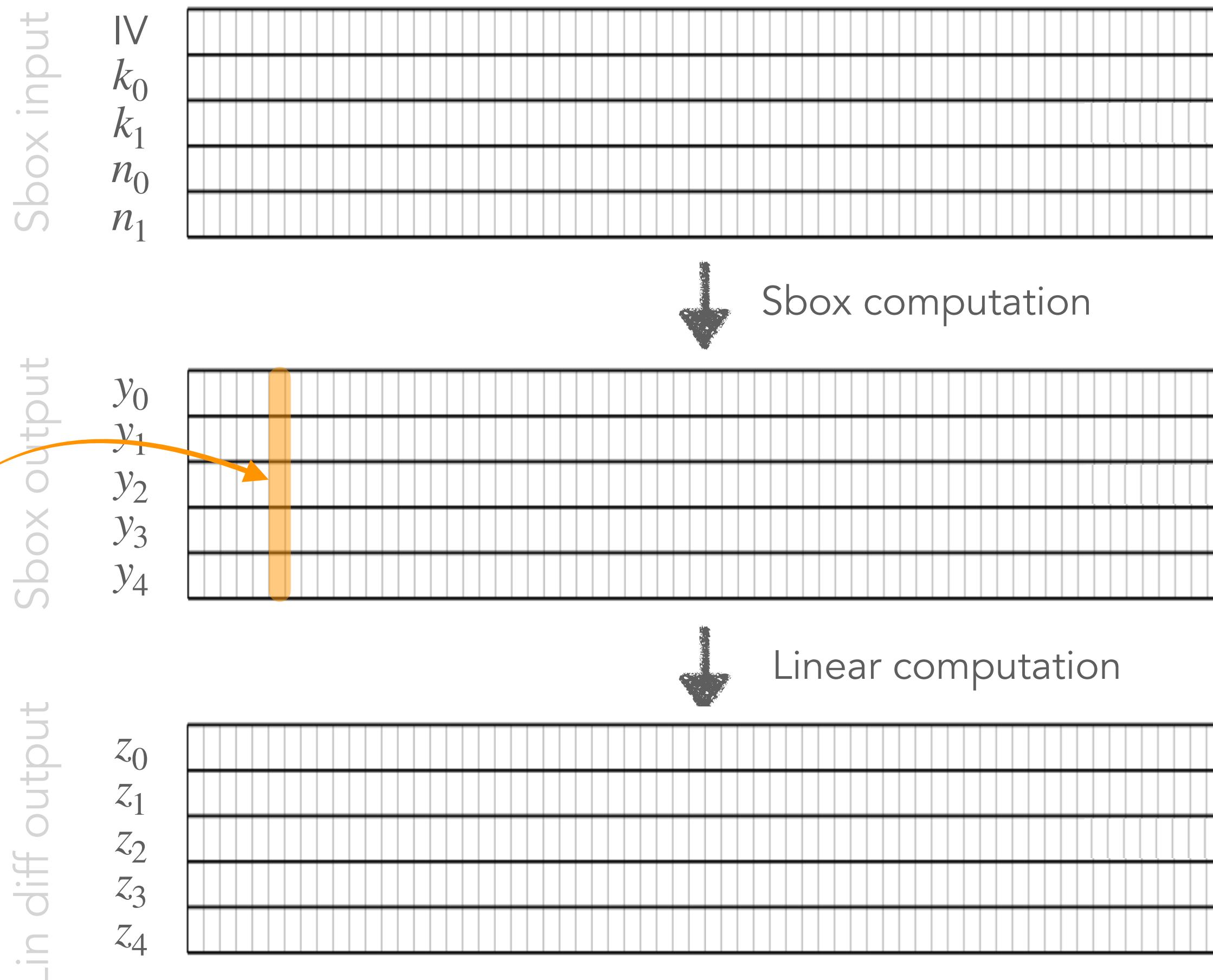


Previous CPA (1)

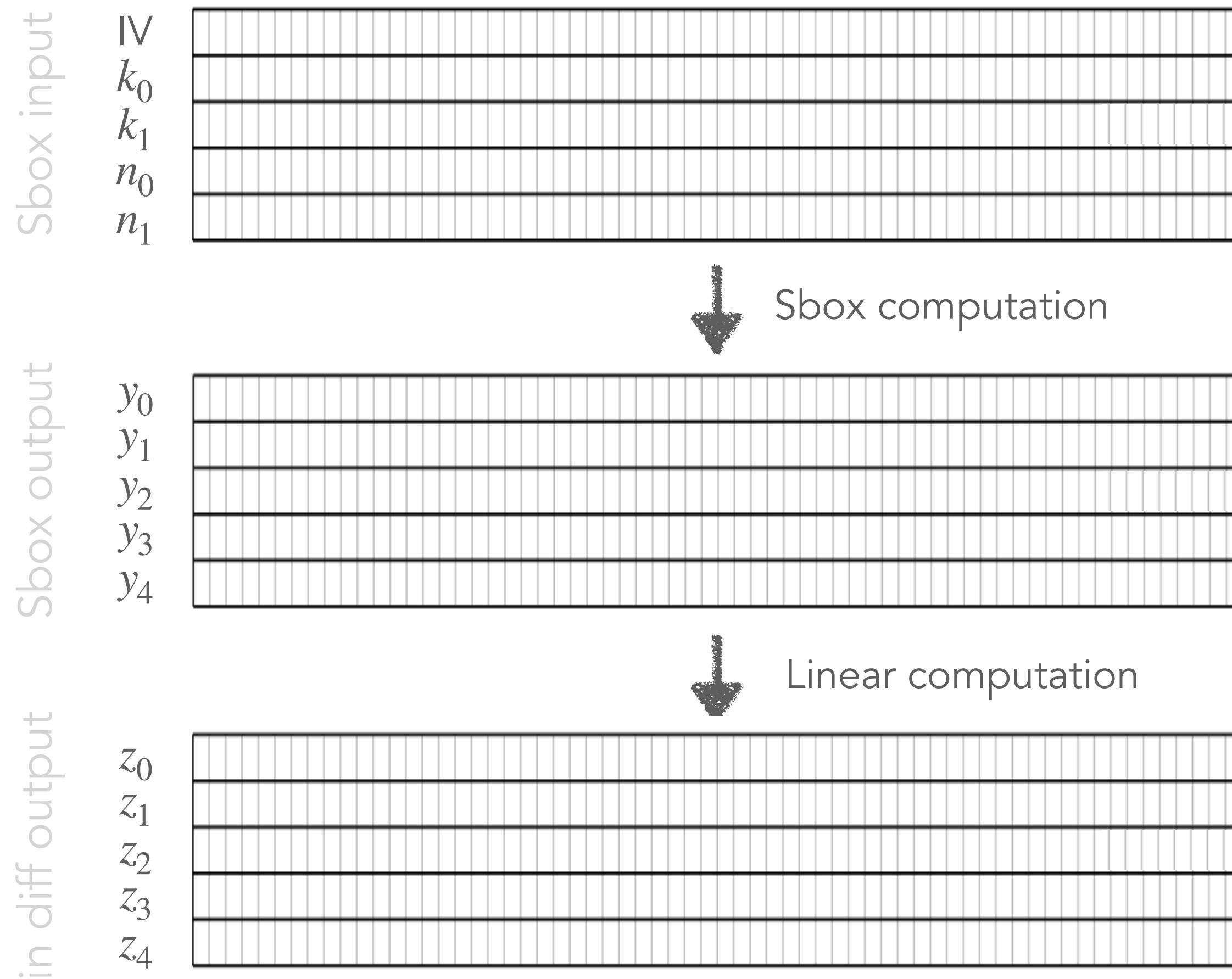
Ramezanpour et al., 2020

Choose Sbox output as attack point

- Intermediate variable: y_0^j
→ Failed (with 40K traces)
- Intermediate variable: $(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$
HW of Sbox output
→ Failed (with 40K traces)



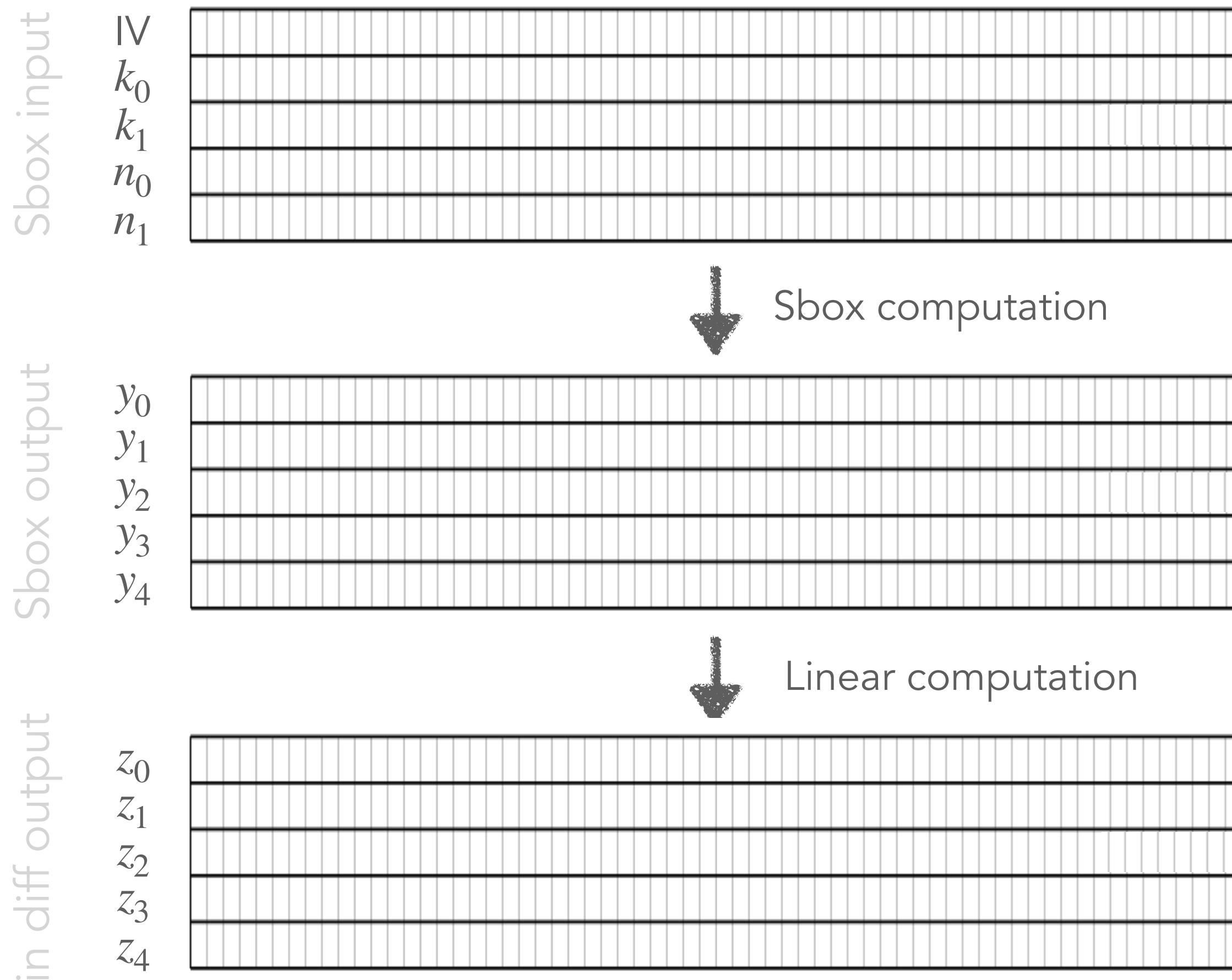
Previous CPA (2)



Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output
as attack point

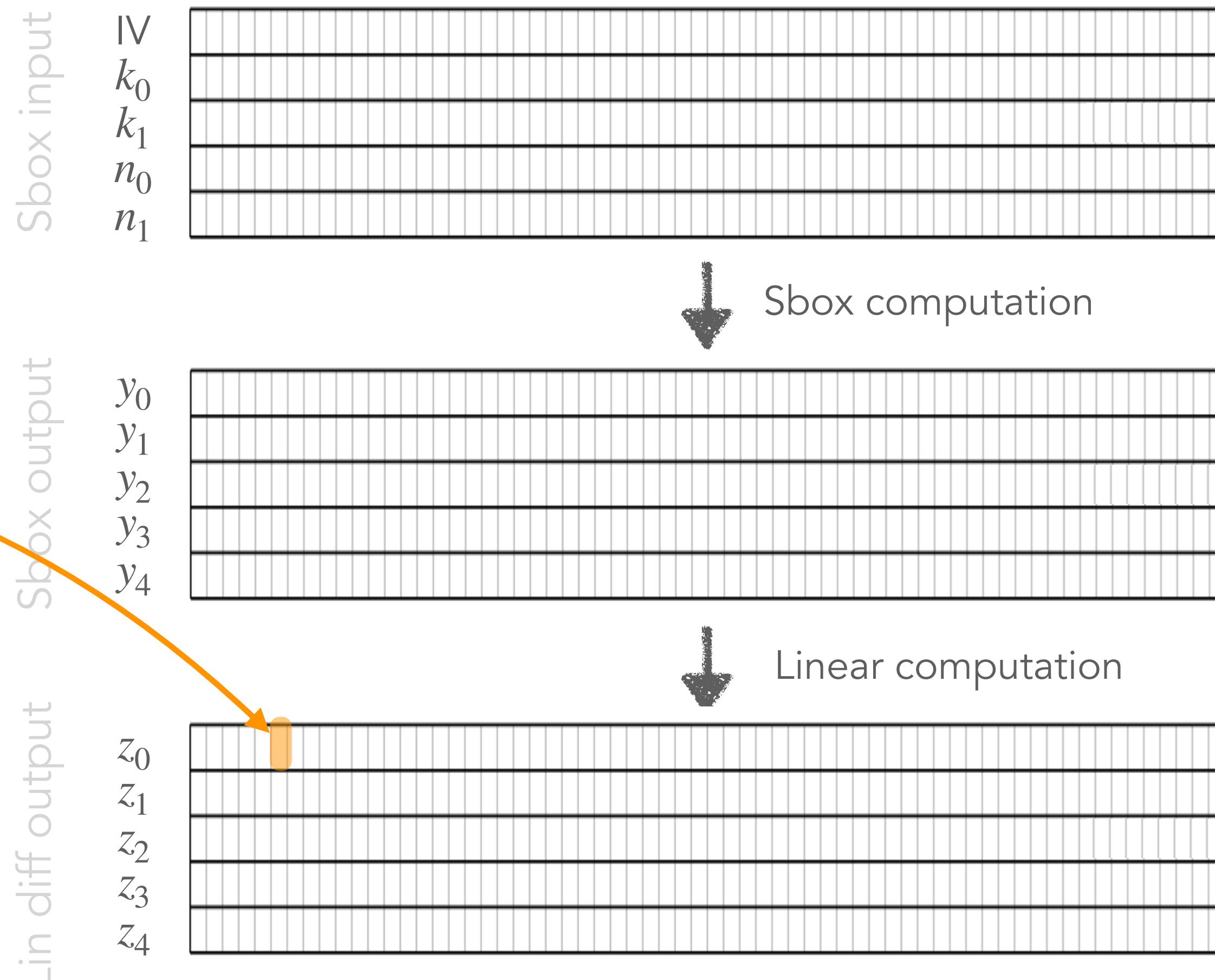


Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output
as attack point

Intermediate variable: z_0^j



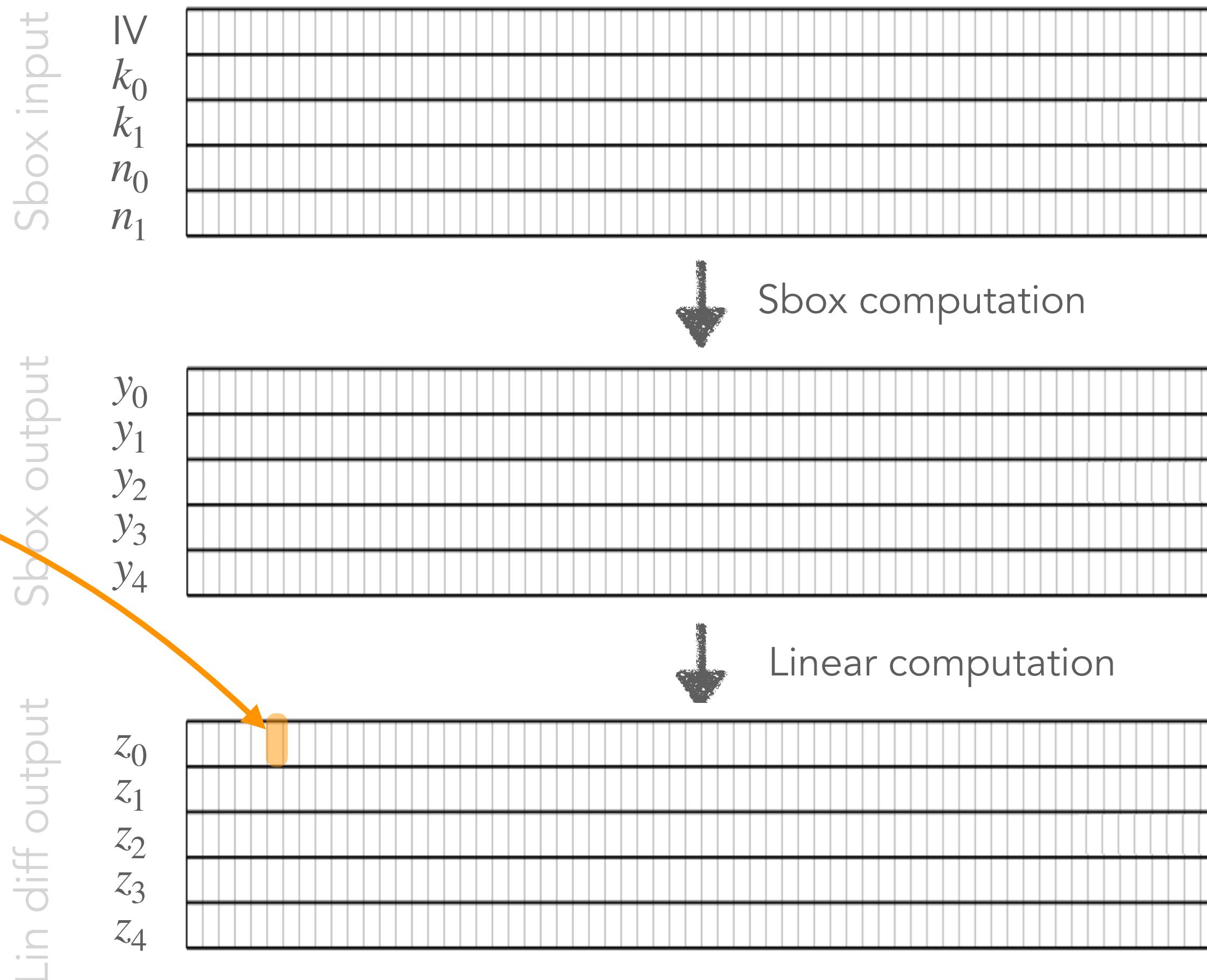
Previous CPA (2)

Samwel and Daemen, 2018

Choose linear diffusion output
as attack point

Intermediate variable: z_0^j

Fine-tune computation: $z_0^j \rightarrow \tilde{z}_0^j$



Previous CPA (2)

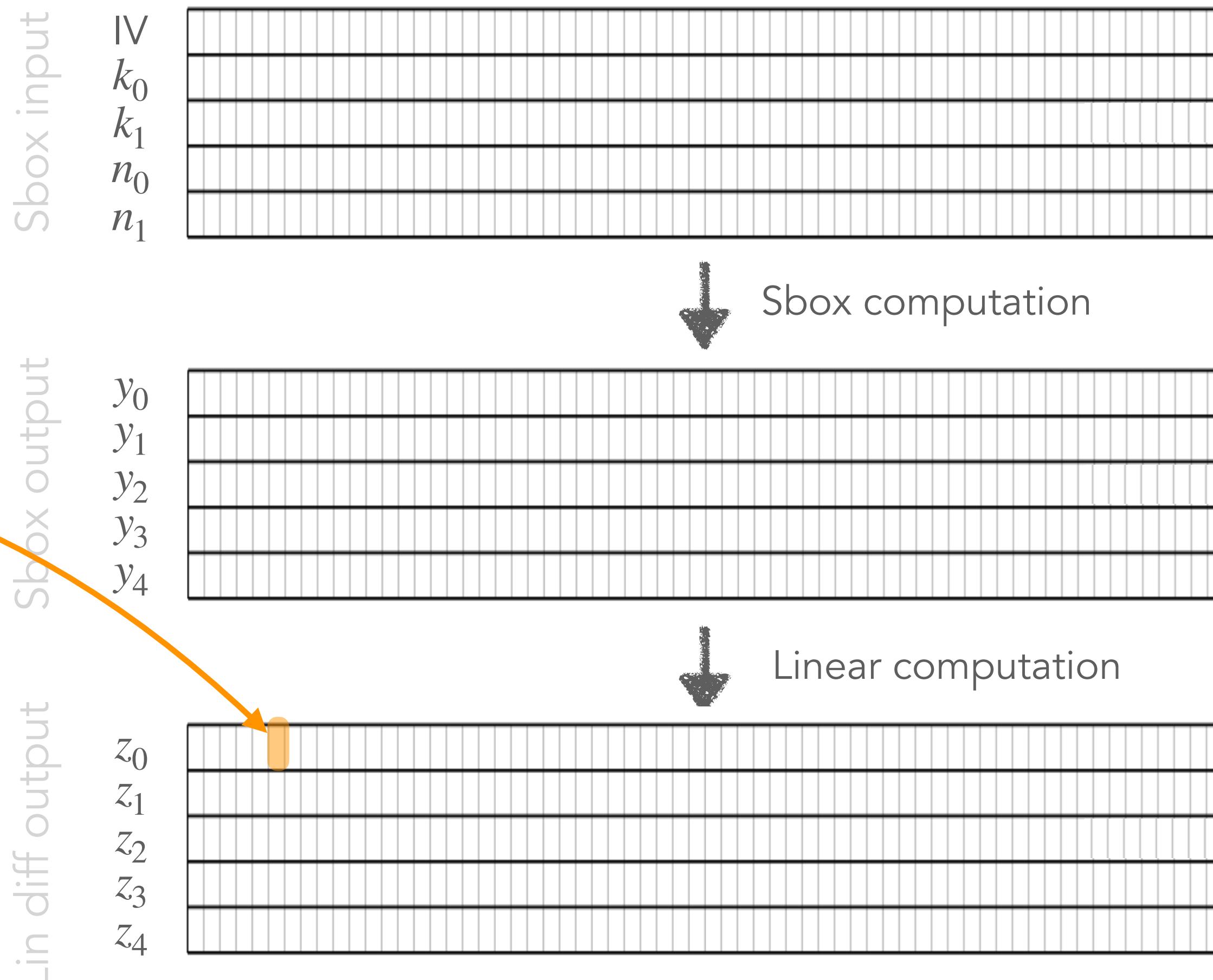
Samwel and Daemen, 2018

Choose linear diffusion output
as attack point

Intermediate variable: z_0^j

Fine-tune computation: $z_0^j \rightarrow \tilde{z}_0^j$

→ Succeeded

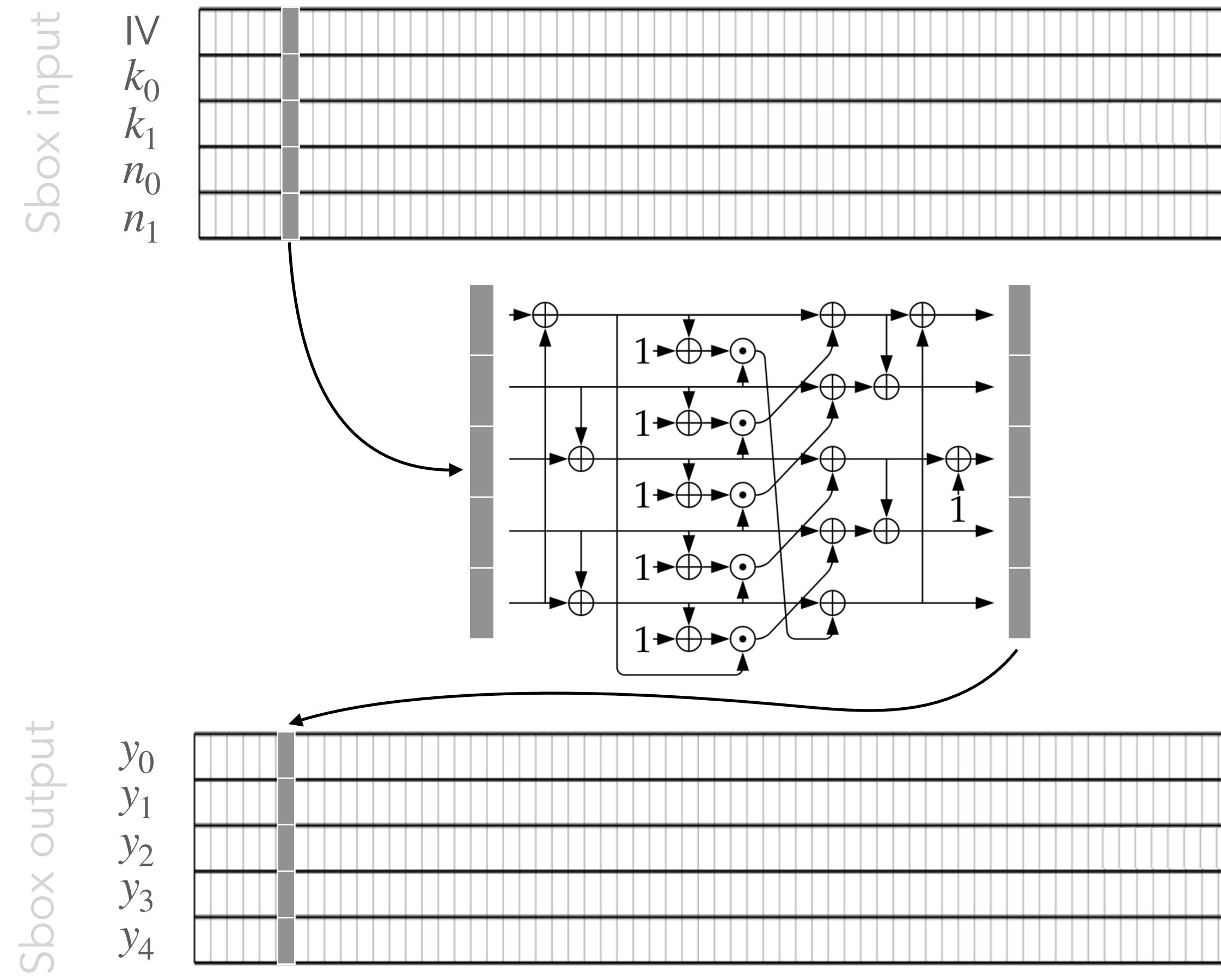




What are the reasons of this difference ?

(1) Sbox output as attack point

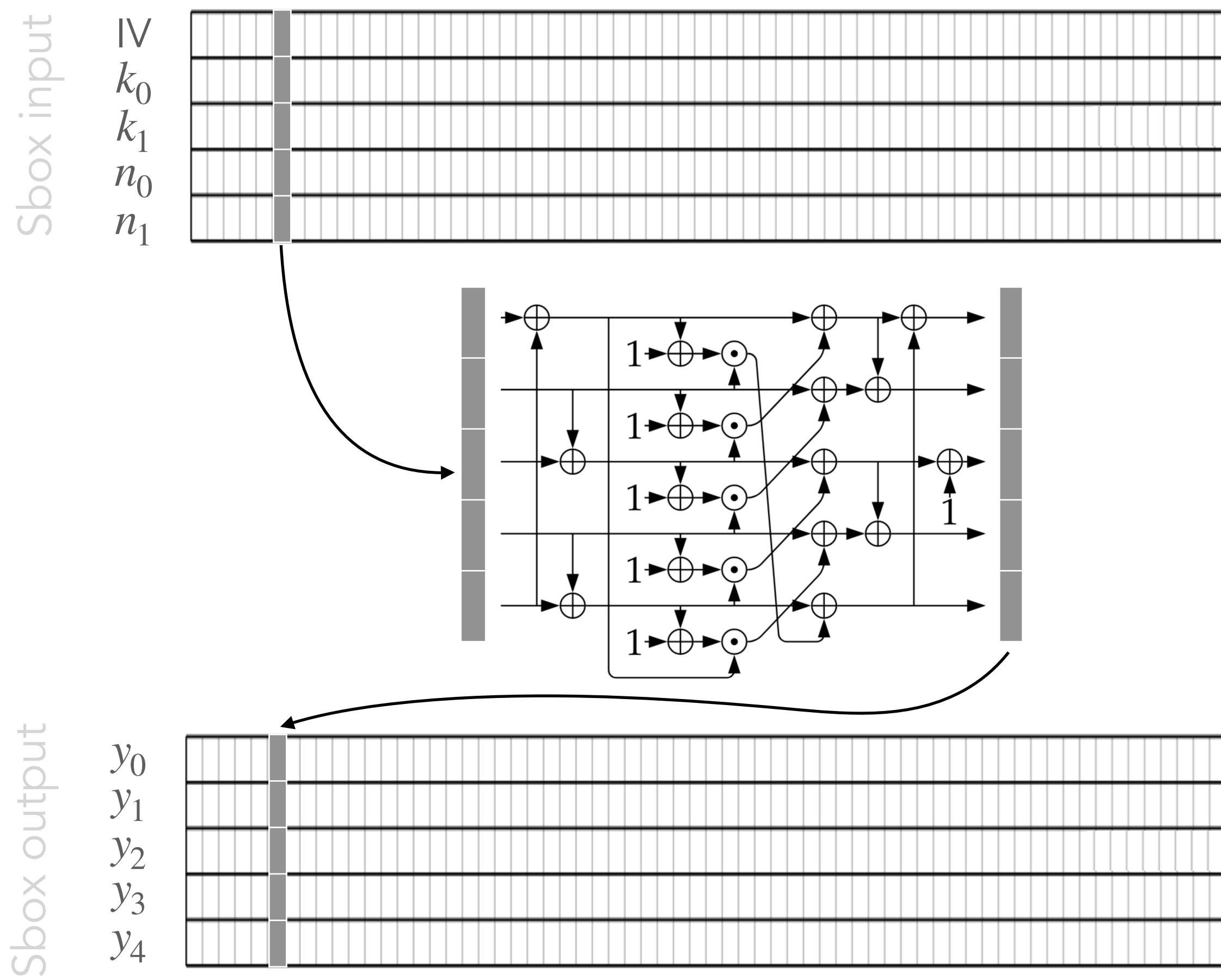
(1) Sbox output as attack point



(1) Sbox output as attack point

In an Sbox computation y_0^j :

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)

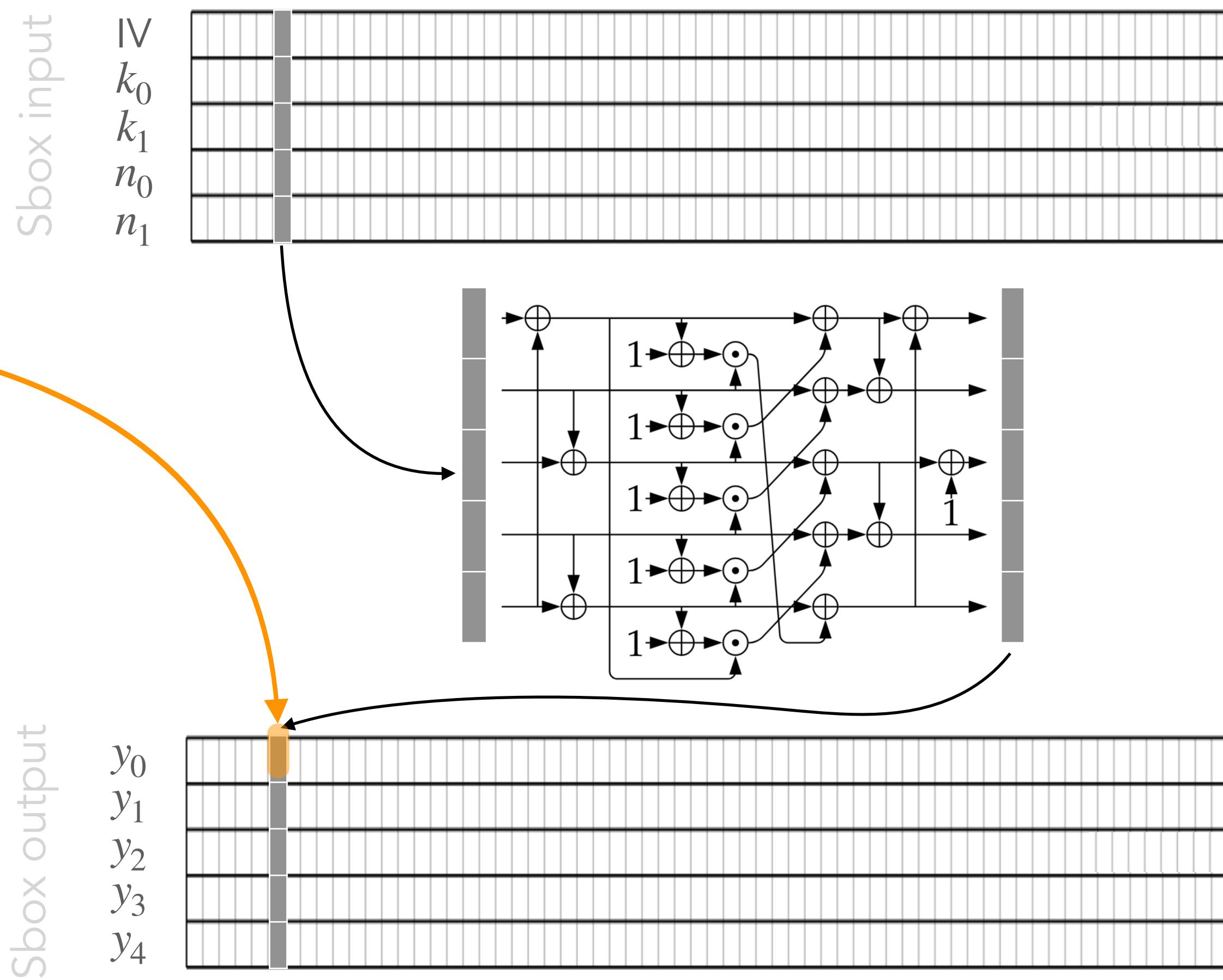


(1) Sbox output as attack point

In an Sbox computation y_0^j :

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$



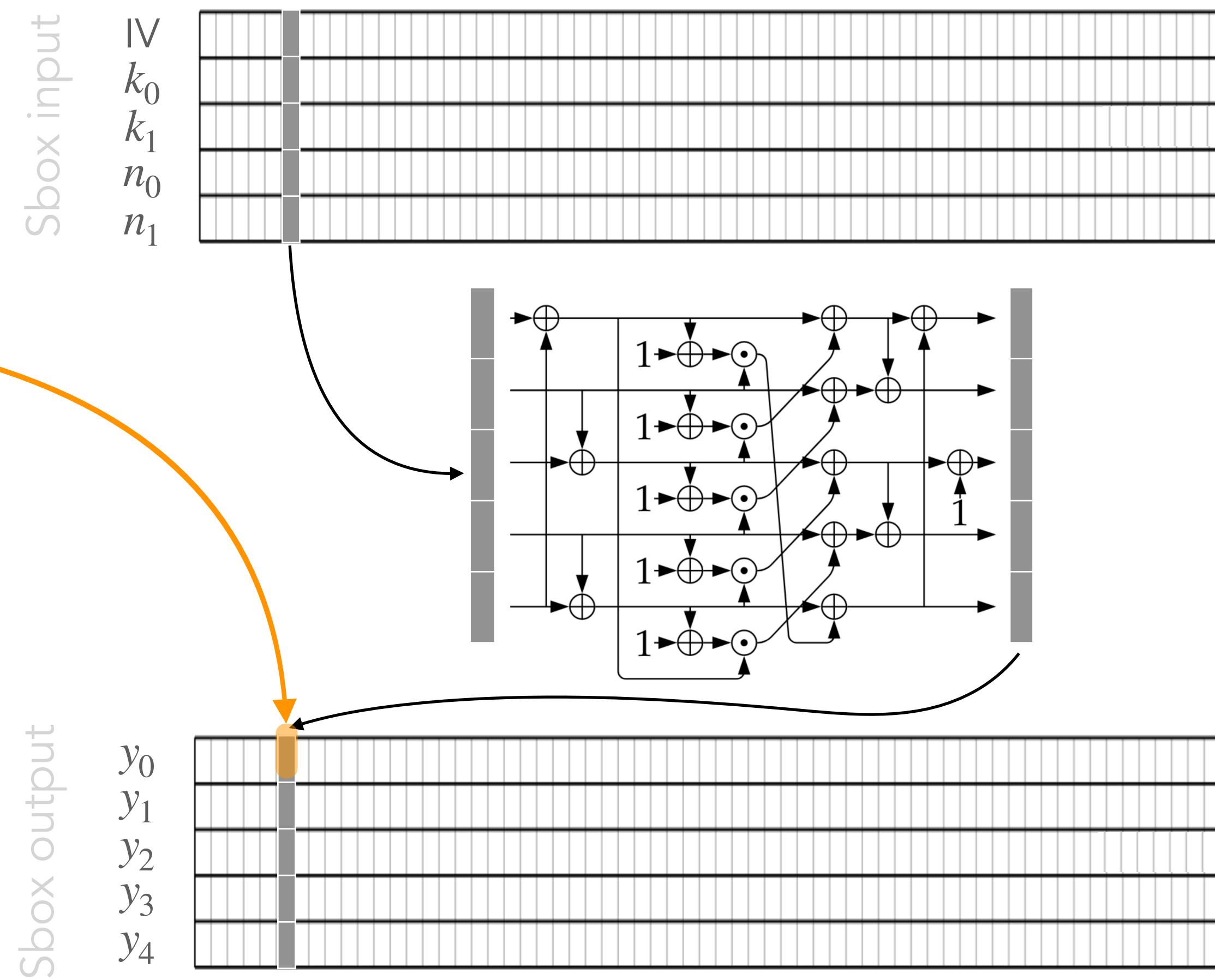
(1) Sbox output as attack point

In an Sbox computation y_0^j :

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

		(k_0^j, k_1^j)			
		$(0,0)$	$(0,1)$	$(1,0)$	$(1,1)$
(n_0^j, n_1^j)	$(0,0)$	0	1	1	1
$(0,1)$	0	1	0	0	
$(1,0)$	1	0	0	0	
$(1,1)$	1	0	1	1	
Correlation	-1		1		



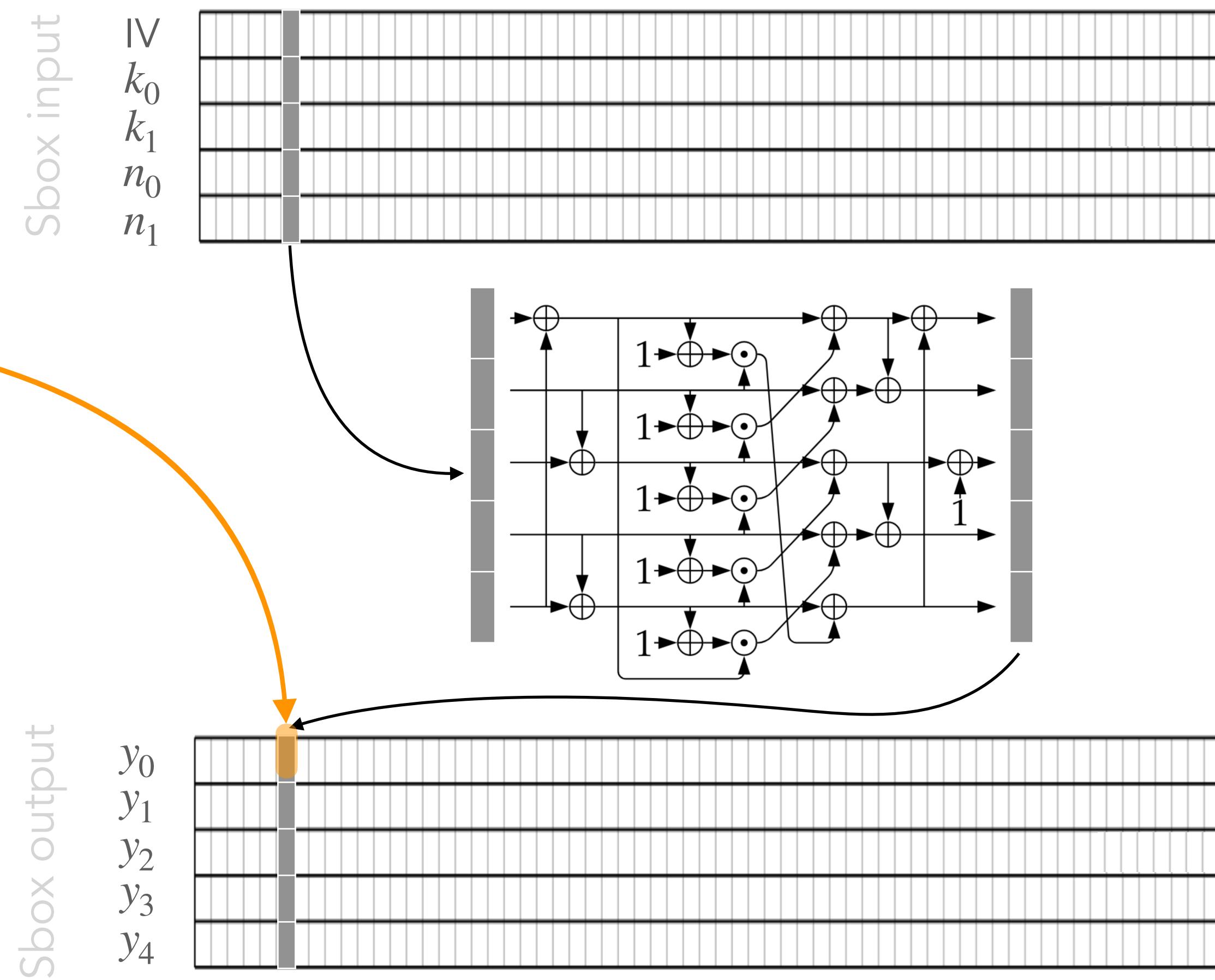
(1) Sbox output as attack point

In an Sbox computation y_0^j :

- 2 bits of key : (k_0^j, k_1^j)
- 2 bits of nonce : (n_0^j, n_1^j)

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

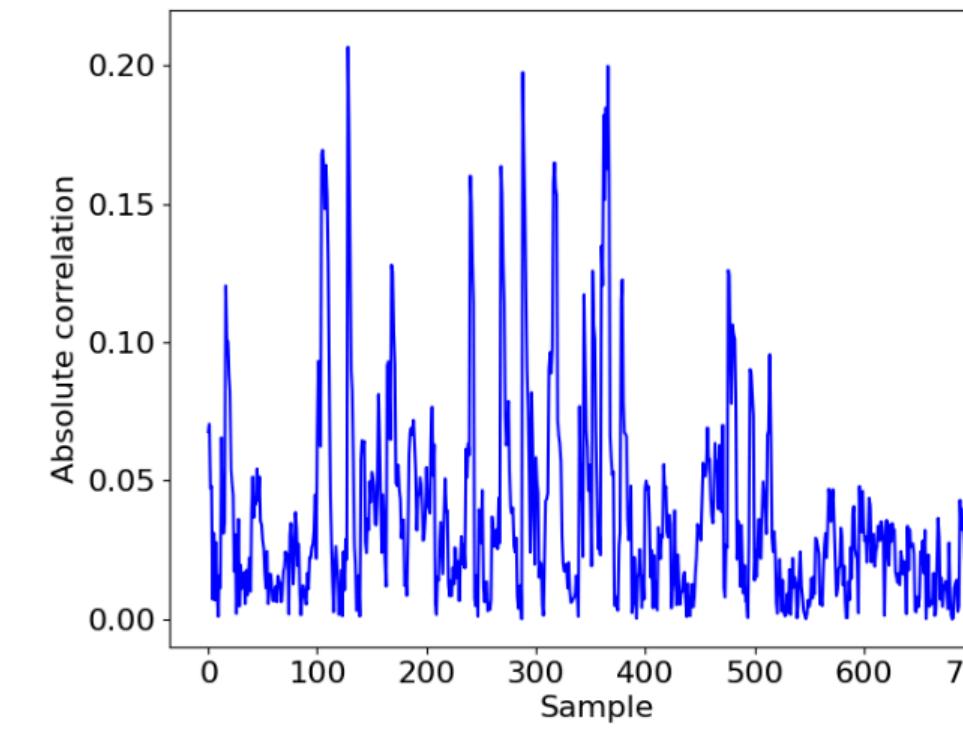
		(k_0^j, k_1^j)			
		$(0,0)$	$(0,1)$	$(1,0)$	$(1,1)$
(n_0^j, n_1^j)	$(0,0)$	0	1	1	1
$(0,1)$	0	1	0	0	
$(1,0)$	1	0	0	0	
$(1,1)$	1	0	1	1	
Correlation	-1		1		



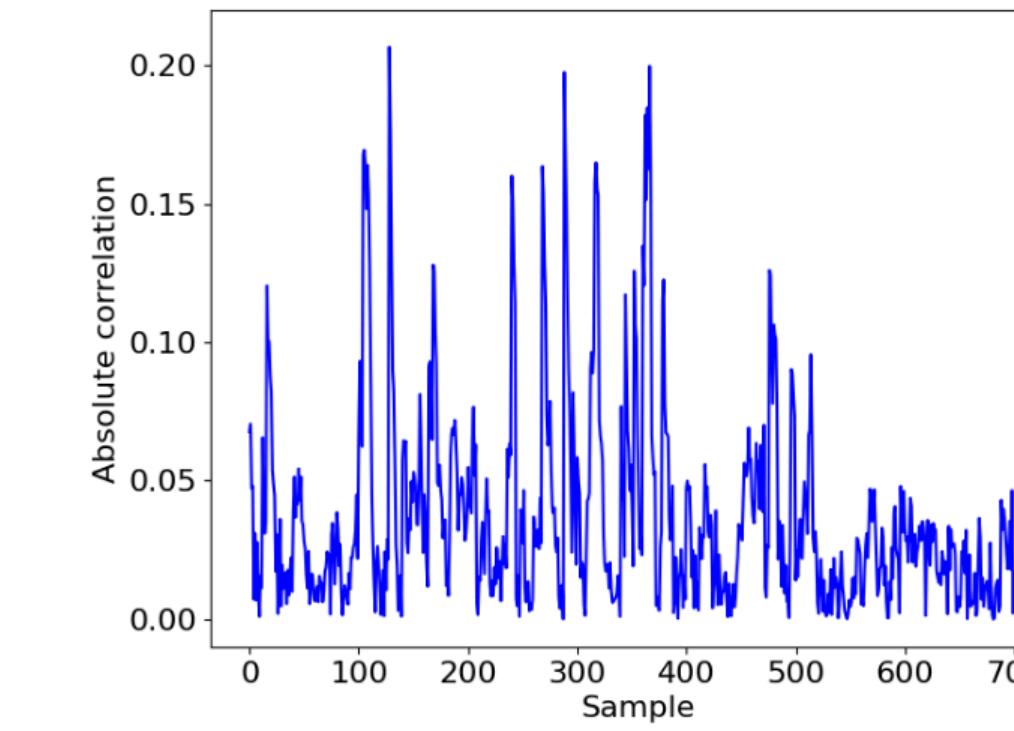
→ We cannot obtain unique (correct) key candidate

(1) Sbox output as attack point

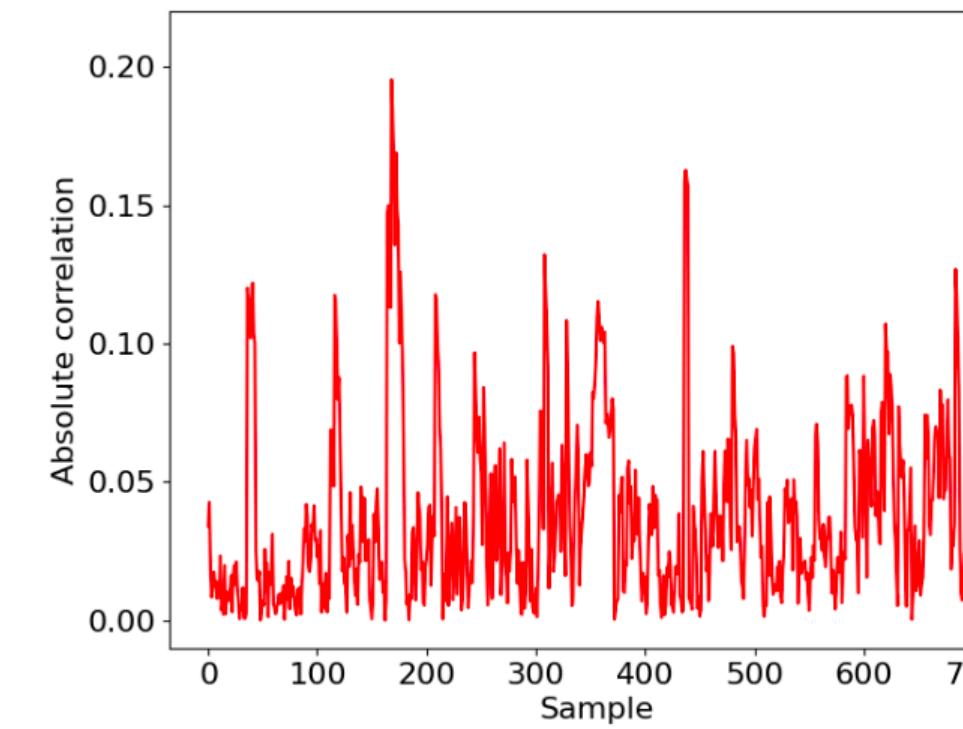
Correlation traces for all key candidates



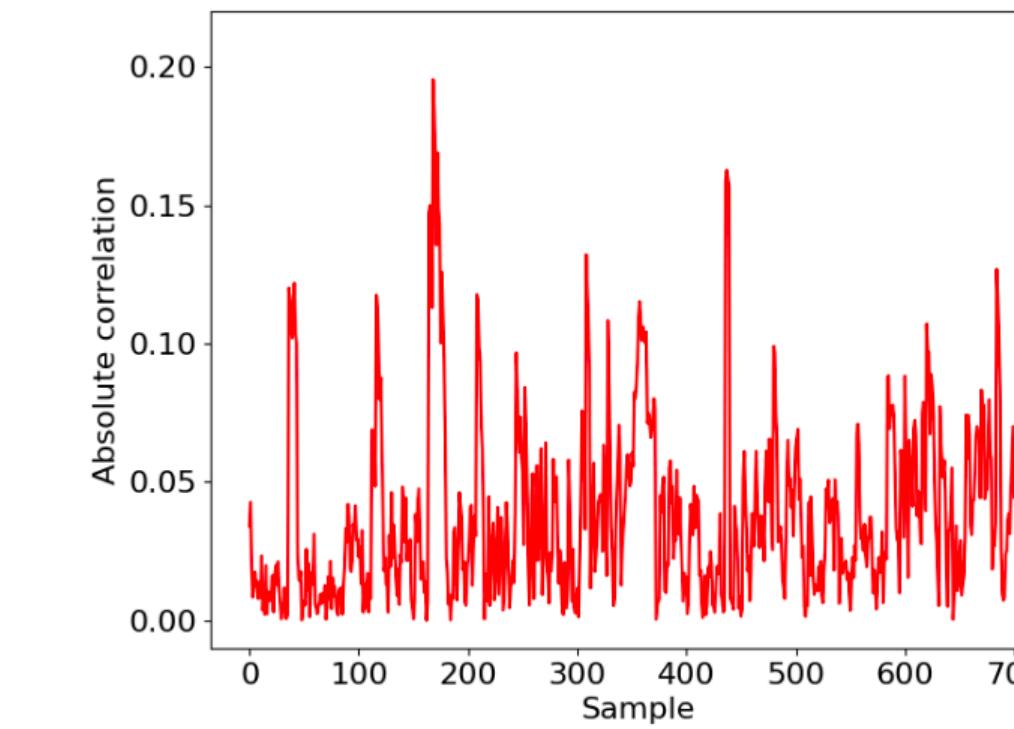
(a) $(k_0^j, k_1^j) = (0, 0)$



(b) $(k_0^j, k_1^j) = (0, 1)$



(c) $(k_0^j, k_1^j) = (1, 0)$



(d) $(k_0^j, k_1^j) = (1, 1)$

(1) Sbox output as attack point

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	-	-
(0,1)	1	1	-	-
(1,0)	-	-	1	1
(1,1)	-	-	1	1

y_0^j

(1) Sbox output as attack point

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	-	-
(0,1)	1	1	-	-
(1,0)	-	-	1	1
(1,1)	-	-	1	1

y_0^j

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	1	1	1
(0,1)	1	1	1	1
(1,0)	1	1	1	1
(1,1)	1	1	1	1

y_2^j and y_3^j

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1	-	-	1
(0,1)	-	1	1	-
(1,0)	-	1	1	-
(1,1)	1	-	-	1

y_1^j

k_0^j	0	1
0	1	0
1	0	1

y_4^j

(1) Sbox output as attack point

Hamming weight of Sbox output: $\text{HW}(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

(1) Sbox output as attack point

Hamming weight of Sbox output: $\text{HW}(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1.00	0.15	0.89	0.87
(0,1)	0.15	1.00	0.48	0.09
(1,0)	0.89	0.48	1.00	0.90
(1,1)	0.87	0.09	0.90	1.00

(1) Sbox output as attack point

Hamming weight of Sbox output: $\text{HW}(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1.00	0.15	0.89	0.87
(0,1)	0.15	1.00	0.48	0.09
(1,0)	0.89	0.48	1.00	0.90
(1,1)	0.87	0.09	0.90	1.00

(1) Sbox output as attack point

Hamming weight of Sbox output: $\text{HW}(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1.00	0.15	0.89	0.87
(0,1)	0.15	1.00	0.48	0.09
(1,0)	0.89	0.48	1.00	0.90
(1,1)	0.87	0.09	0.90	1.00

→ Not effective for CPA attacks

(1) Sbox output as attack point

Hamming weight of Sbox output: $\text{HW}(y_0^j | y_1^j | y_2^j | y_3^j | y_4^j)$

Correlations of distributions associated to all key pairs

(k_0^j, k_1^j)	(0,0)	(0,1)	(1,0)	(1,1)
(0,0)	1.00	0.15	0.89	0.87
(0,1)	0.15	1.00	0.48	0.09
(1,0)	0.89	0.48	1.00	0.90
(1,1)	0.87	0.09	0.90	1.00

→ Not effective for CPA attacks

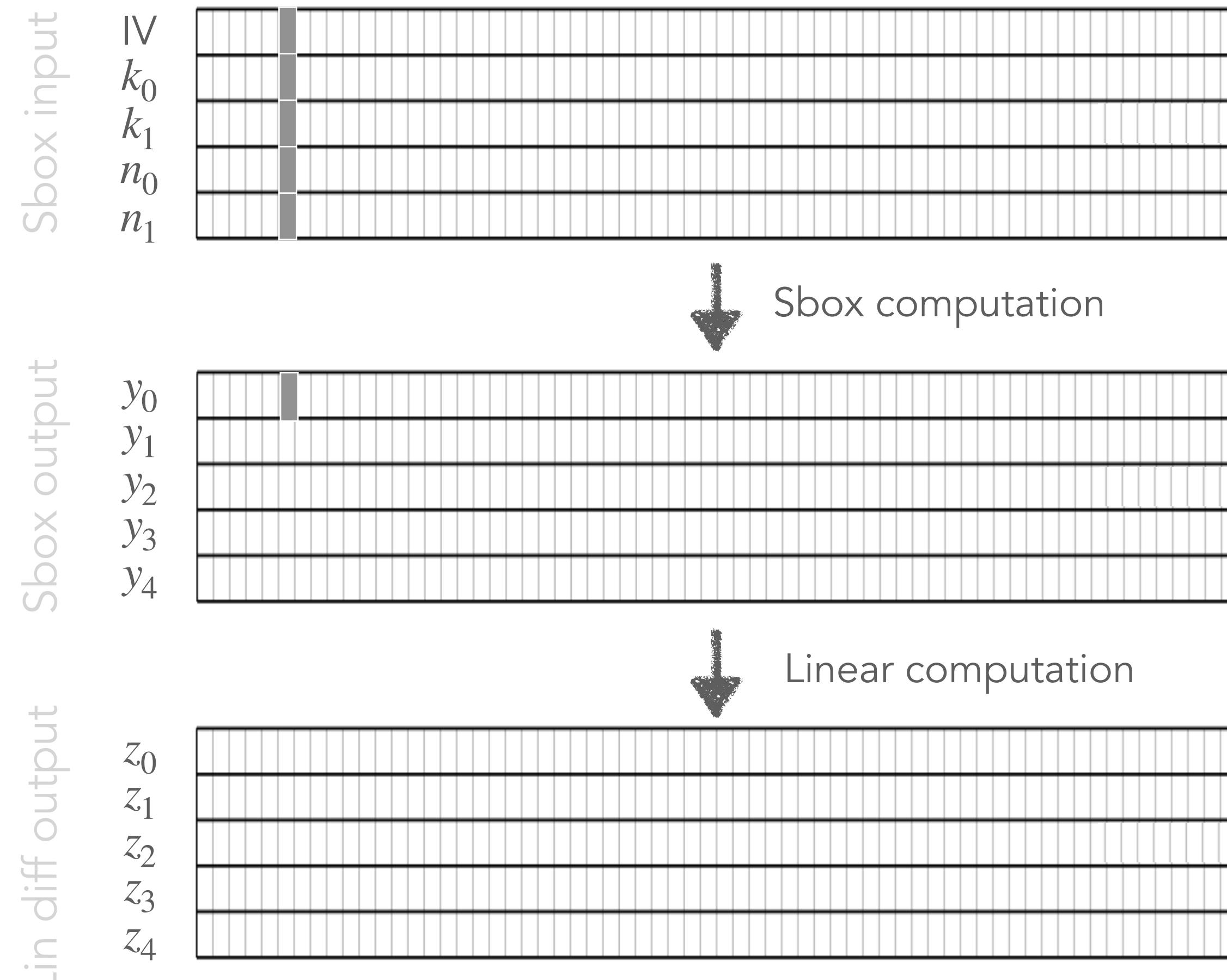
This may explain why

Ramezanpour et al., 2020

failed

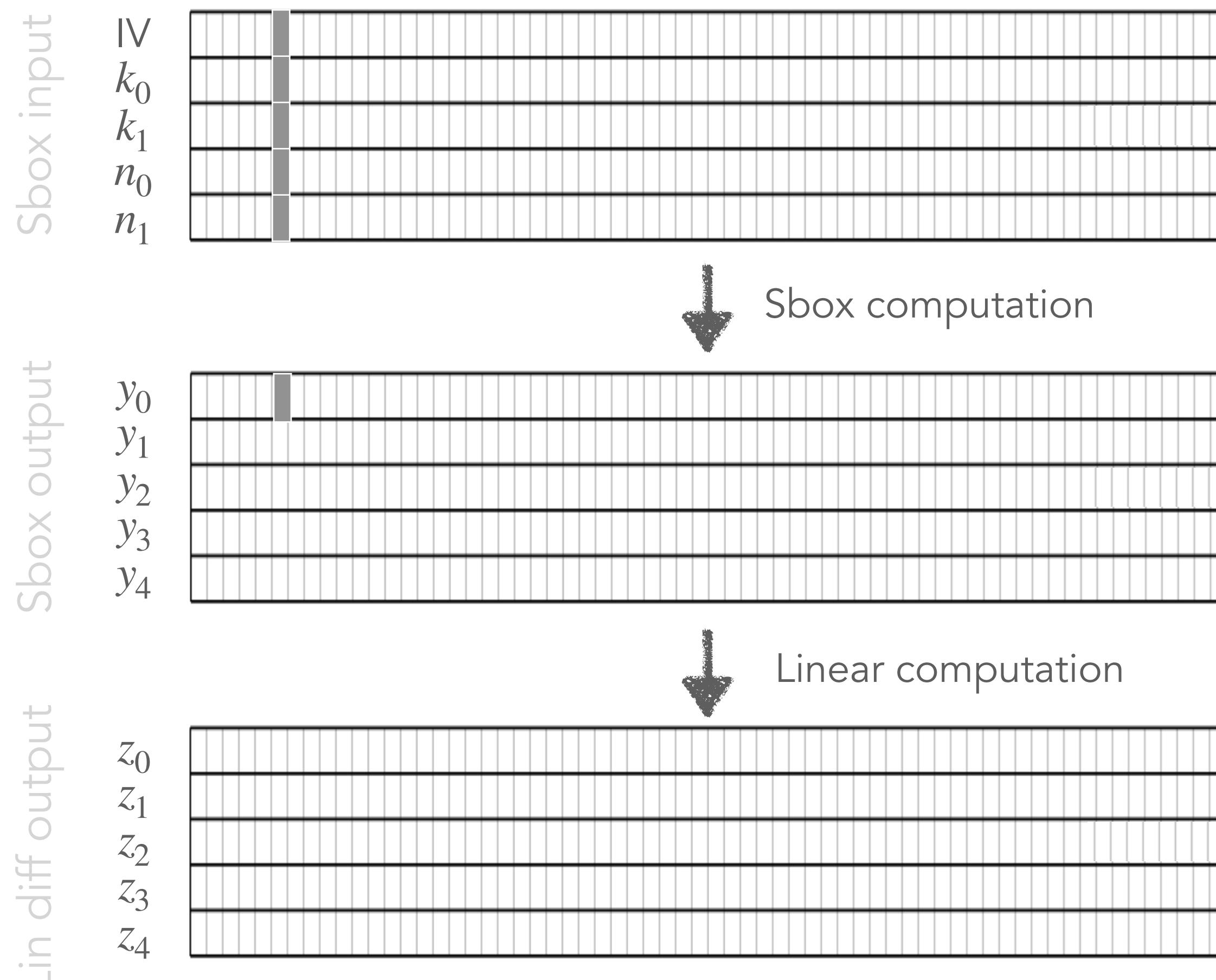
(2) Fine-tuned linear diffusion output as attack point

(2) Fine-tuned linear diffusion output as attack point



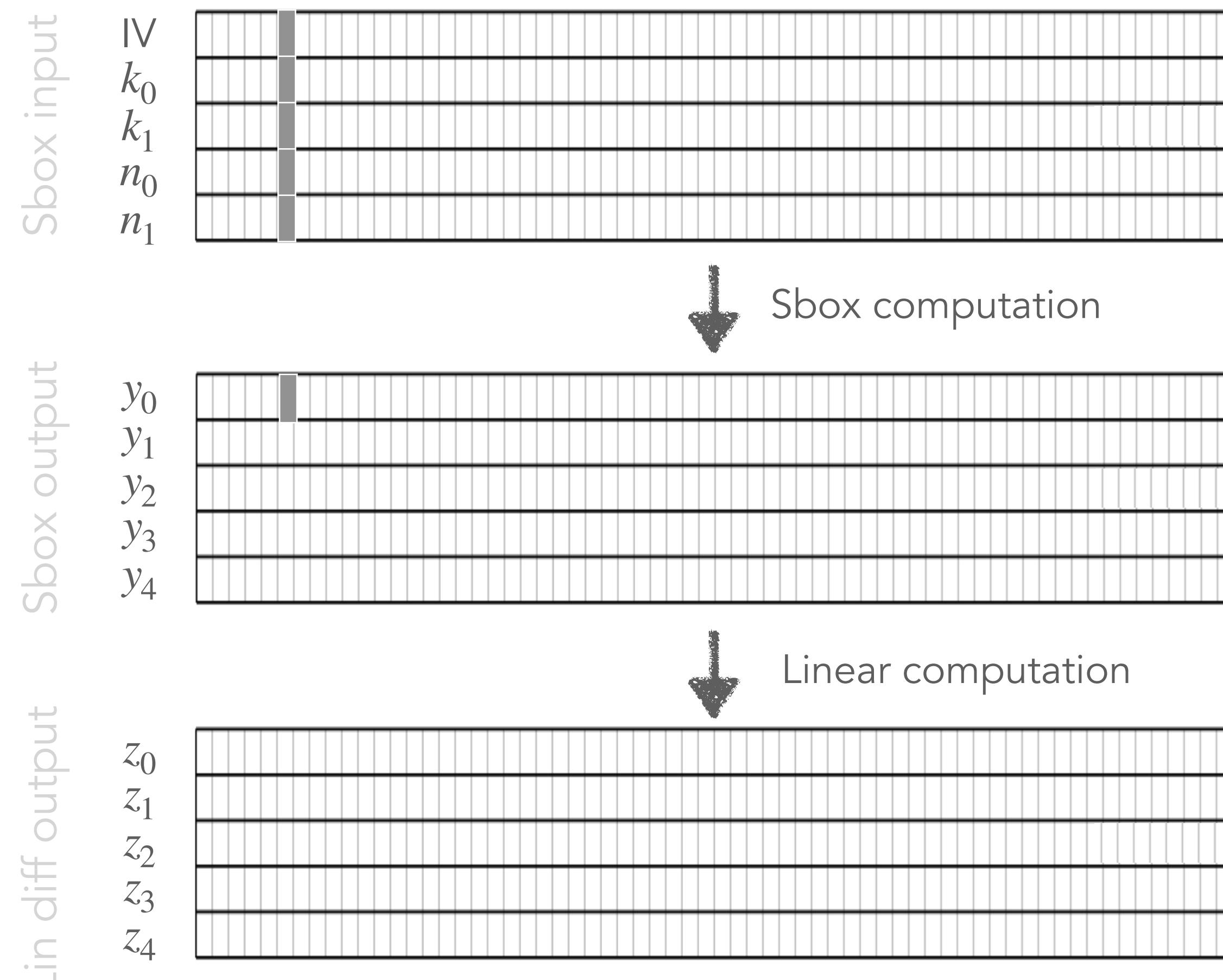
(2) Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$



(2) Fine-tuned linear diffusion output as attack point

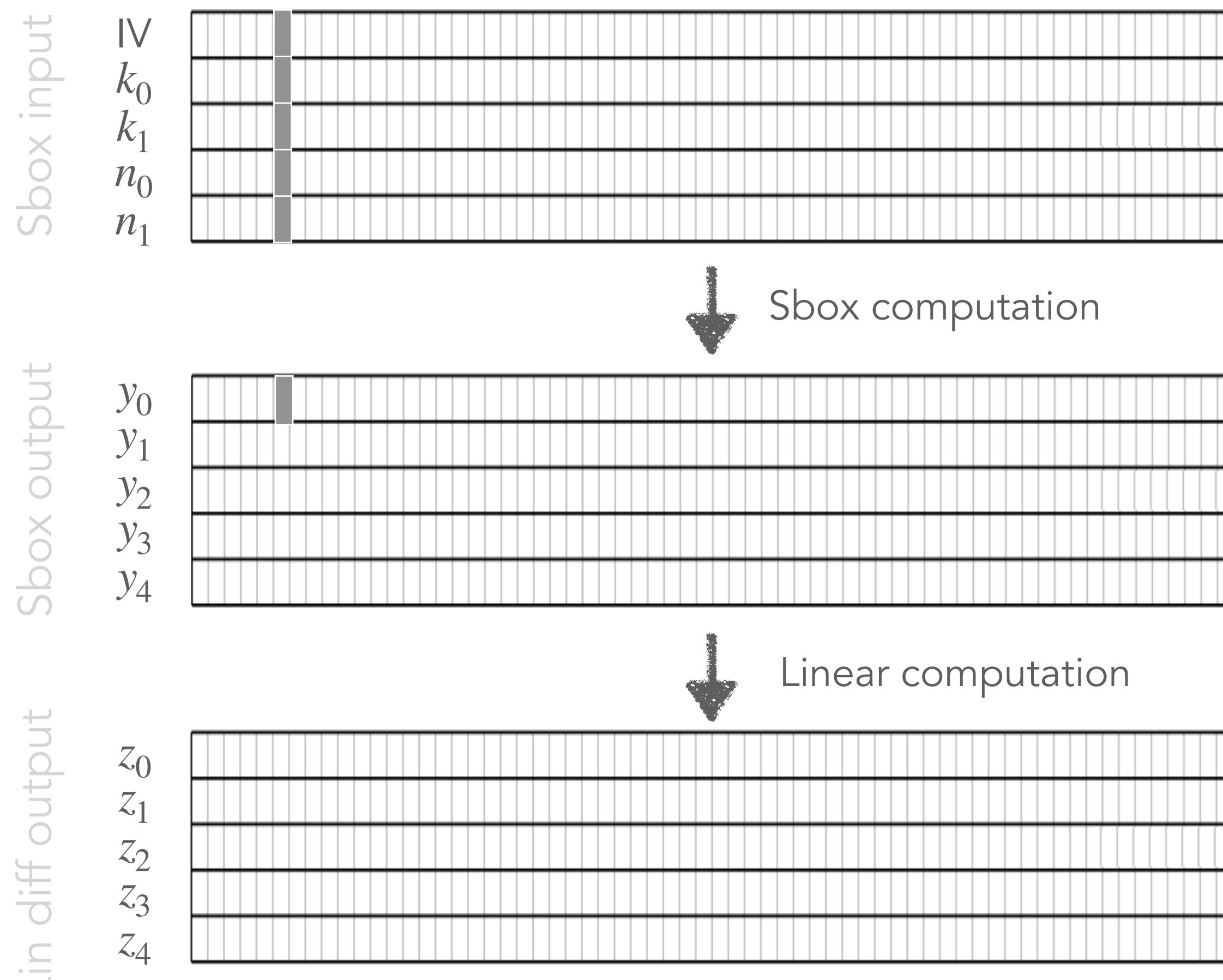
$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$



(2) Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

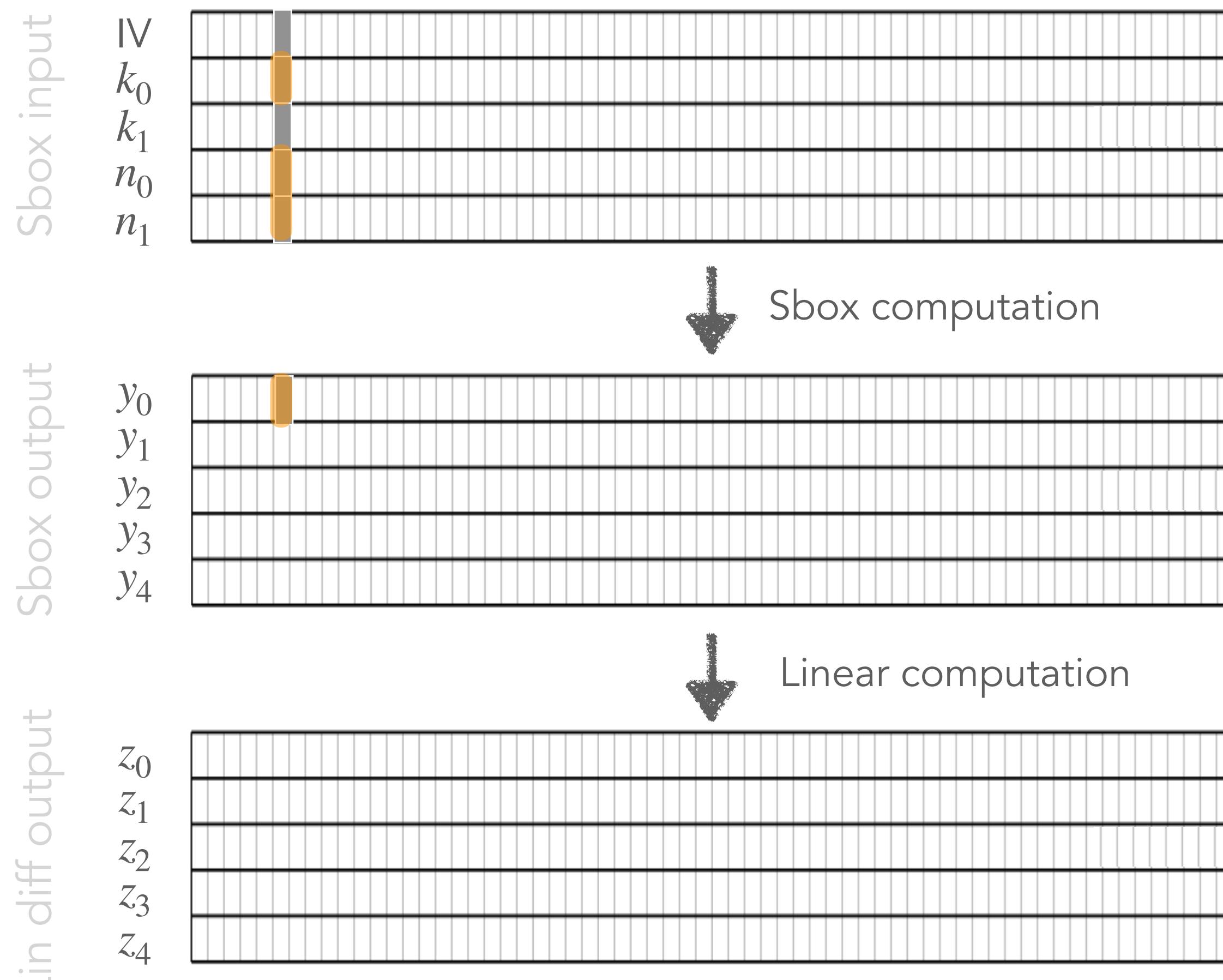
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$



(2) Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

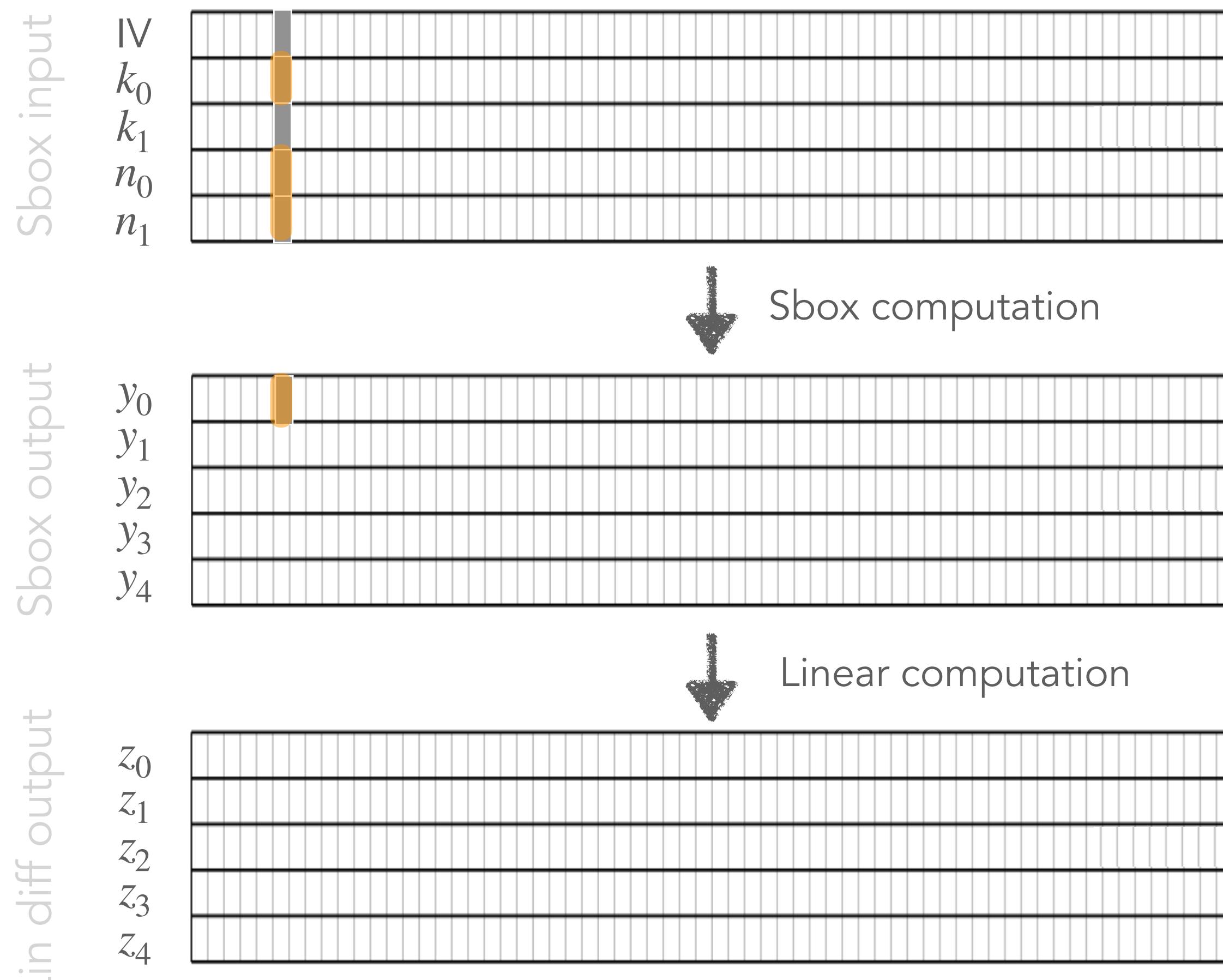


(2) Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

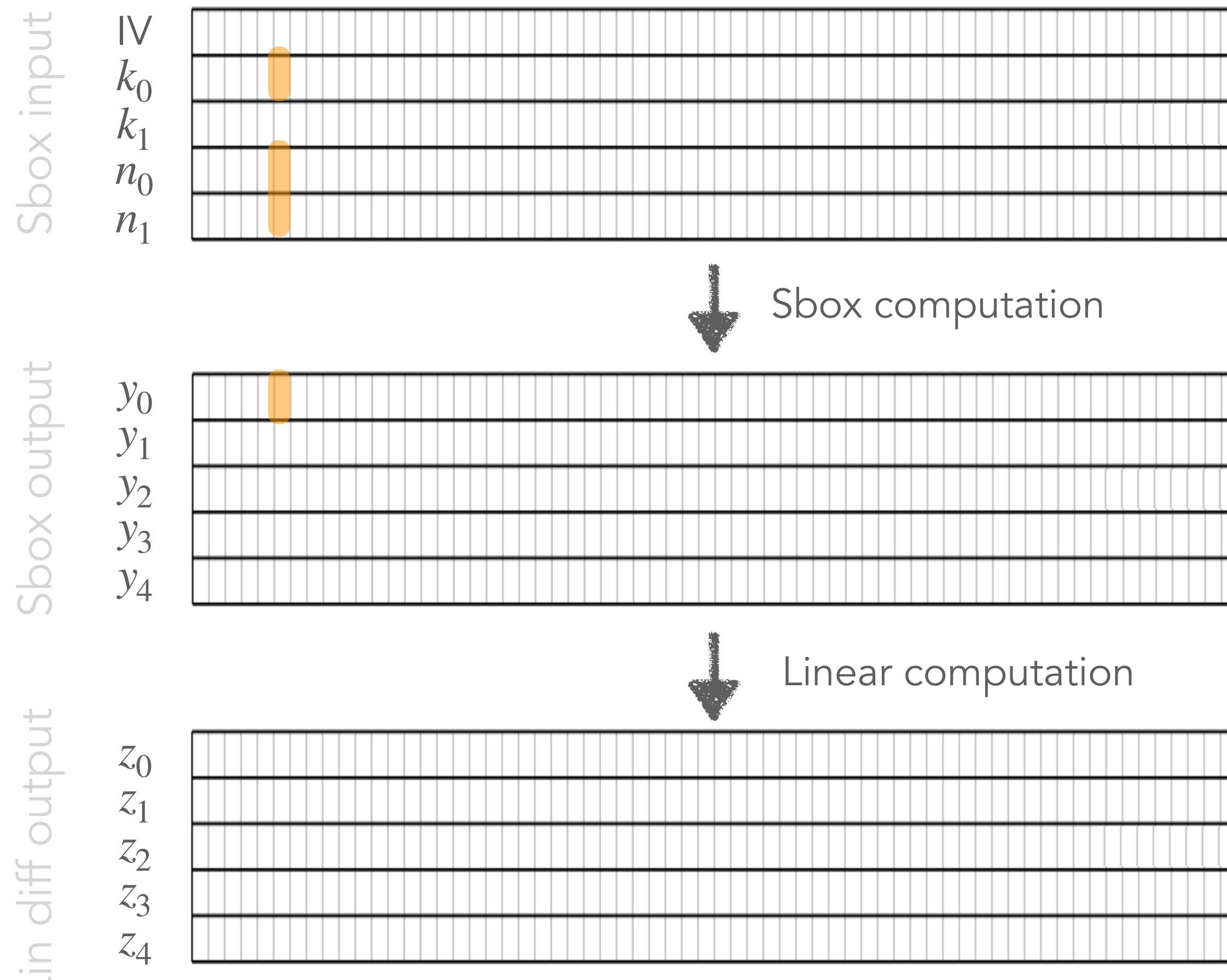
	k_0^j
(n_0^j, n_1^j)	0 1
(0,0)	0 1
(0,1)	0 0
(1,0)	1 0
(1,1)	1 1
Correlation	0



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$



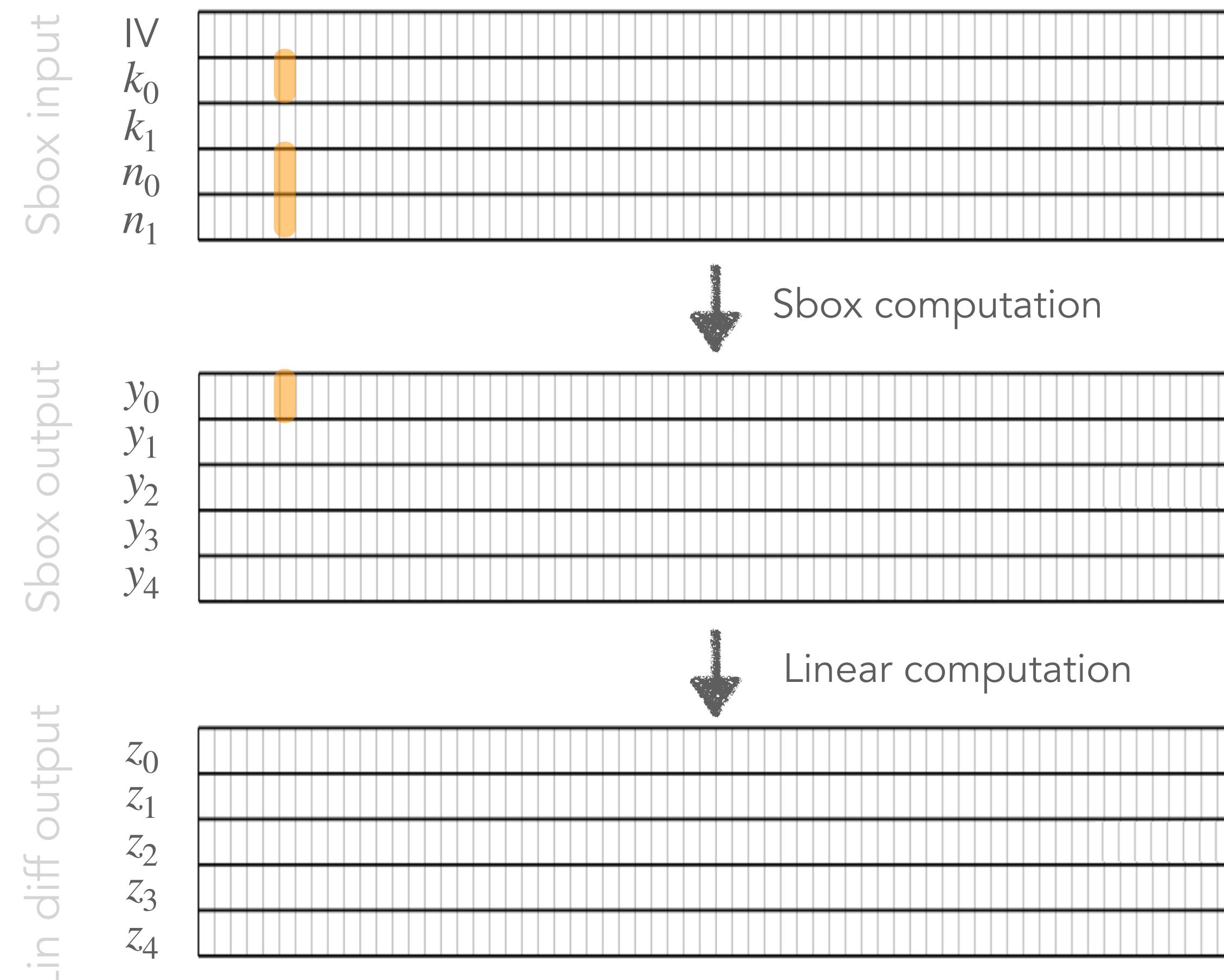
Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j IV^j \oplus k_1^j \oplus IV^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j IV^j \oplus k_1^j \oplus IV^j$$

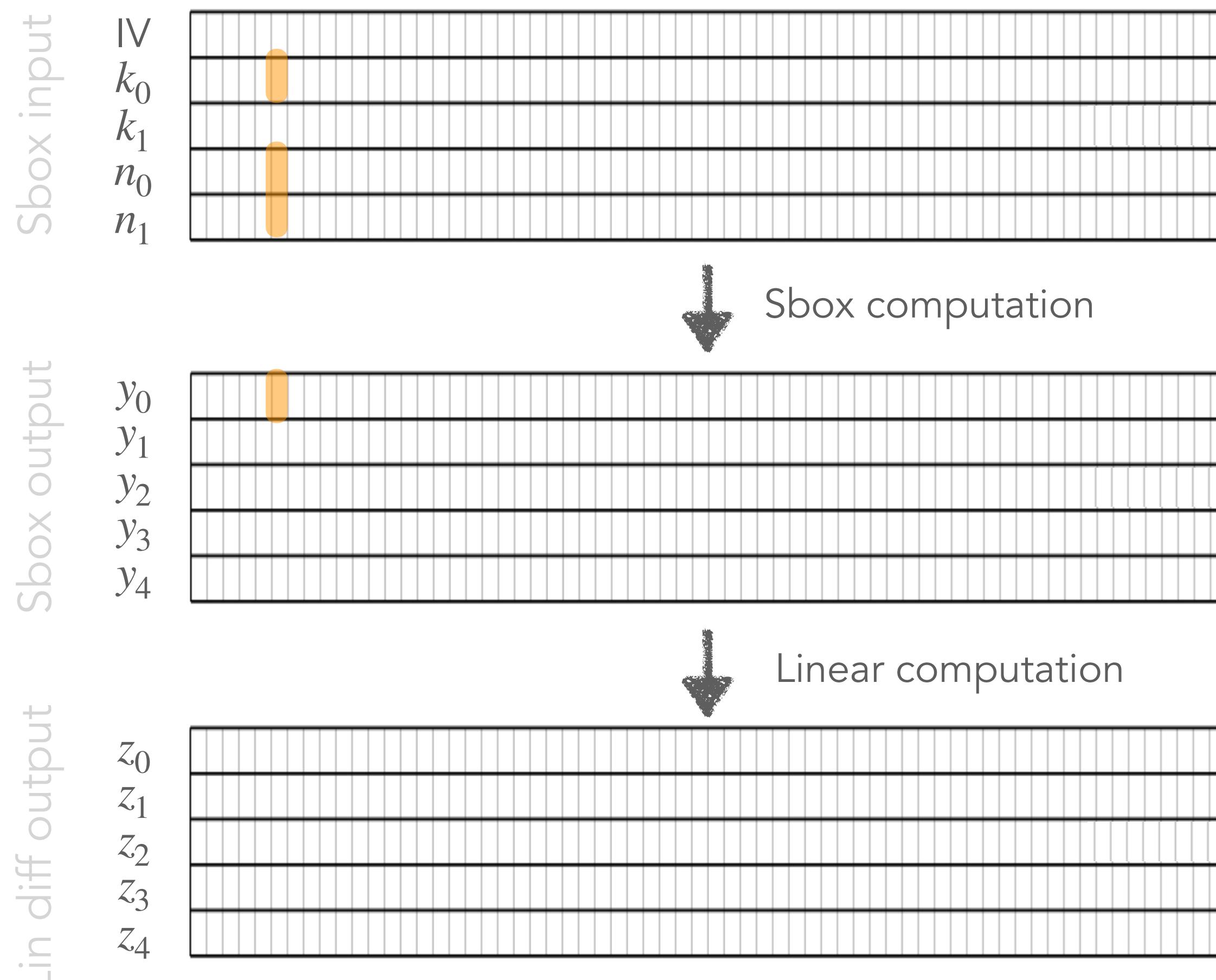
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j IV^j \oplus k_1^j \oplus IV^j$$

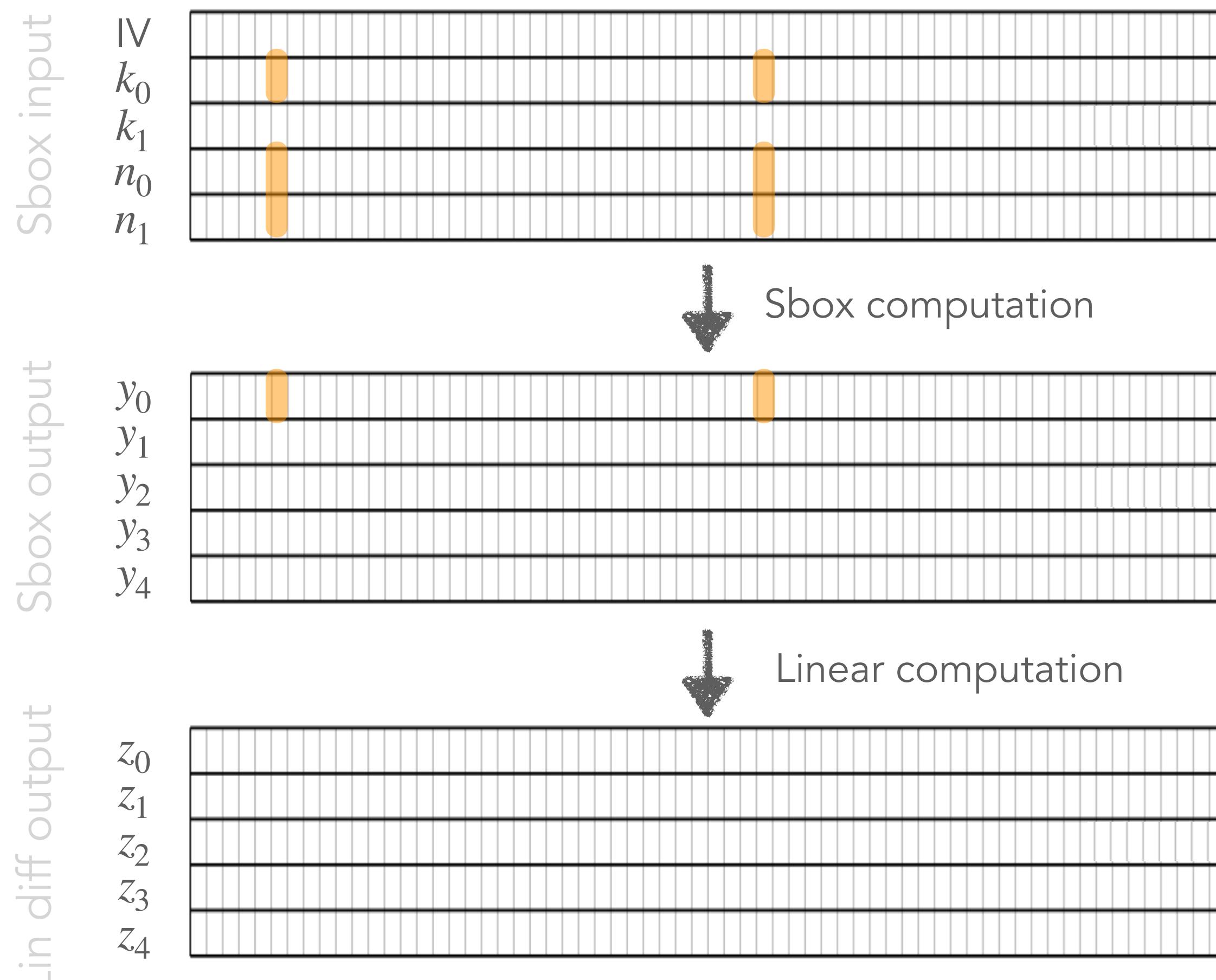
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

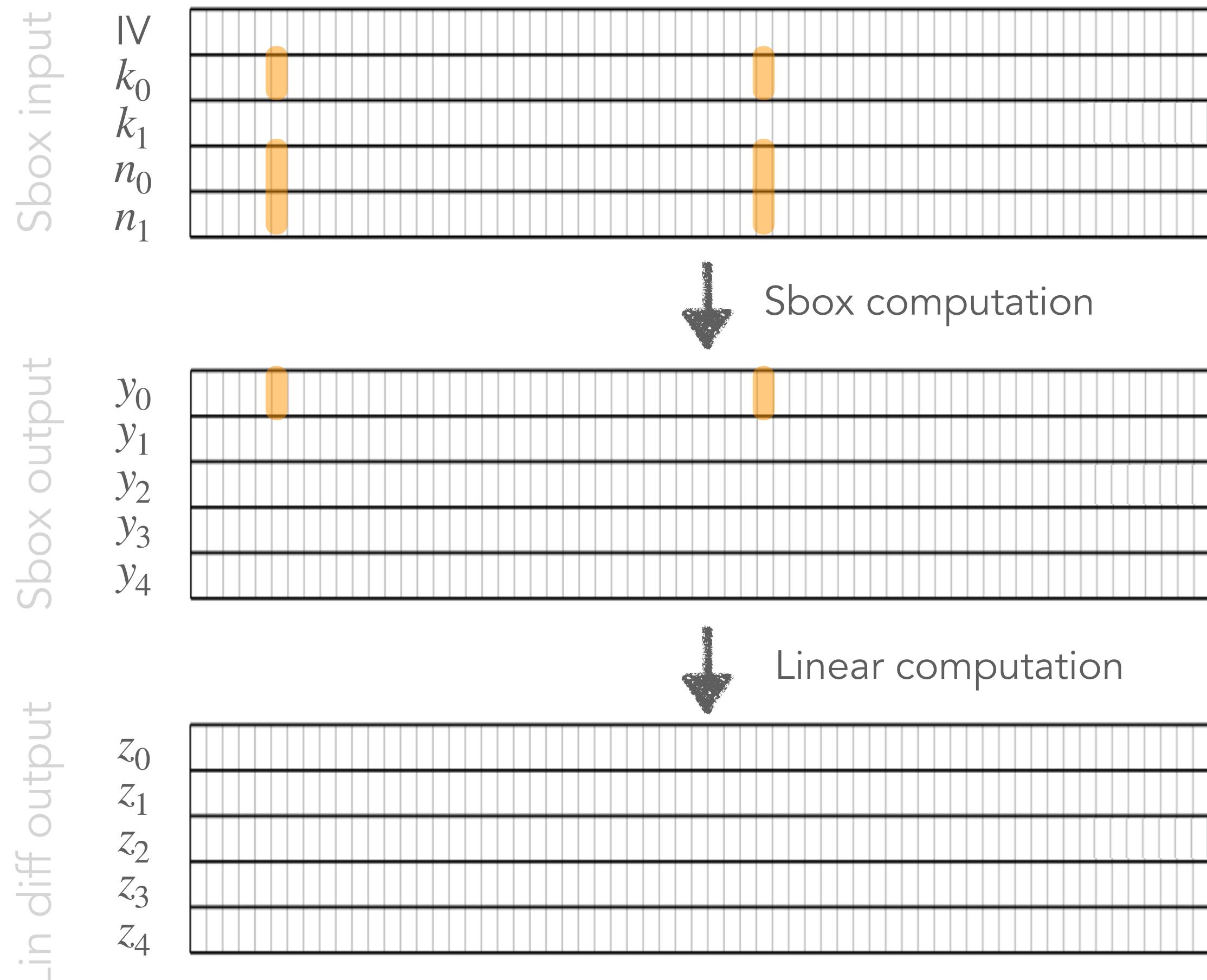
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

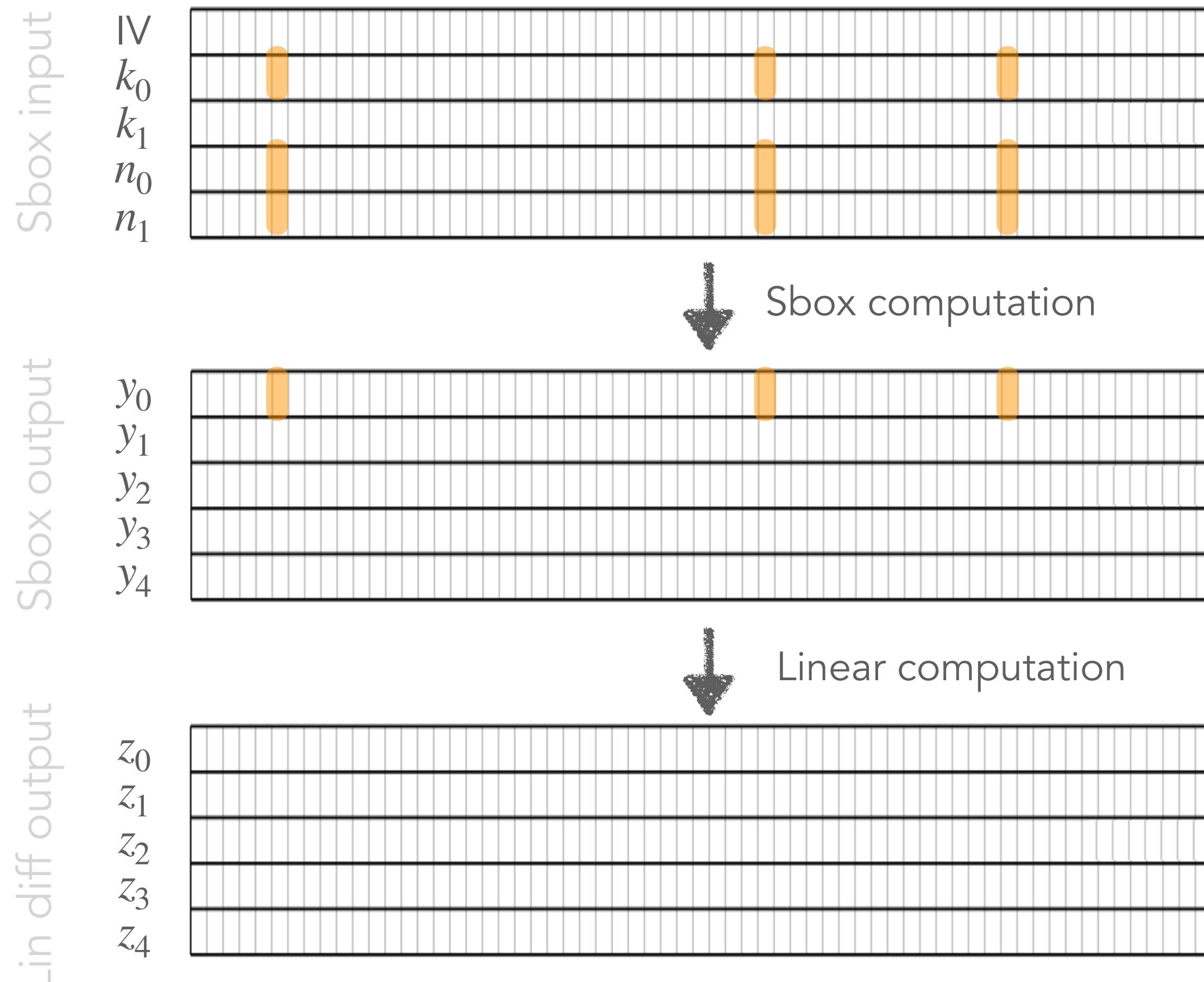
Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

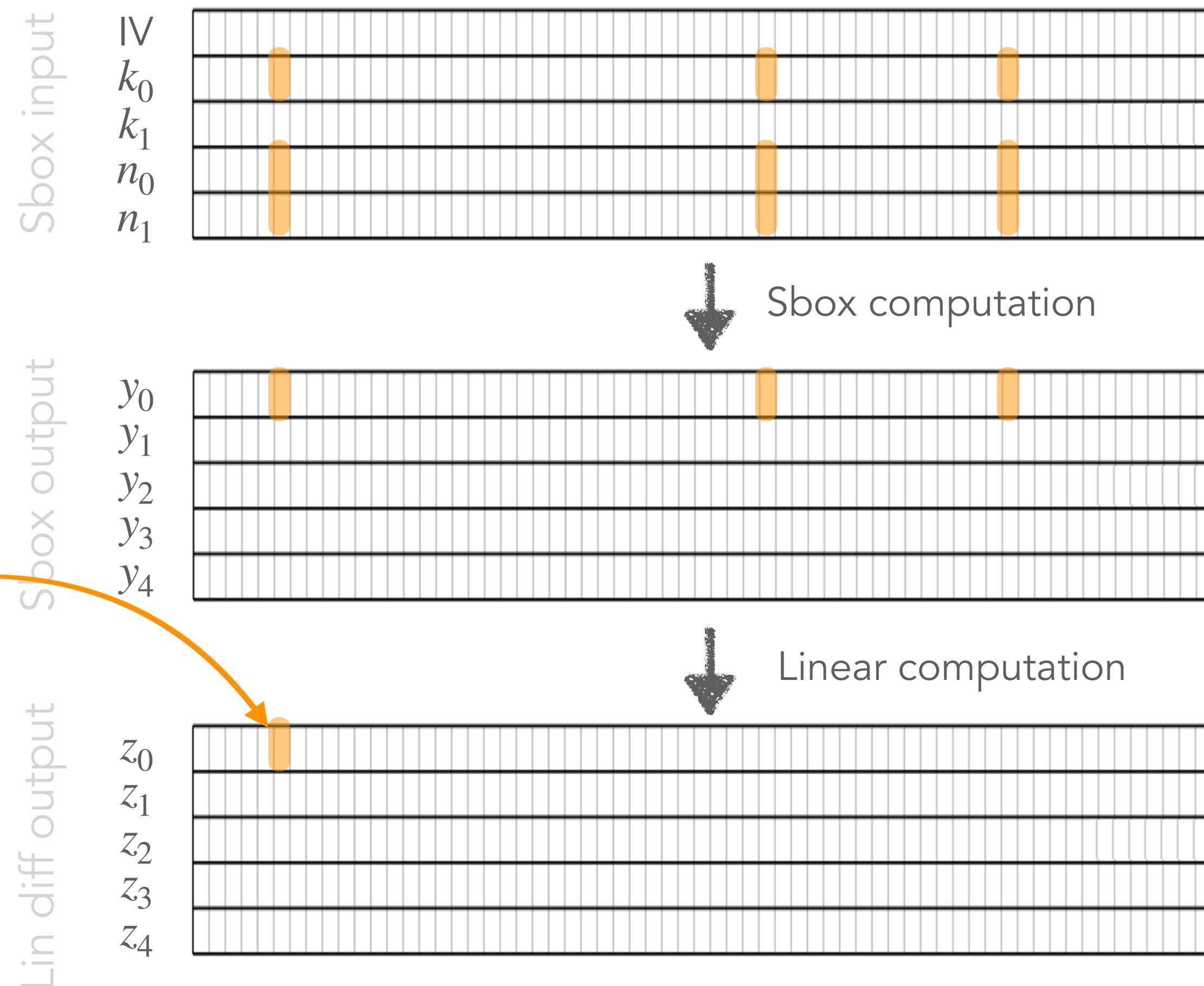
Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$

Linear computation:

$$\tilde{z}_0^j = \tilde{y}_0^j \oplus \tilde{y}_0^{j+36} \oplus \tilde{y}_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation
with register at linear layer output (z_0^j)

Similarly:

$$\tilde{y}_0^{j+36} = k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\tilde{y}_0^{j+45} = k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$

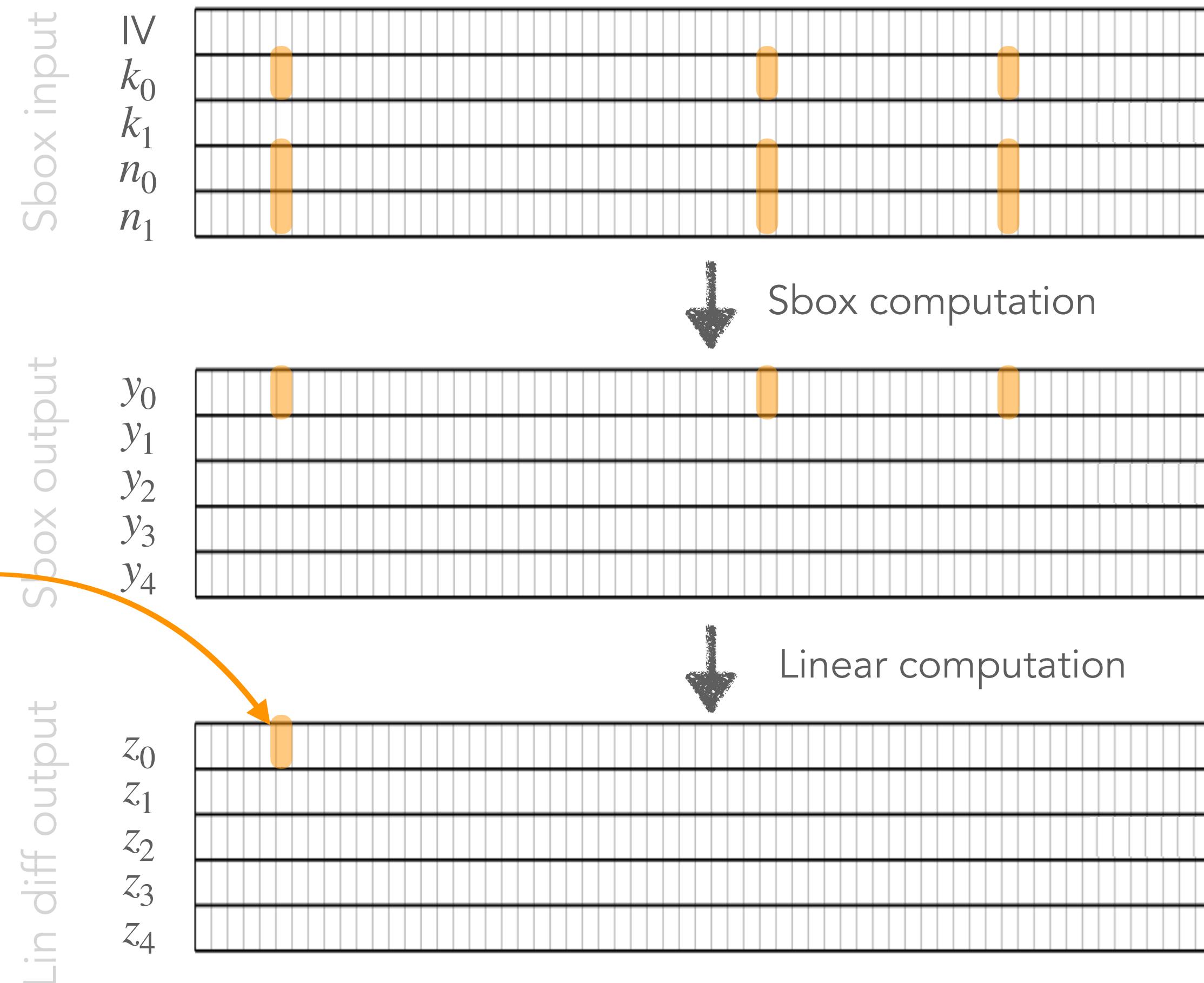
Linear computation:

$$\tilde{z}_0^j = \tilde{y}_0^j \oplus \tilde{y}_0^{j+36} \oplus \tilde{y}_0^{j+45}$$

$$\tilde{z}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

$$\oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36}$$

$$\oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}$$



Fine-tuned linear diffusion output as attack point

$$\begin{aligned}\tilde{z}_0^j &= k_0^j(n_1^j \oplus 1) \oplus n_0^j \\ &\oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \\ &\oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}\end{aligned}$$

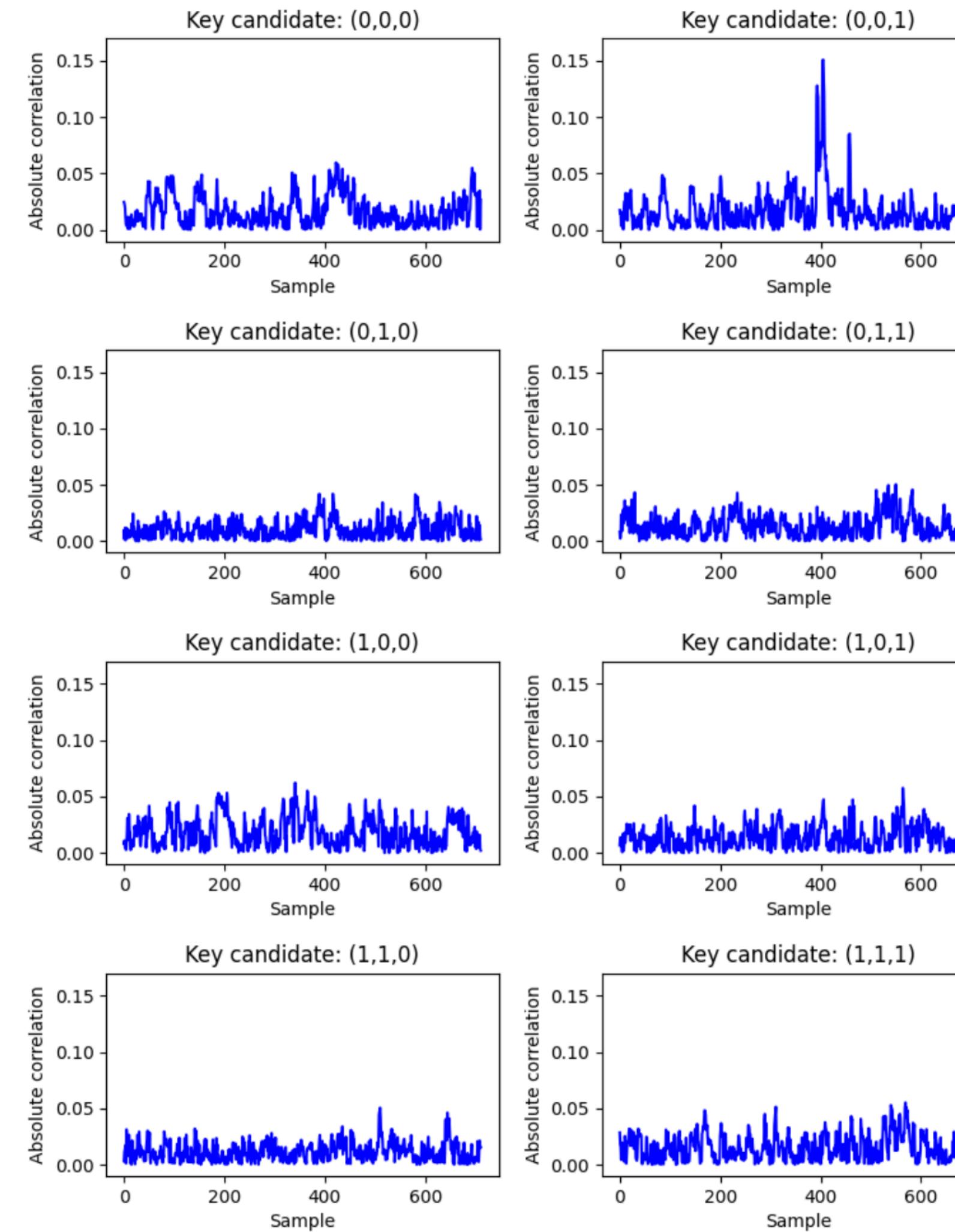
Fine-tuned linear diffusion output as attack point

Correlations of distributions associated to all key pairs

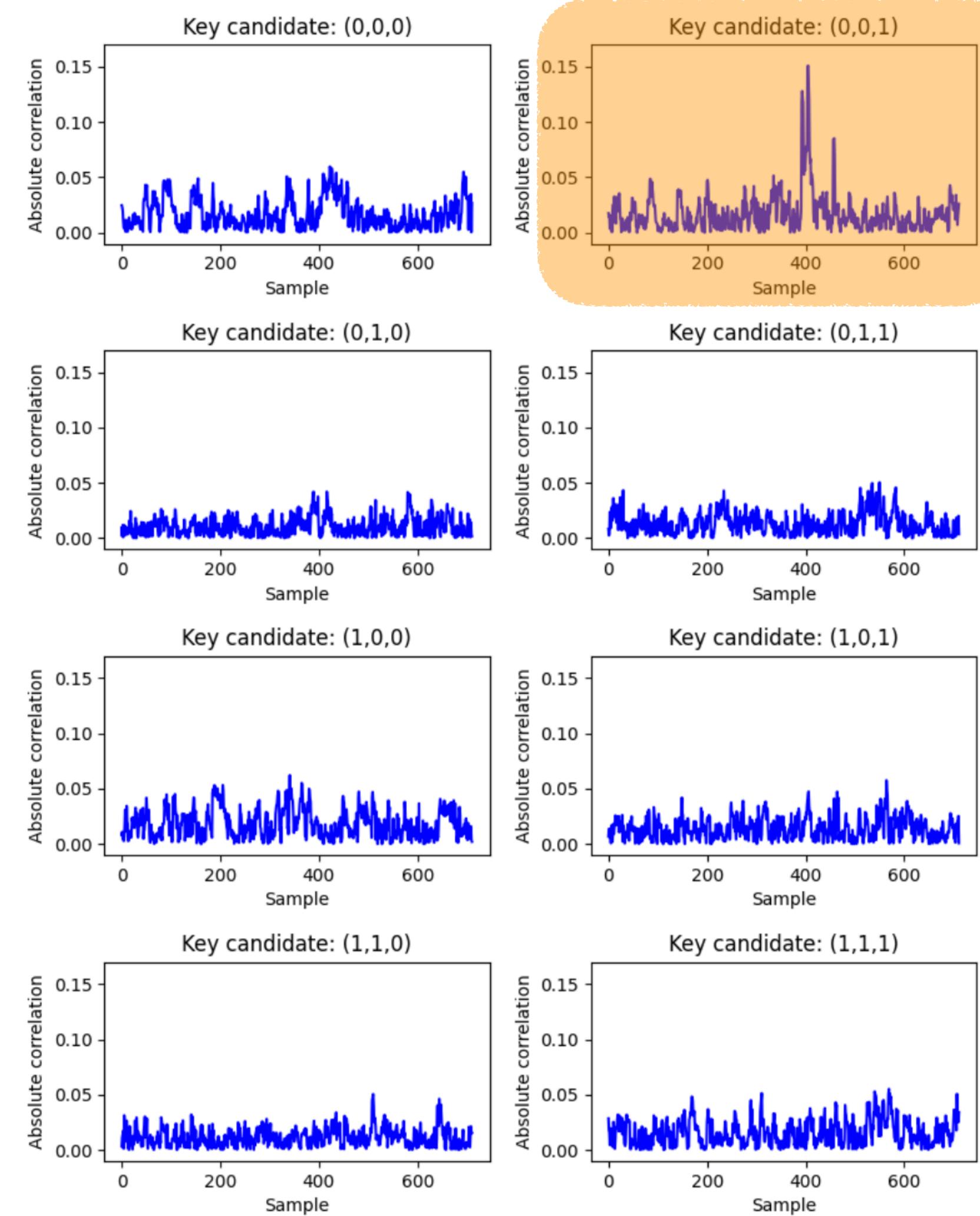
$(k_0^j, k_0^{j+36}, k_0^{j+45})$	(0,0,0)	(0,0,1)	(0,1,0)	(0,1,1)	(1,0,0)	(1,0,1)	(1,1,0)	(1,1,1)
(0,0,0)	1	-	-	-	-	-	-	-
(0,0,1)	-	1	-	-	-	-	-	-
(0,1,0)	-	-	1	-	-	-	-	-
(0,1,1)	-	-	-	1	-	-	-	-
(1,0,0)	-	-	-	-	1	-	-	-
(1,0,1)	-	-	-	-	-	1	-	-
(1,1,0)	-	-	-	-	-	-	1	-
(1,1,1)	-	-	-	-	-	-	-	1

$$\begin{aligned}\tilde{z}_0^j &= k_0^j(n_1^j \oplus 1) \oplus n_0^j \\ &\oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \\ &\oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}\end{aligned}$$

Fine-tuned linear diffusion output as attack point



Fine-tuned linear diffusion output as attack point



\tilde{y}_0^j as attack point ?

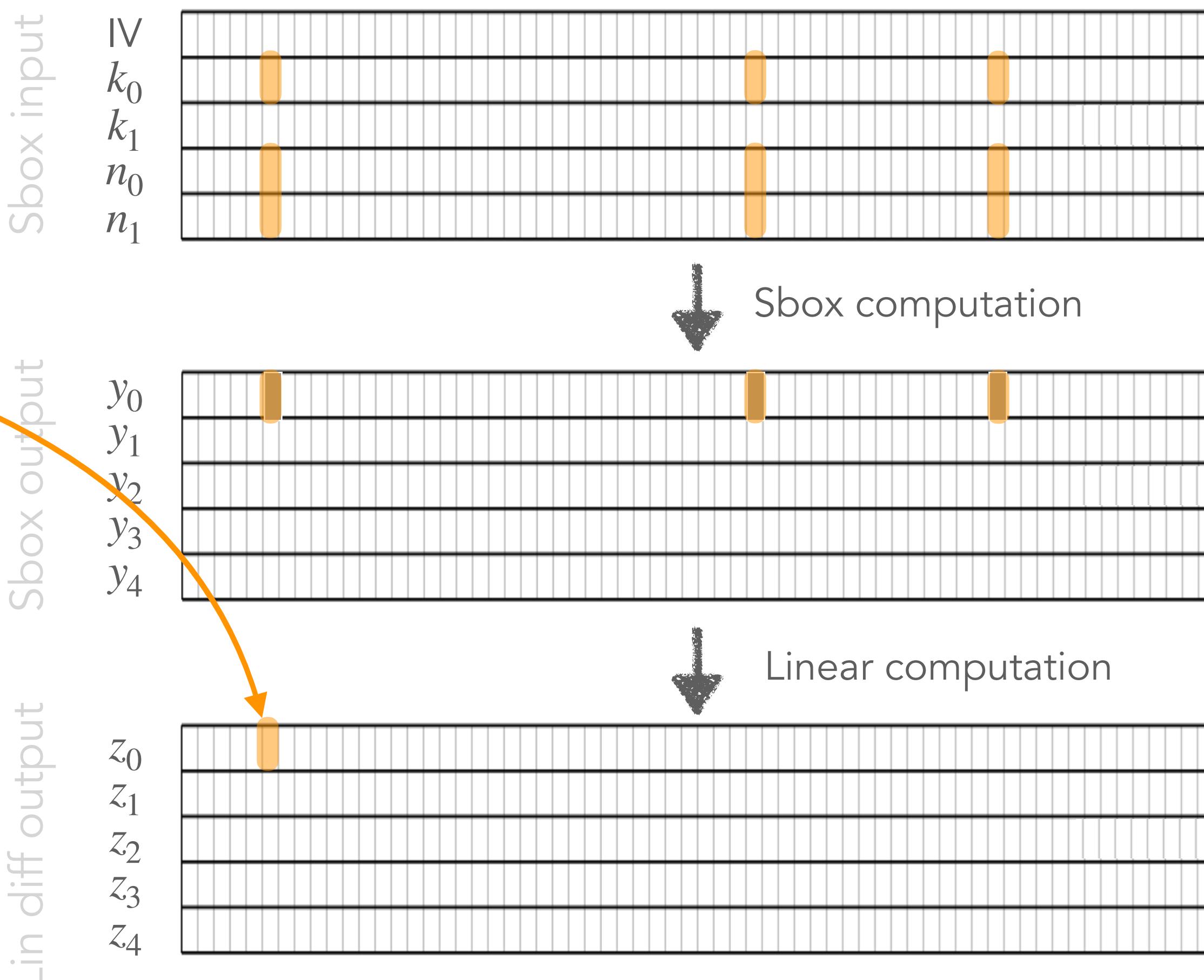
$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)



\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

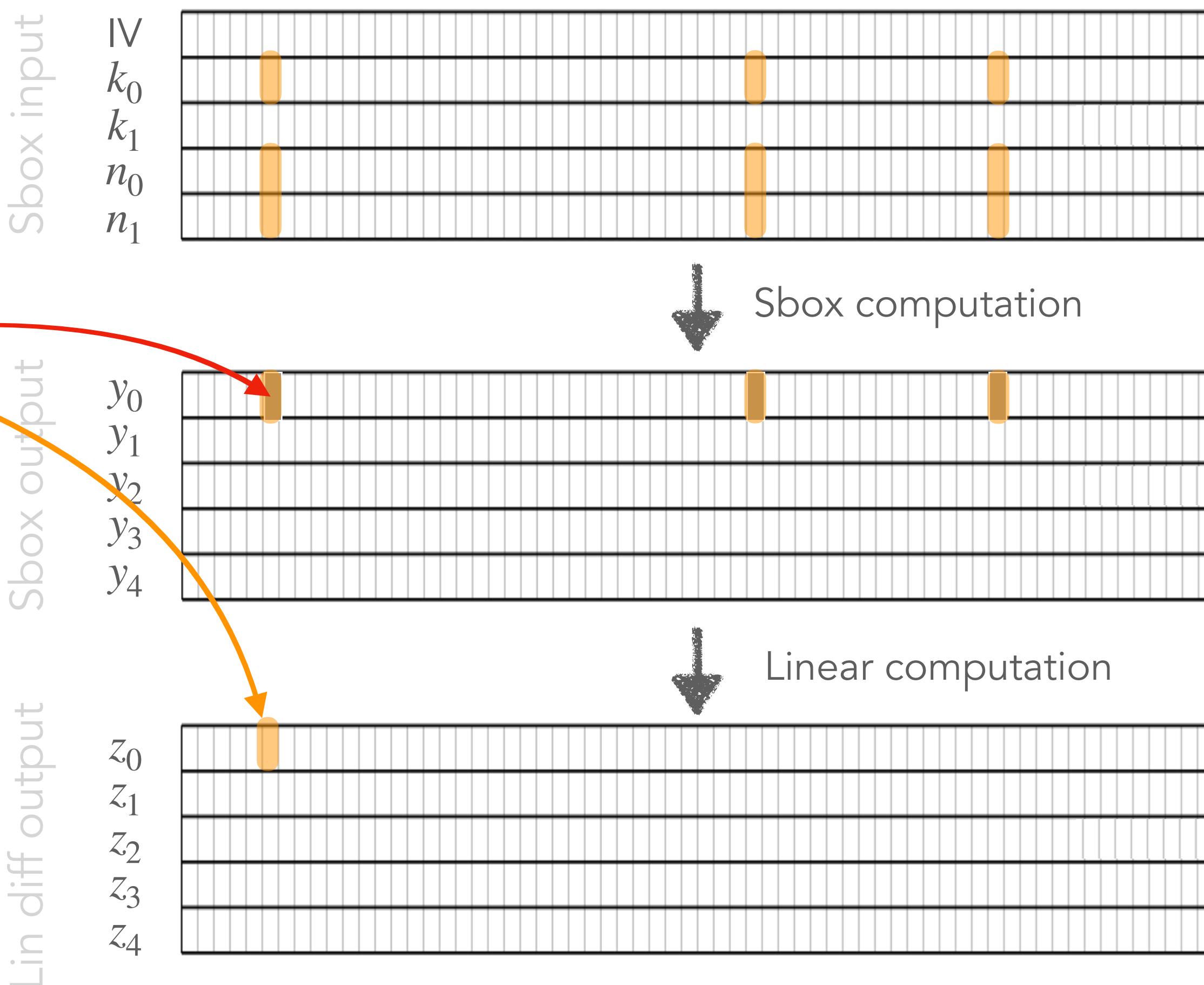
Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)

Software implementation:

Can we use \tilde{y}_0^j as attack point ?



\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathbb{W}^j \oplus k_1^j \oplus \mathbb{W}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)

Software implementation:

Can we use \tilde{y}_0^j as attack point ?

\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathbb{W}^j \oplus k_1^j \oplus \mathbb{W}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)

Software implementation:

Can we use \tilde{y}_0^j as attack point ?

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$

\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \mathbb{W}^j \oplus k_1^j \oplus \mathbb{W}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)

Software implementation:

Can we use \tilde{y}_0^j as attack point ?

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases} \rightarrow \text{Correlated with activity of } n_0$$

\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

Hardware implementation

with register at linear layer output (z_0^j)

Software implementation:

Can we use \tilde{y}_0^j as attack point ?

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$

→ Correlated with activity of n_0
→ Correlated with activity of $n_0 \oplus n_1$

\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

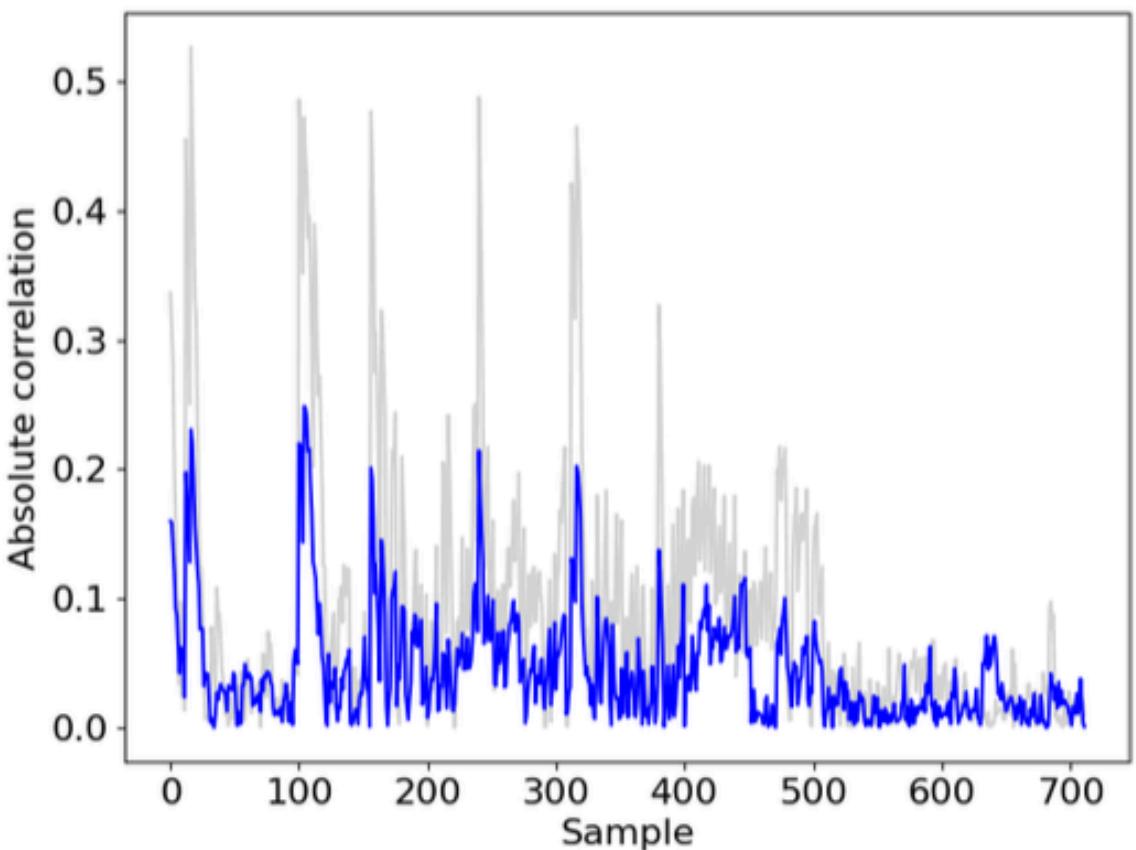
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

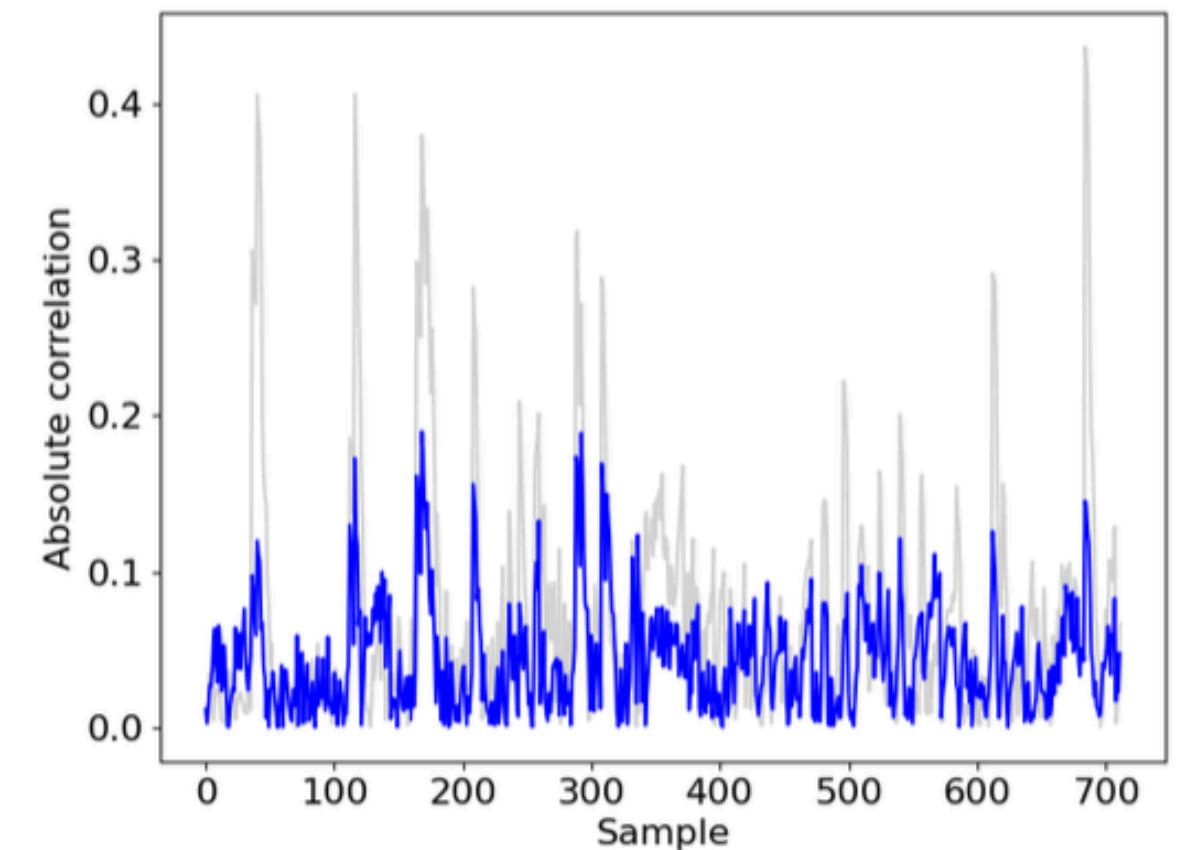
Hardware implementation
with register at linear layer output (z_0^j)

Software implementation:
Can we use \tilde{y}_0^j as attack point ?

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$



(a) $k_0^j = 0$



(b) $k_0^j = 1$

- Correlated with activity of n_0
- Correlated with activity of $n_0 \oplus n_1$

\tilde{y}_0^j as attack point ?

$$y_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j \oplus k_1^j k_0^j \oplus k_0^j \text{IV}^j \oplus k_1^j \oplus \text{IV}^j$$

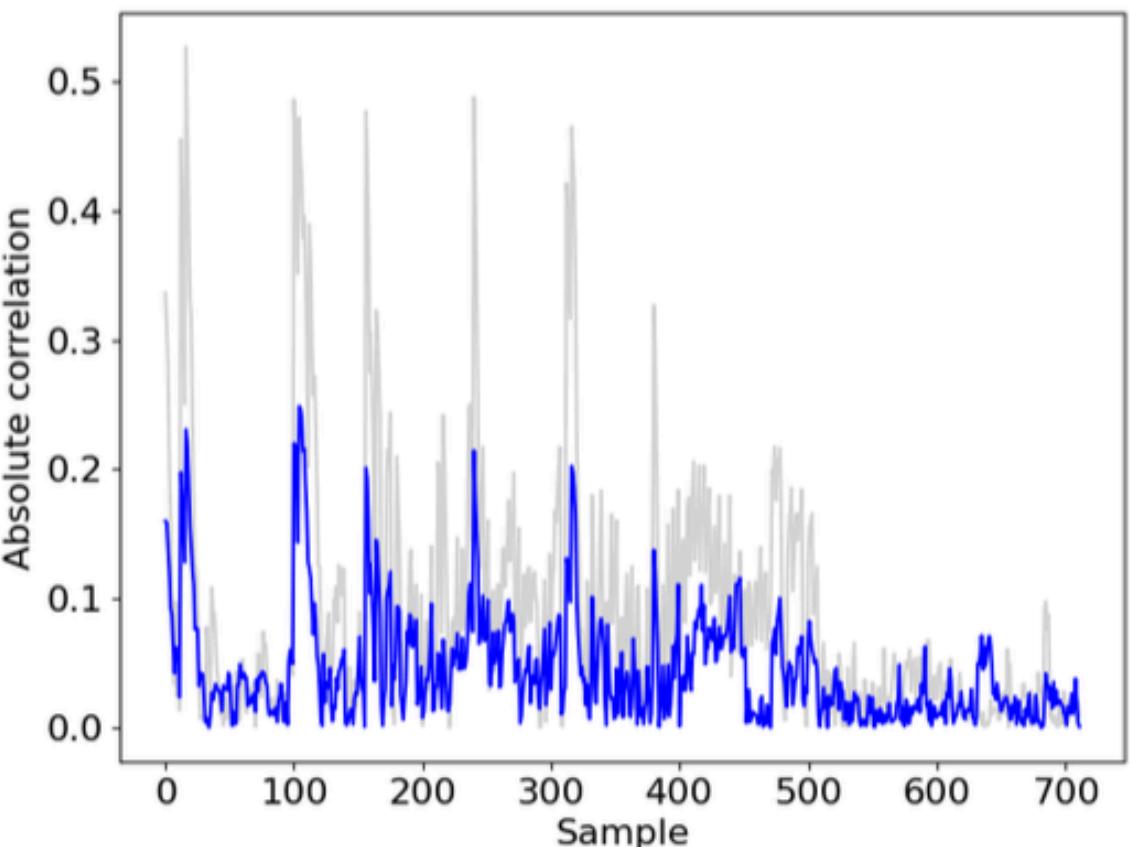
$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j$$

Samwel and Daemen, 2018

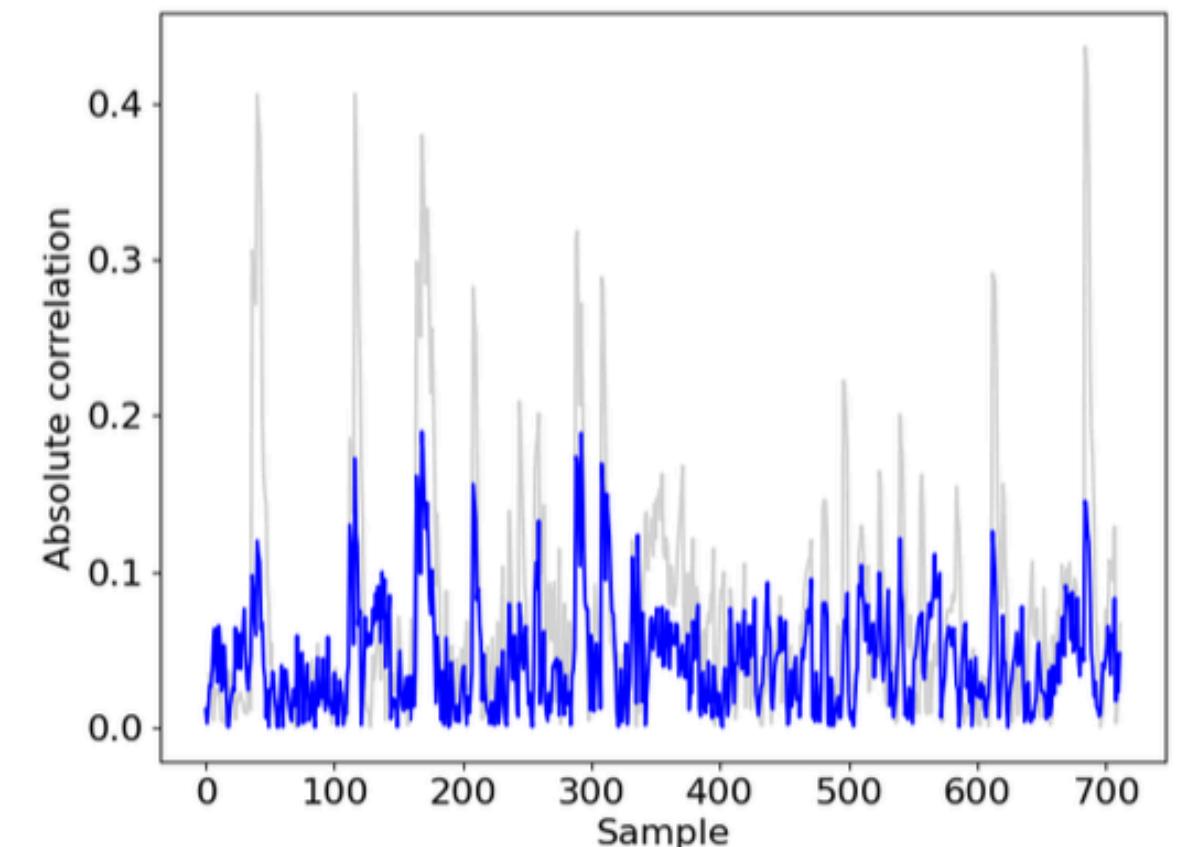
Hardware implementation
with register at linear layer output (z_0^j)

Software implementation:
Can we use \tilde{y}_0^j as attack point ?

$$\tilde{y}_0^j = k_0^j(n_1^j \oplus 1) \oplus n_0^j = \begin{cases} n_0^j & \text{if } k_0^j = 0, \\ n_0^j \oplus n_1^j \oplus 1 & \text{if } k_0^j = 1. \end{cases}$$



(a) $k_0^j = 0$



(b) $k_0^j = 1$

- Correlated with activity of n_0
- Correlated with activity of $n_0 \oplus n_1$

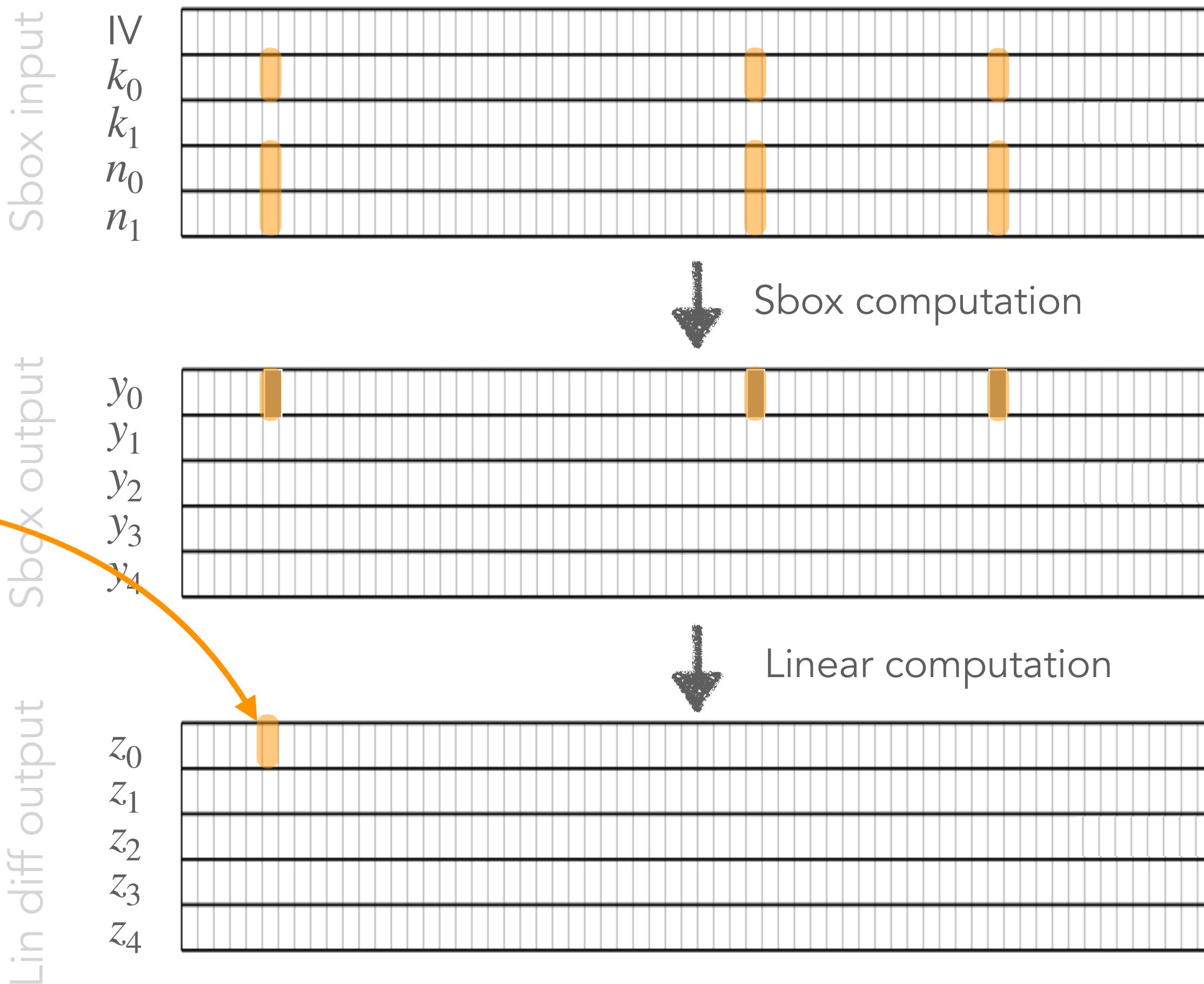
→ Not effective for CPA attacks

The best choice

for both hardware and software implementations

Samwel and Daemen, 2018

$$\begin{aligned}\tilde{z}_0^j &= k_0^j(n_1^j \oplus 1) \oplus n_0^j \\ &\oplus k_0^{j+36}(n_1^{j+36} \oplus 1) \oplus n_0^{j+36} \\ &\oplus k_0^{j+45}(n_1^{j+45} \oplus 1) \oplus n_0^{j+45}\end{aligned}$$



Second-Order CPA attack

Target implementation

Masked software implementations with 2 shares
by Ascon team

<https://github.com/ascon/simpleserial-ascon>

The screenshot shows the GitHub repository page for 'simpleserial-ascon' owned by 'ascon'. The repository is public and has 7 watchers, 2 forks, and 11 starred users. It contains 1 branch and 1 tag. The main branch has 19 commits from 'mschlaeffe' and others. The repository description is 'Masked Ascon Software Implementations' and it links to 'ascon.iak.tugraz.at/'. The repository also includes a 'Readme' file, a 'CC0-1.0 license', and an 'Activity' section.

Code Issues Pull requests Actions Projects Security Insights

simpleserial-ascon Public

main 1 Branch 1 Tag

mschlaeffe Add more t-test results ca4a609 · 3 years ago 19 Commits

Documents Use single jupyter notebook for plain and shared interface 3 years ago

Implementations/crypto_aead/ascon128v12 Add initial version of masked Ascon implementations 3 years ago

jupyter Note that SS_VER_2_1 only works on the CW develop bra... 3 years ago

About

Masked Ascon Software Implementations

ascon.iak.tugraz.at/

Readme

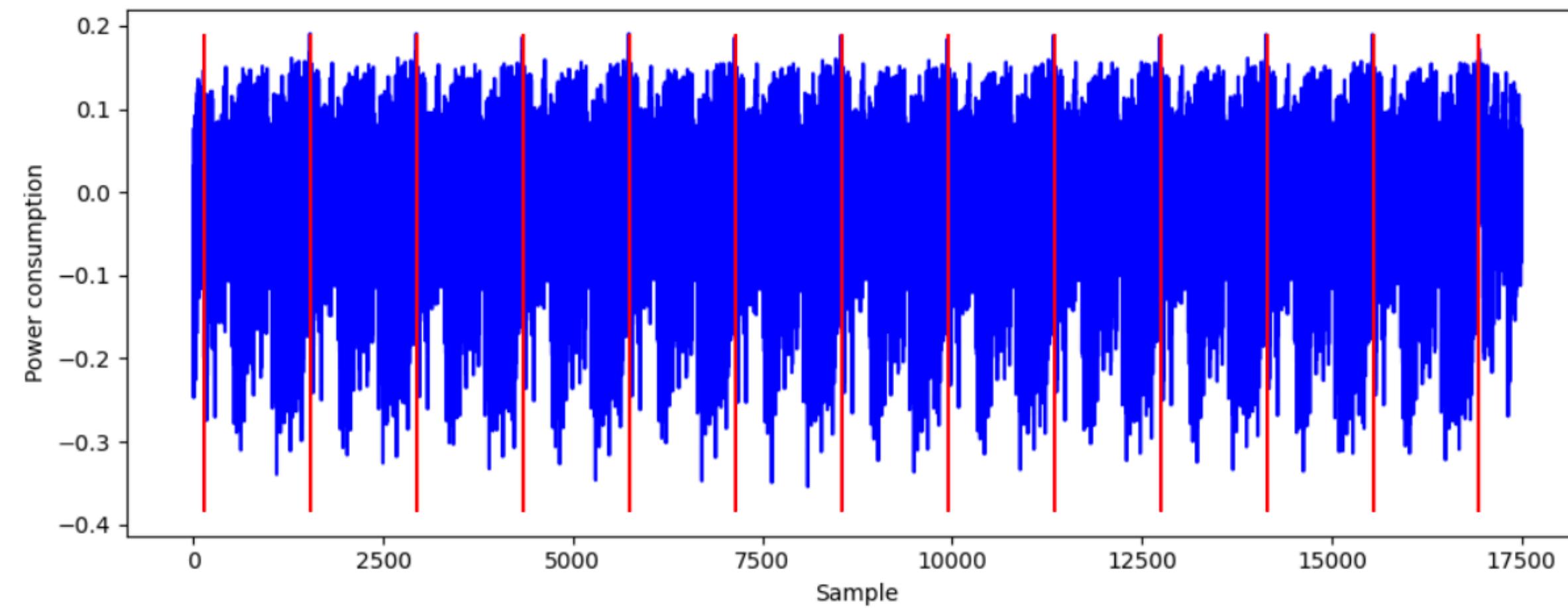
CC0-1.0 license

Activity

Target implementation

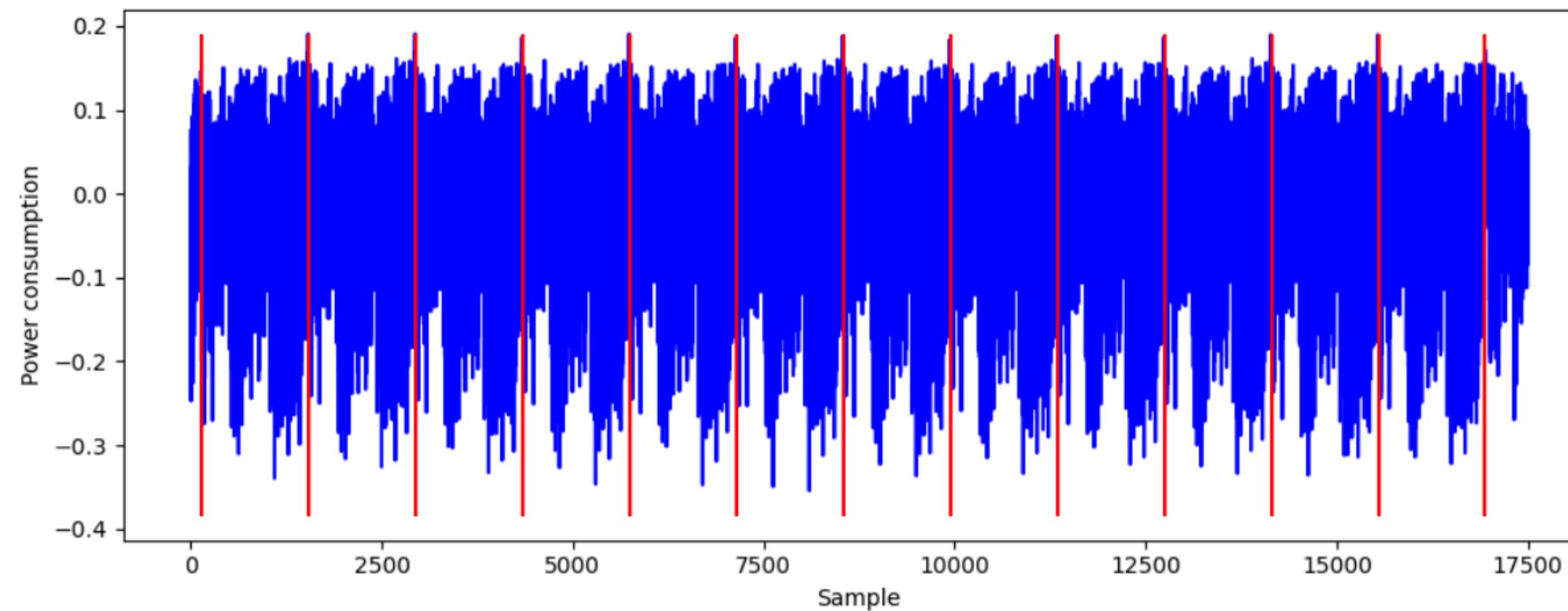
Target implementation

Power consumption of the first 12 rounds from ChipWhisperer ARM with Cortex-M3 core:

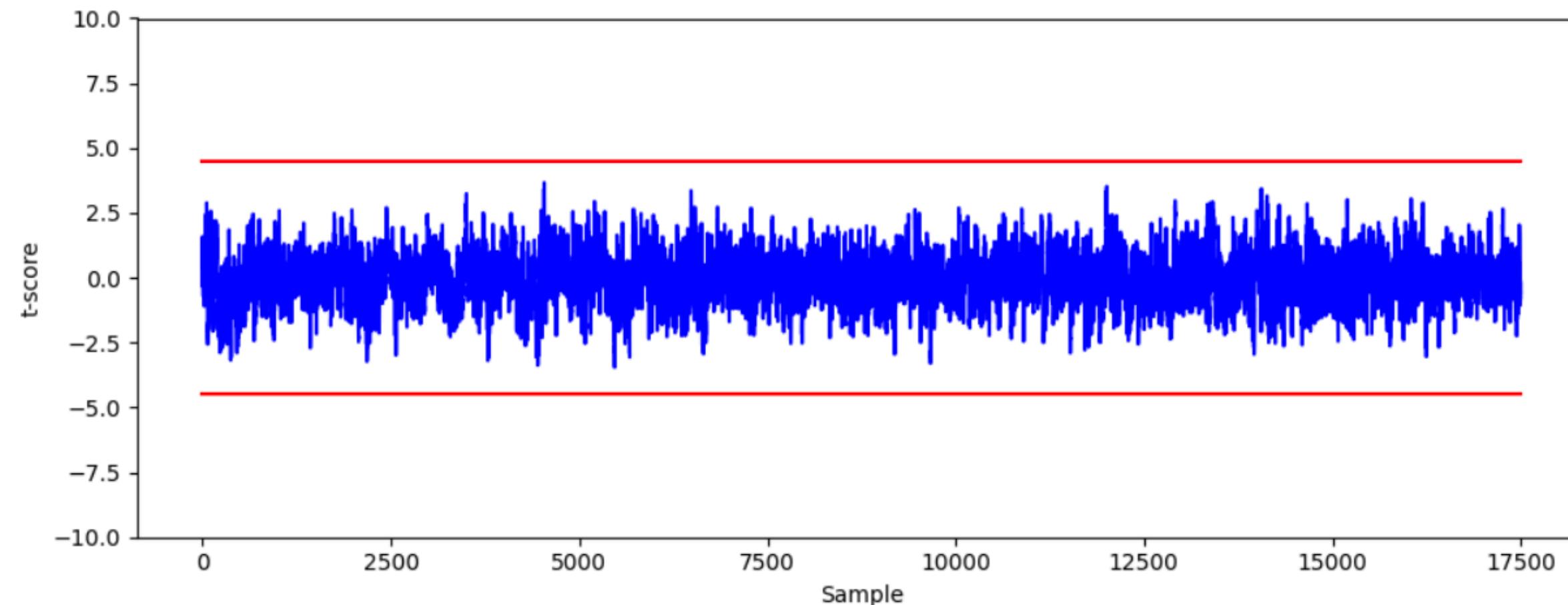


Target implementation

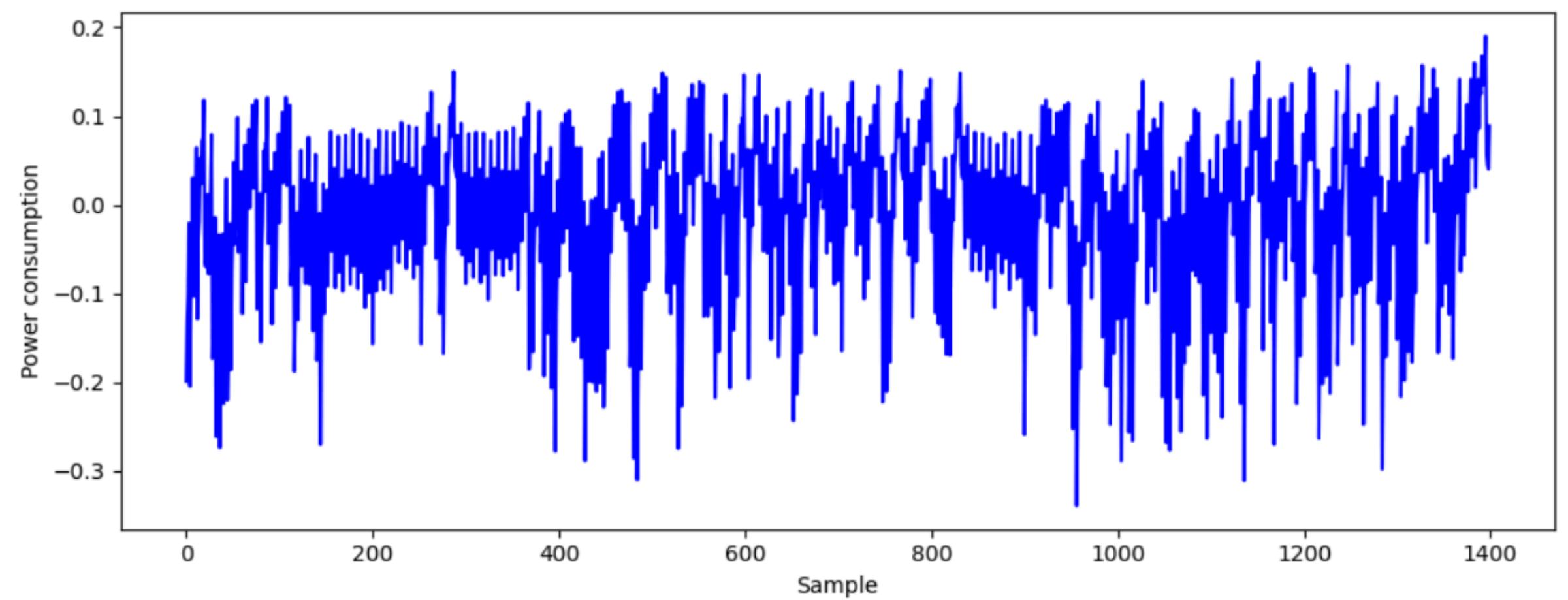
Power consumption of the first 12 rounds from ChipWhisperer ARM with Cortex-M3 core:



Verify first-order leakage by TVLA:

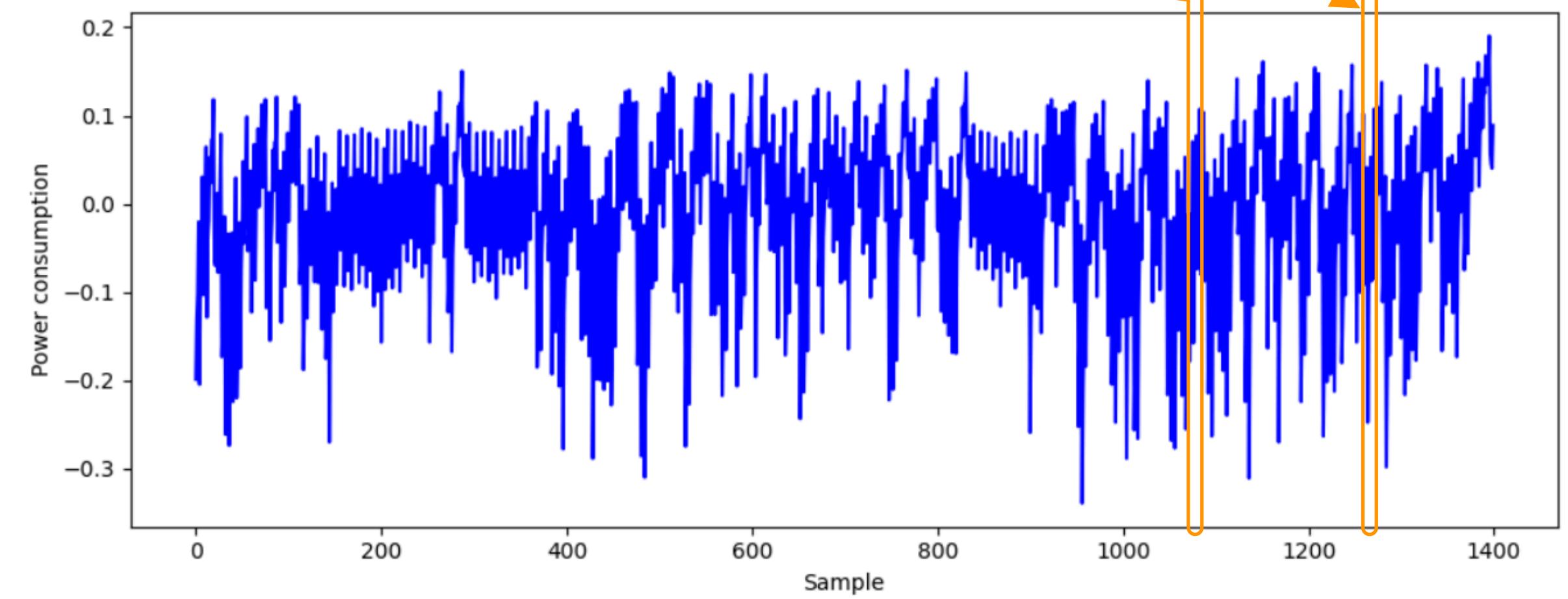


Second-Order CPA



Second-Order CPA

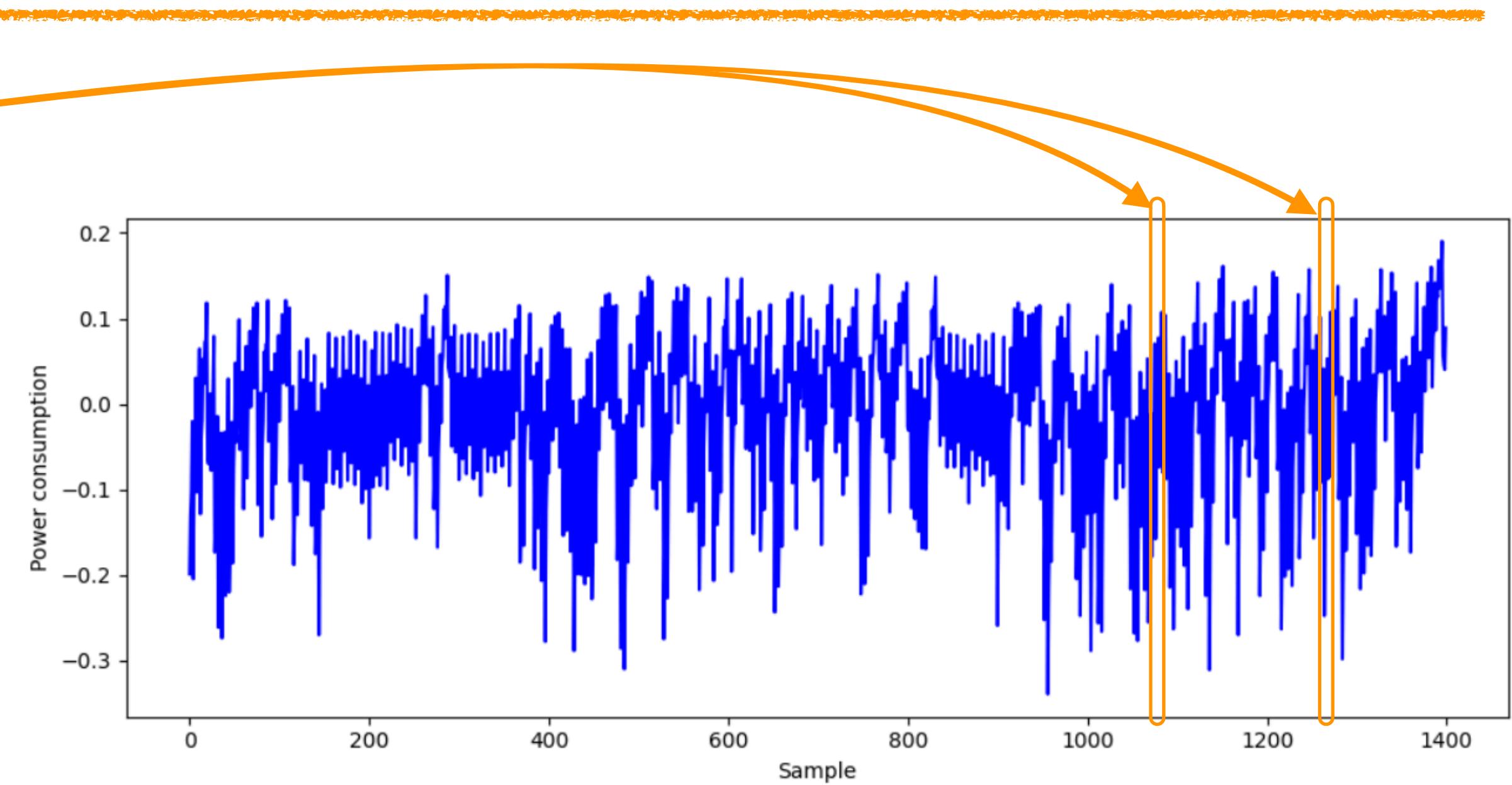
Combine two points on the trace
(by normalized product)



Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

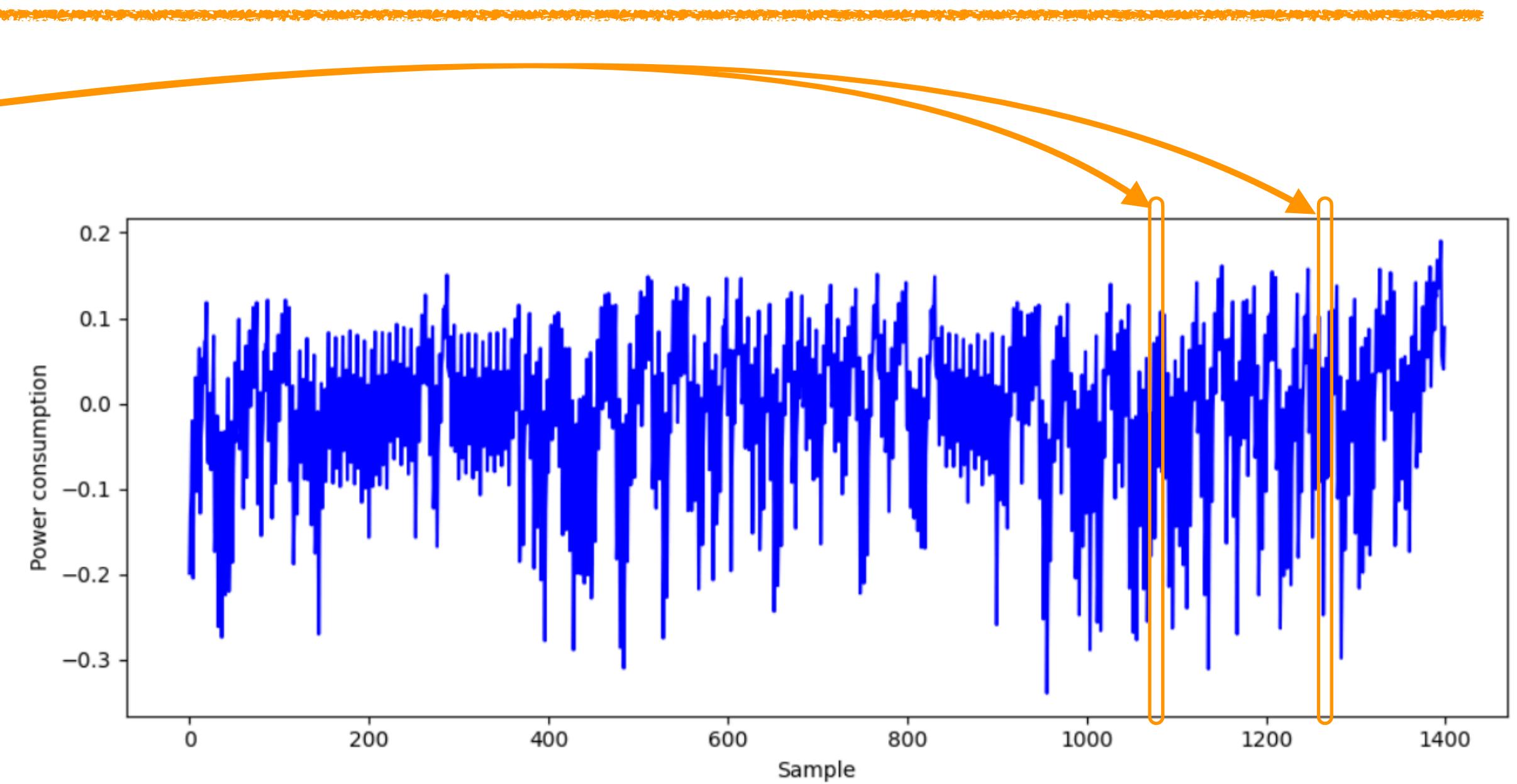


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output

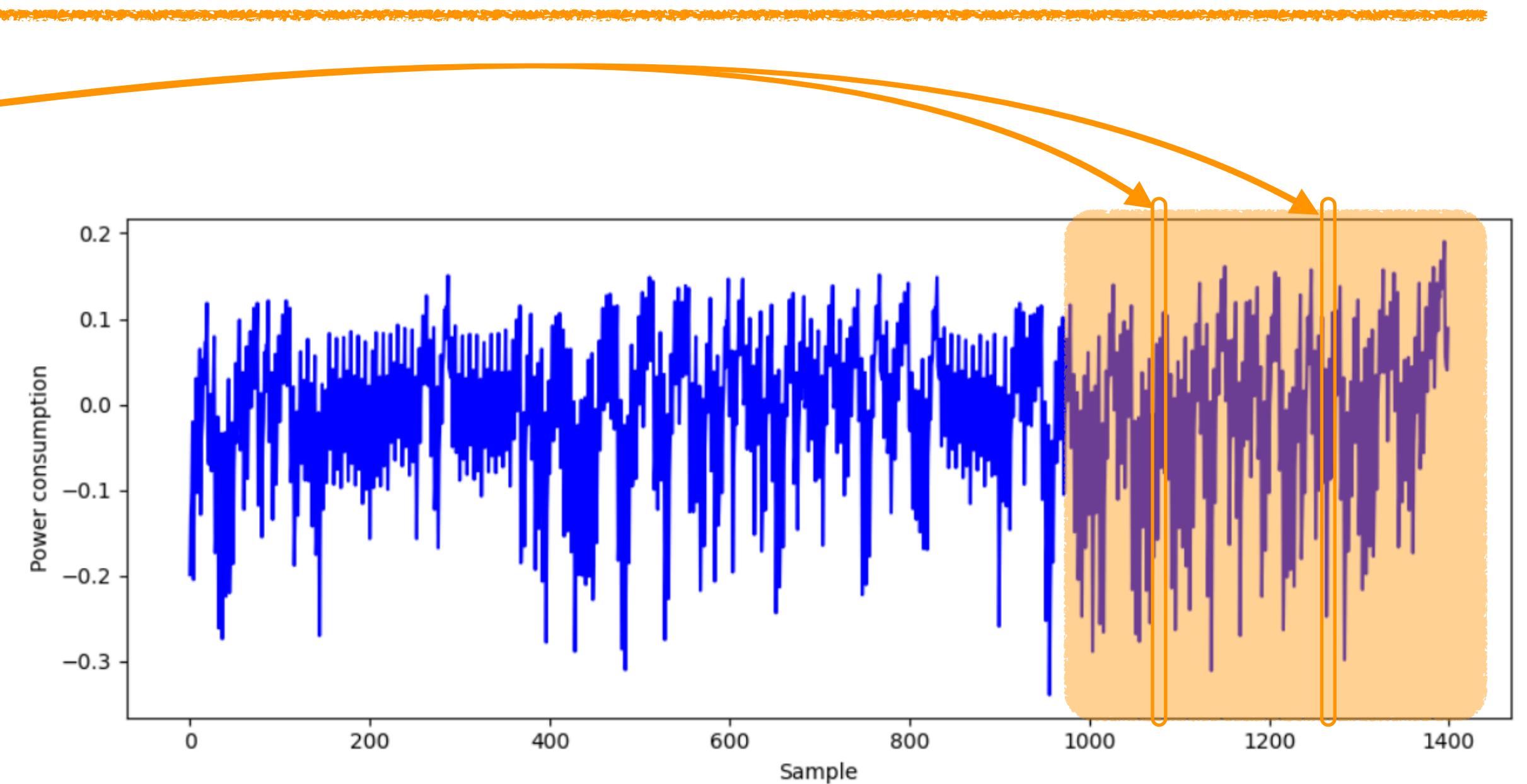


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output

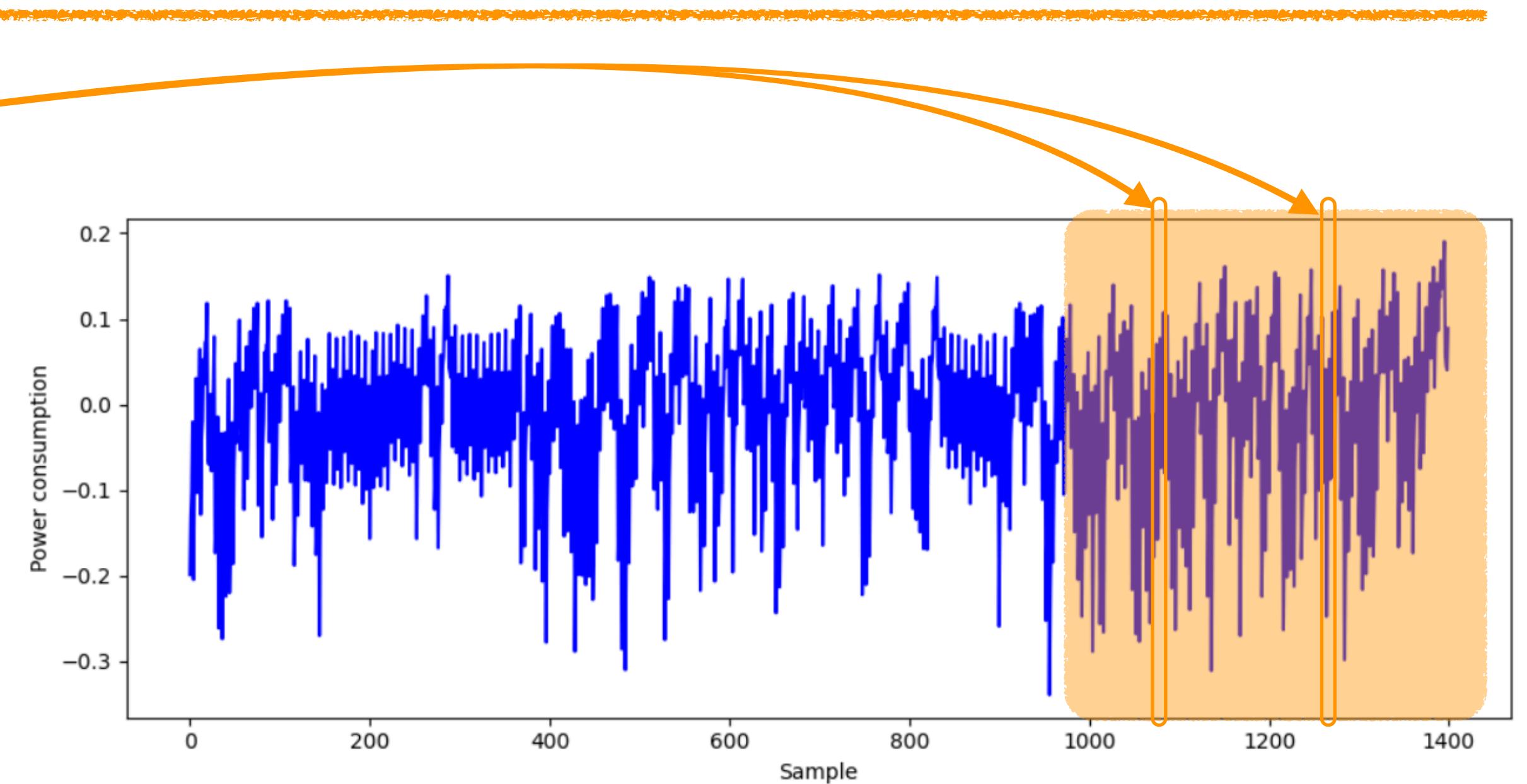


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output
→ focus on the right part of the trace

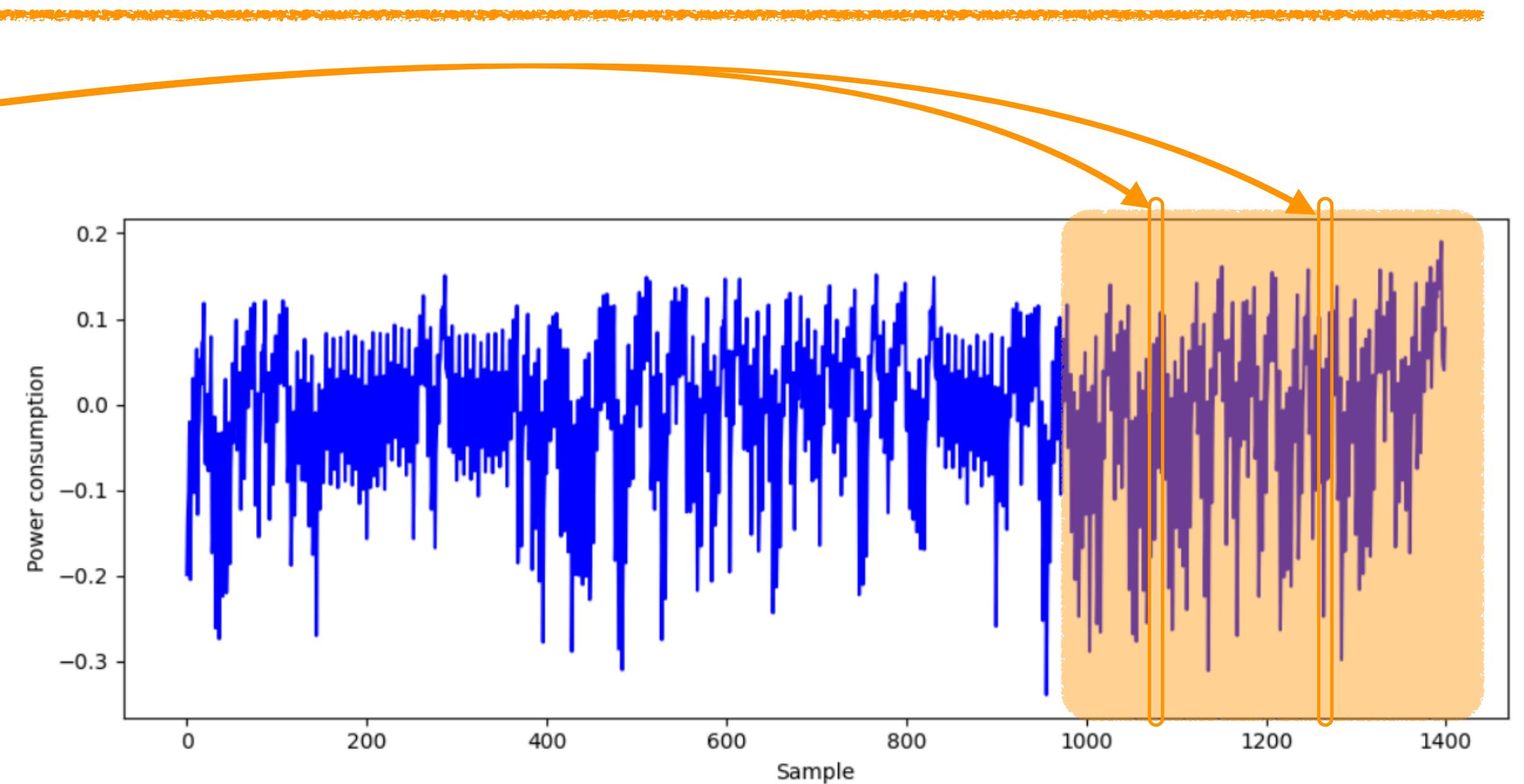


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output
→ focus on the right part of the trace
- Two shares occur in a time span

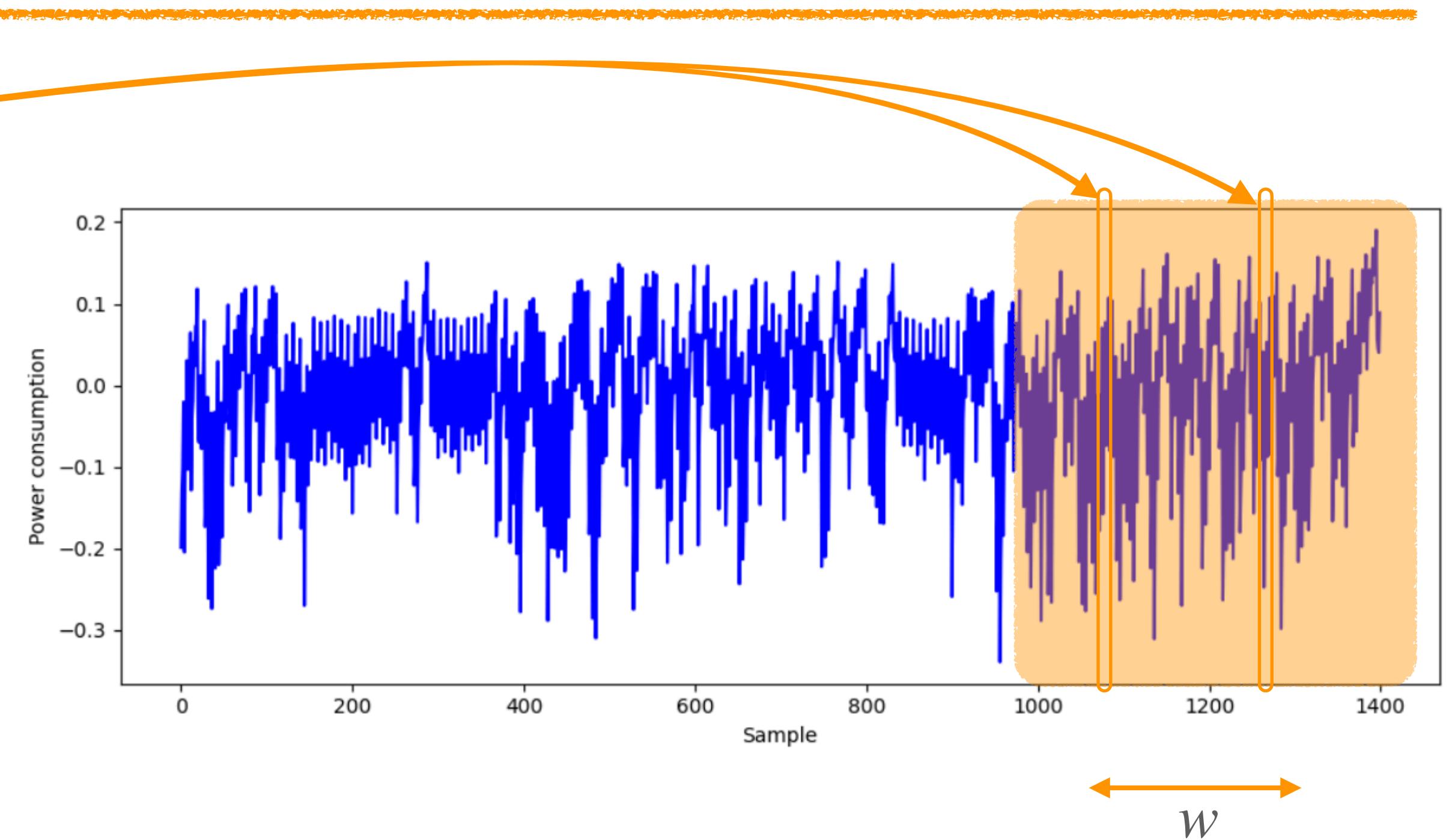


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output
→ focus on the right part of the trace
- Two shares occur in a time span

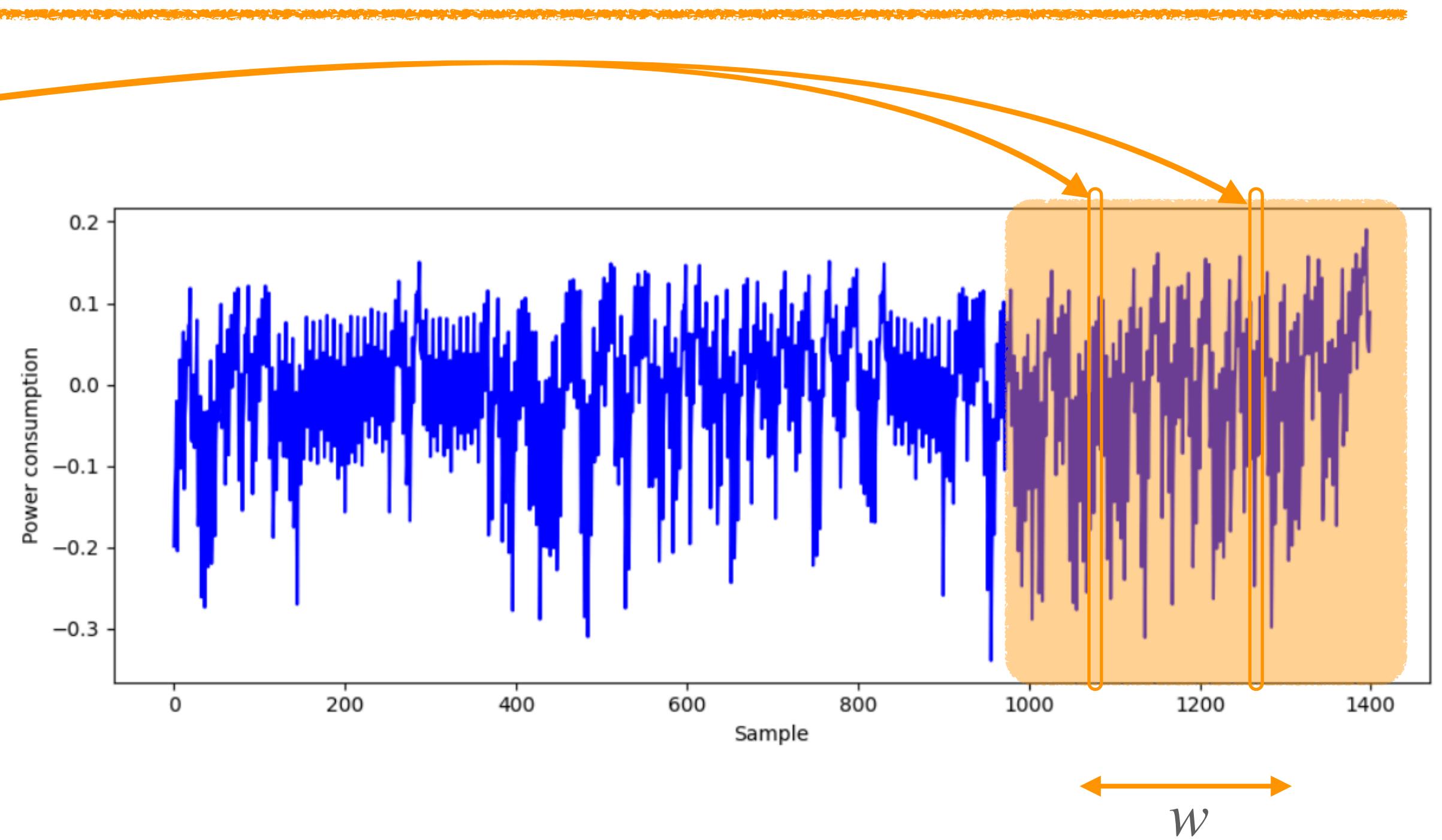


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output
→ focus on the right part of the trace
- Two shares occur in a time span
→ parameter window w

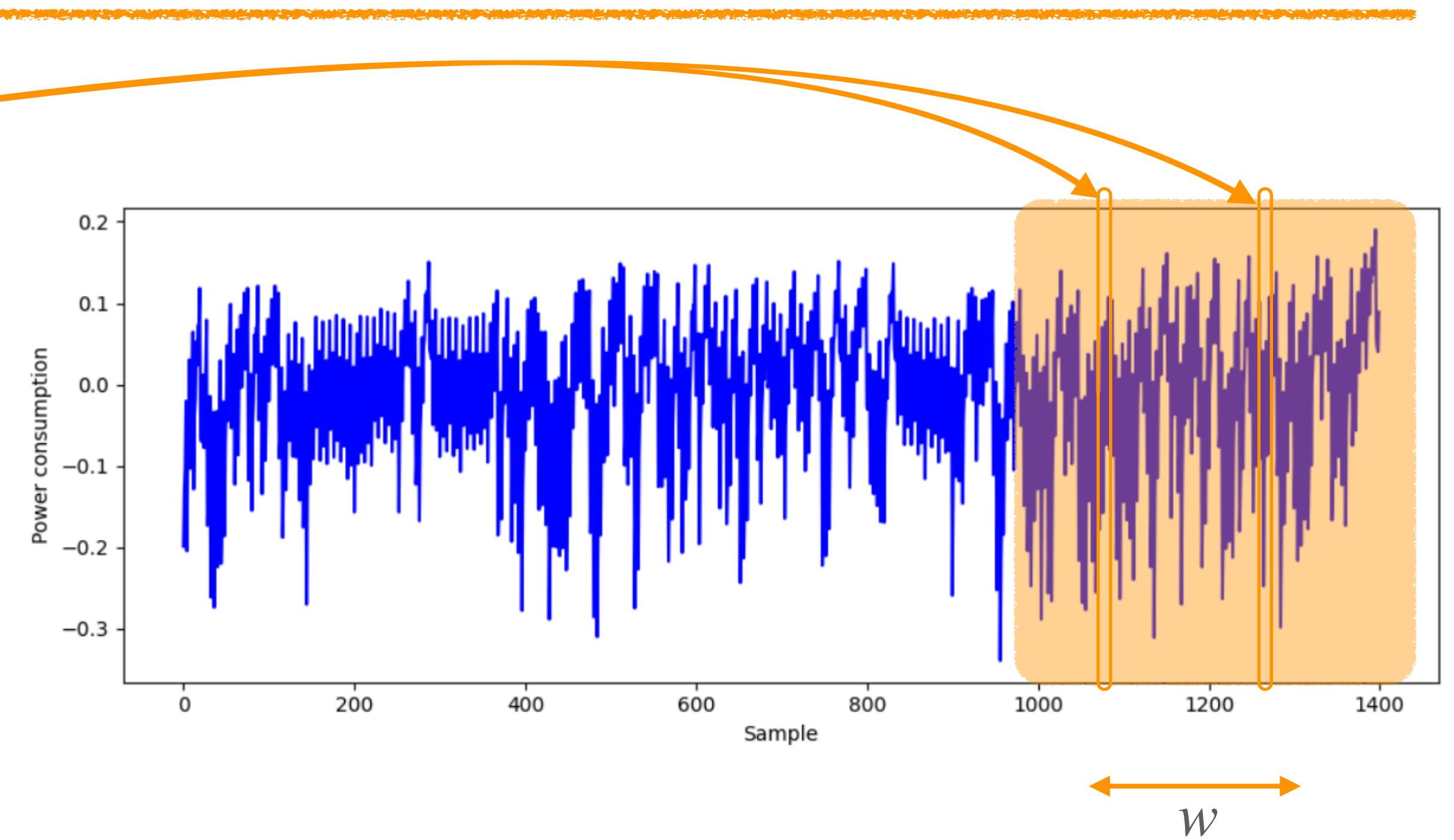


Second-Order CPA

Combine two points on the trace
(by normalized product)

Optimizations:

- Attack point at linear layer output
→ focus on the right part of the trace
- Two shares occur in a time span
→ parameter window w

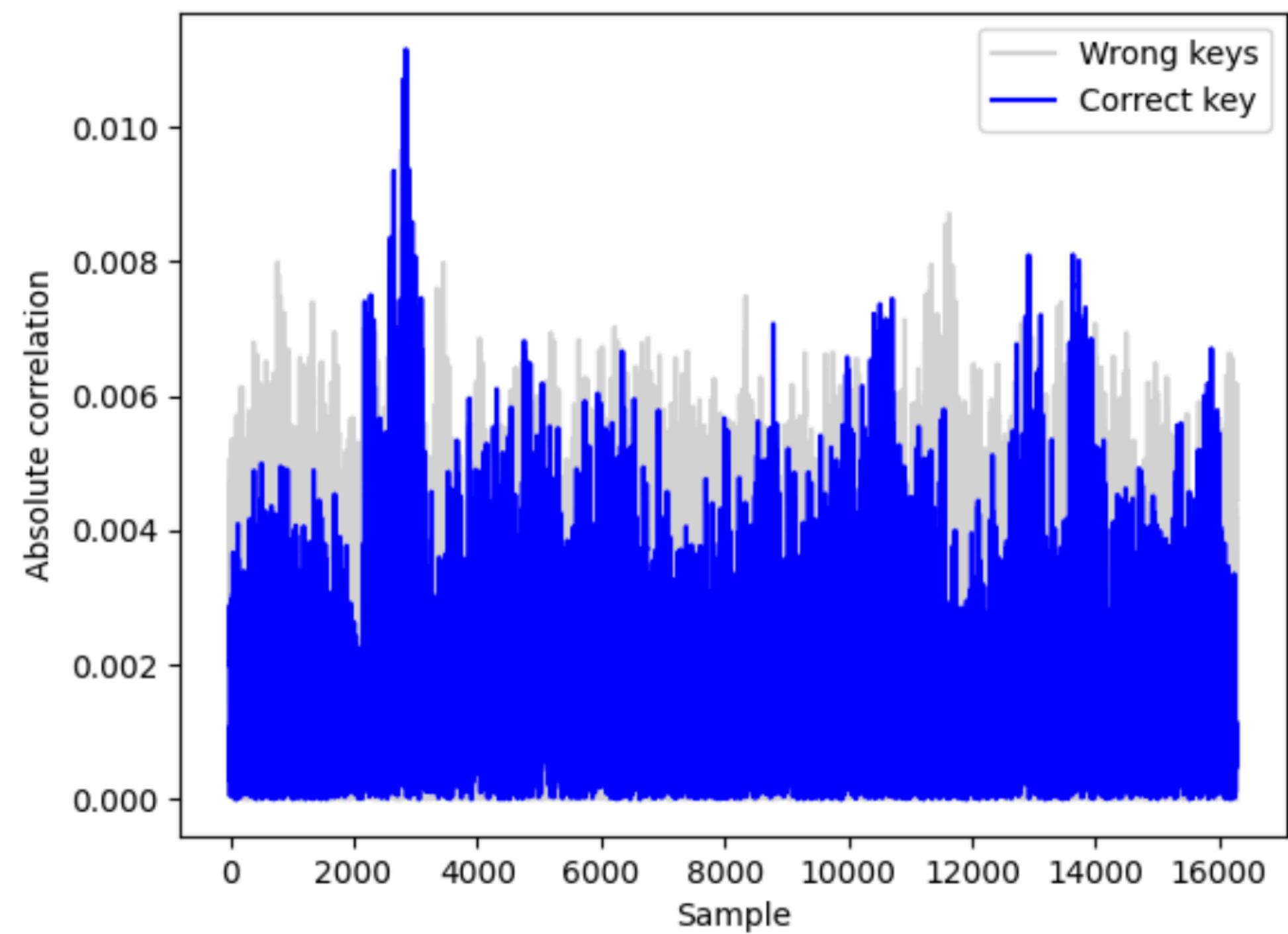


Our attack considers the last 350 samples
 $w = 50$

Results

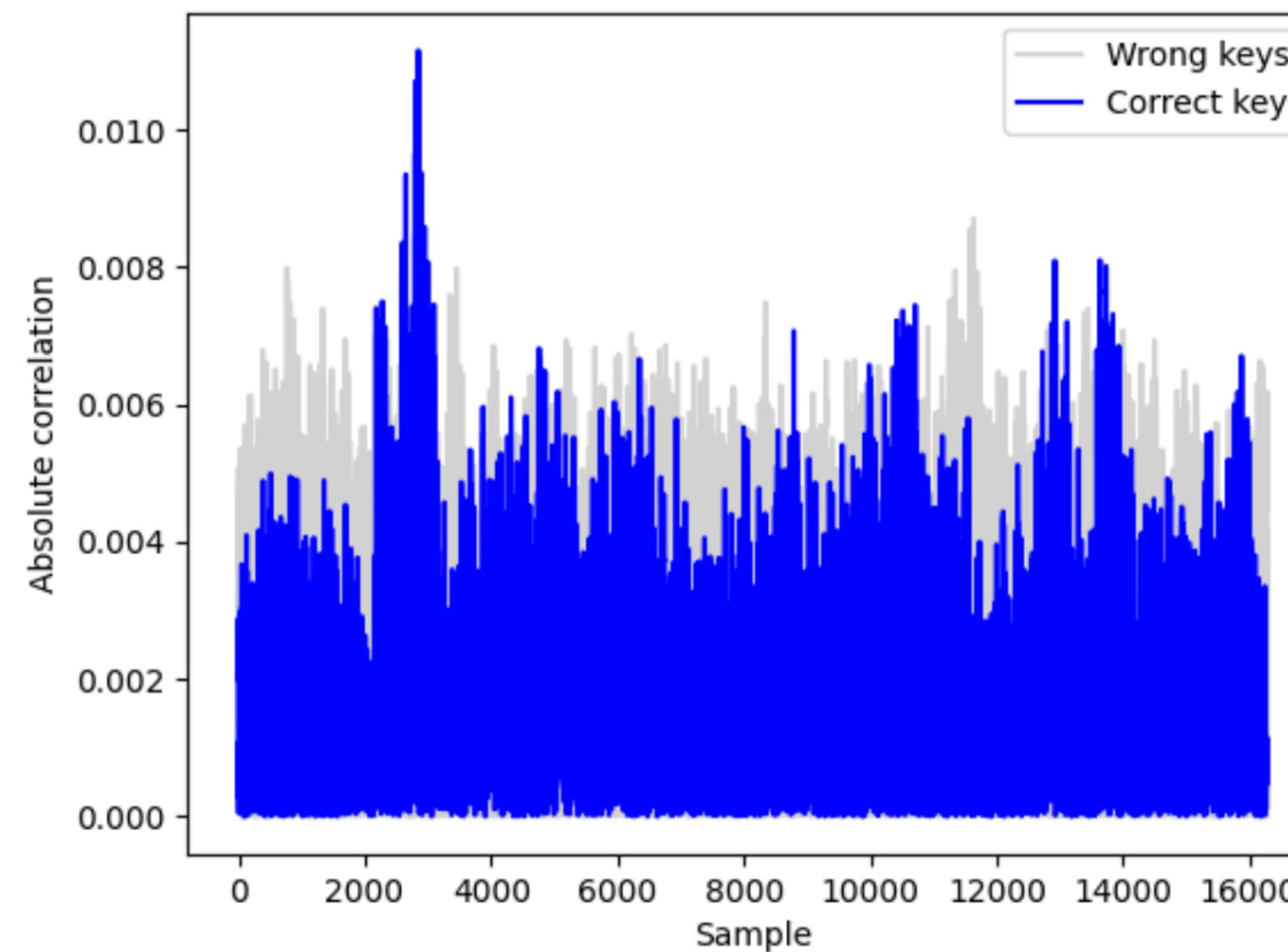
Results

Correlation traces

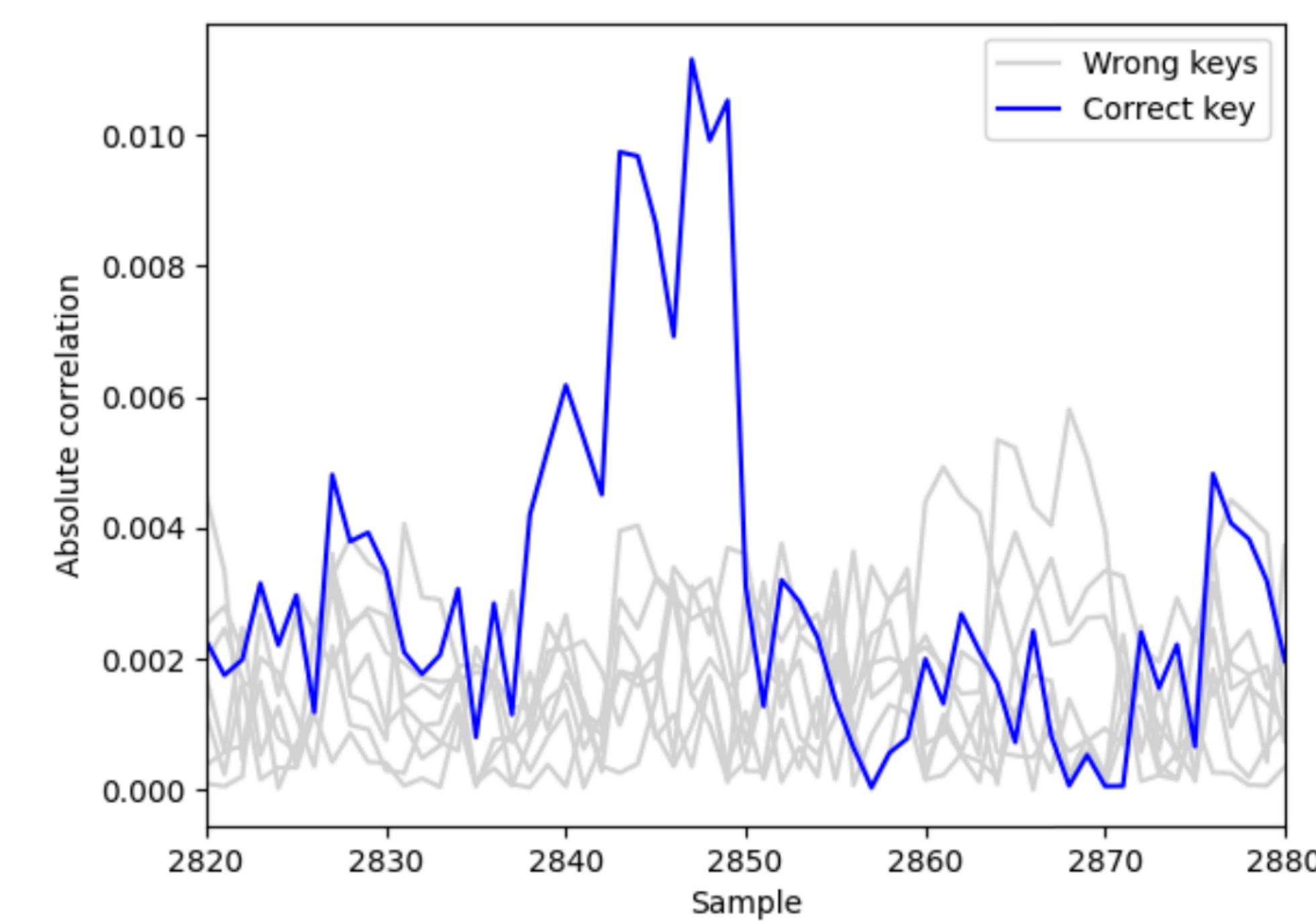


Results

Correlation traces



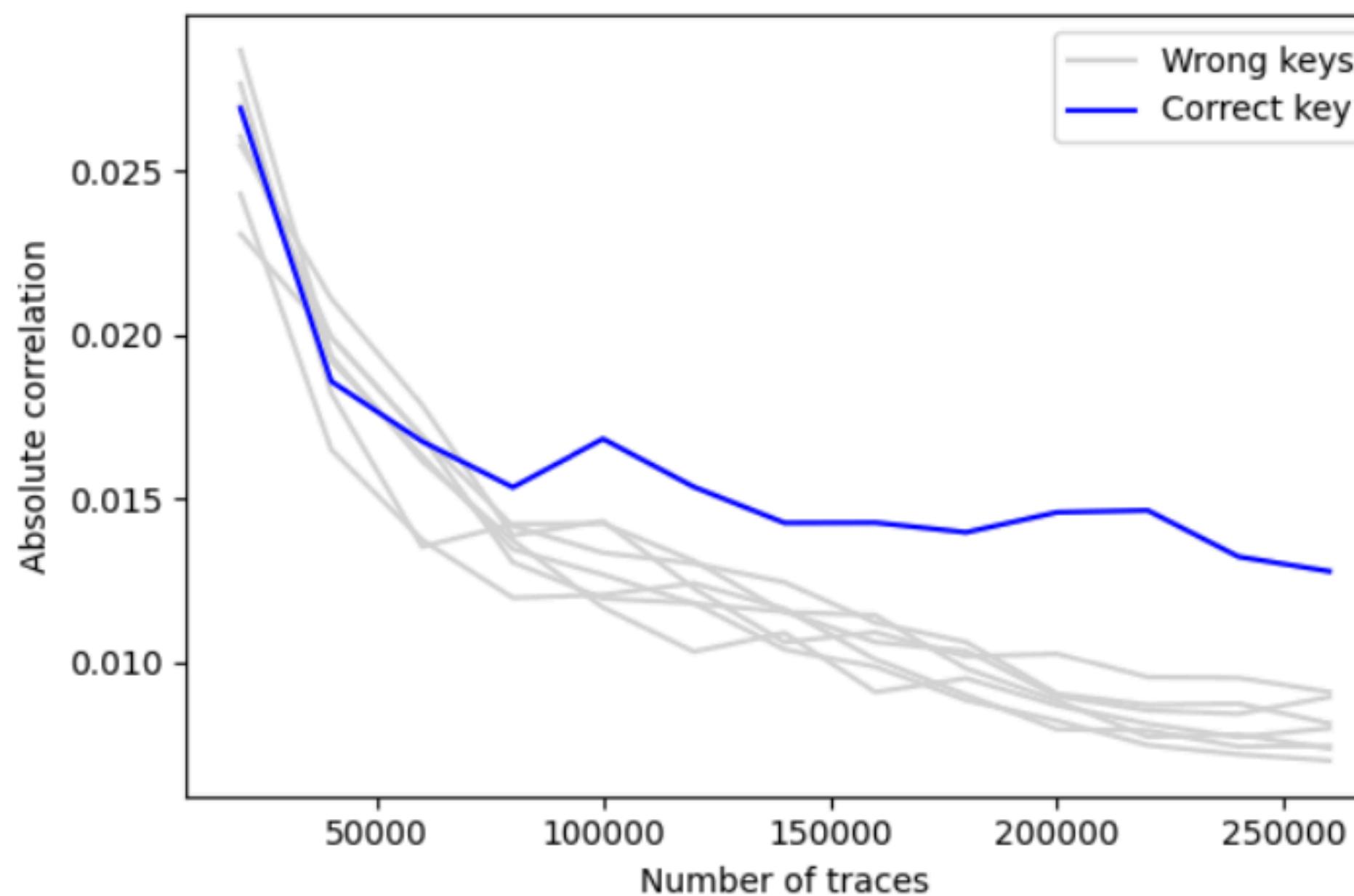
Correlation peak



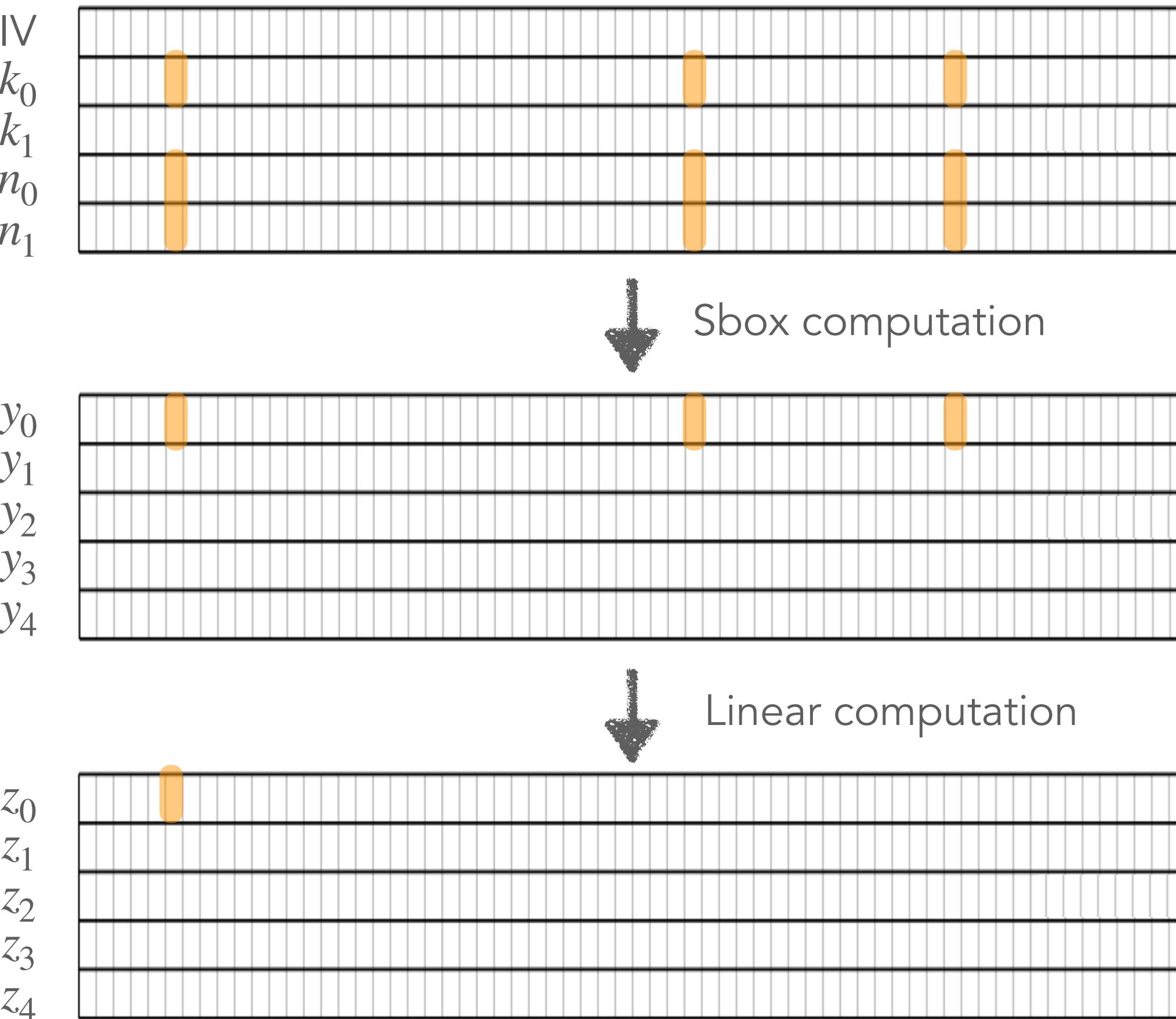
Results

Results

Correlation with increasing number of traces

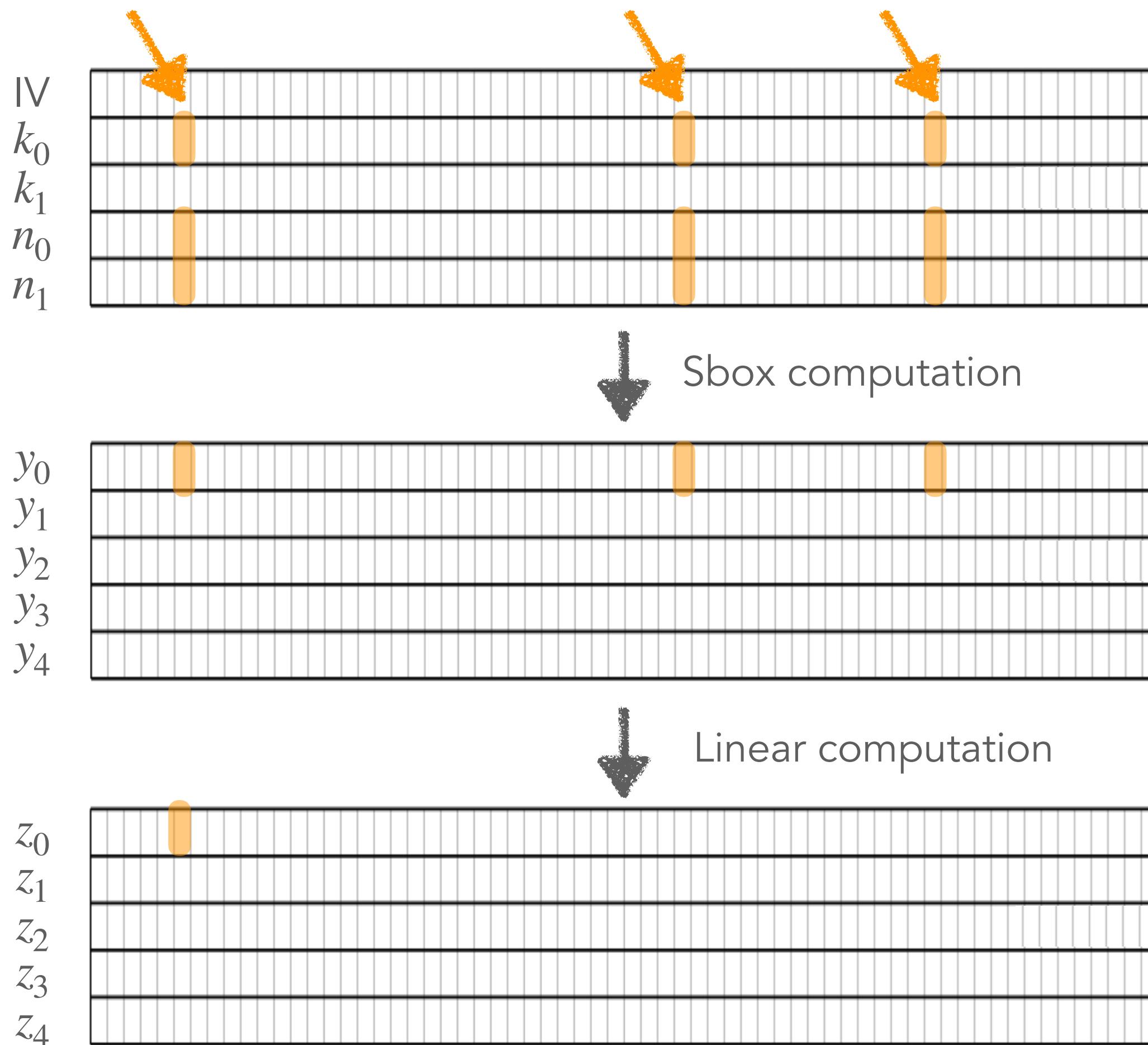


CPA runs for full-key recovery



CPA runs for full-key recovery

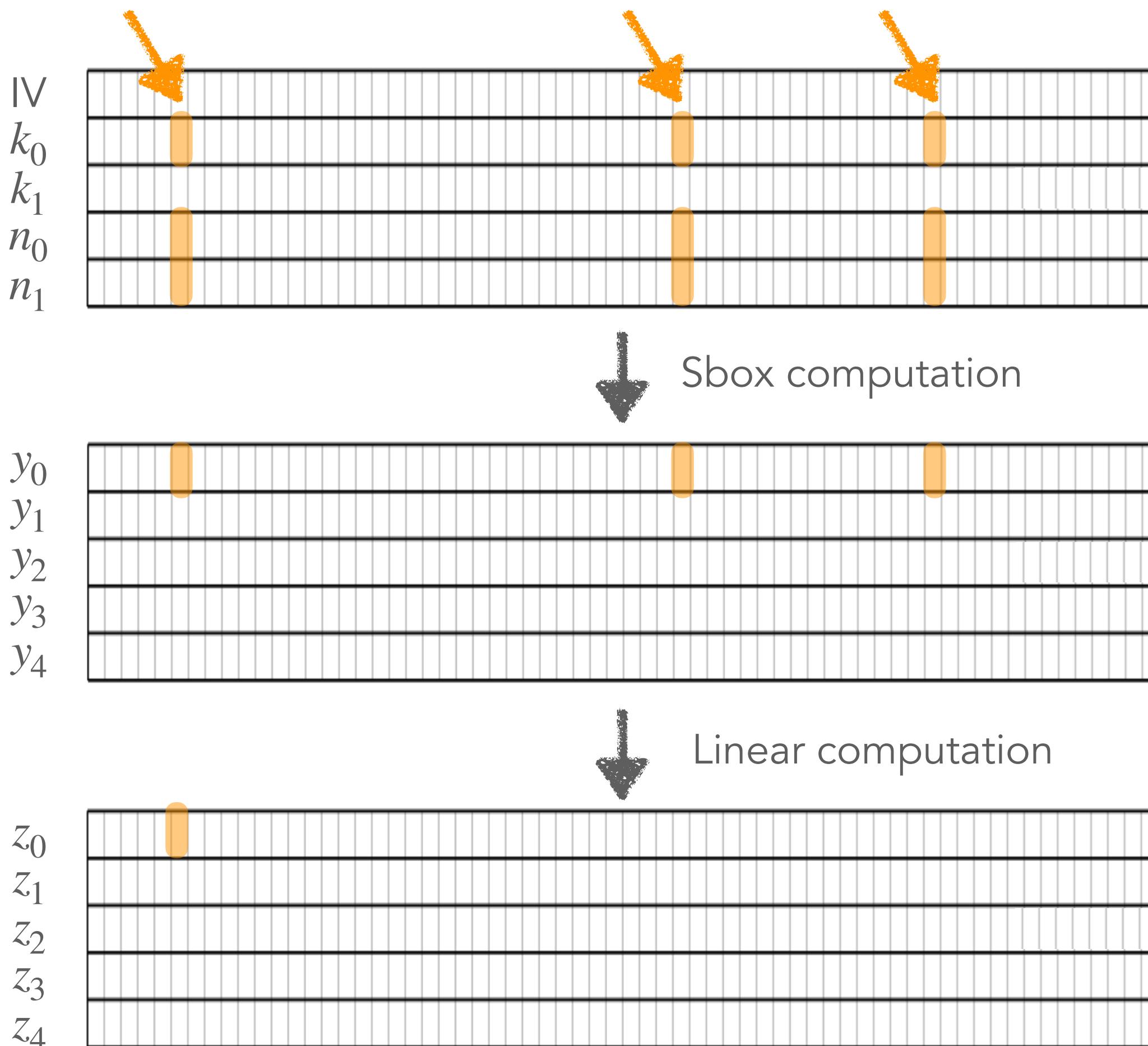
Each CPA run recovers 3 key bits



CPA runs for full-key recovery

Each CPA run recovers 3 key bits

How many CPA runs to recover 128 key bits?



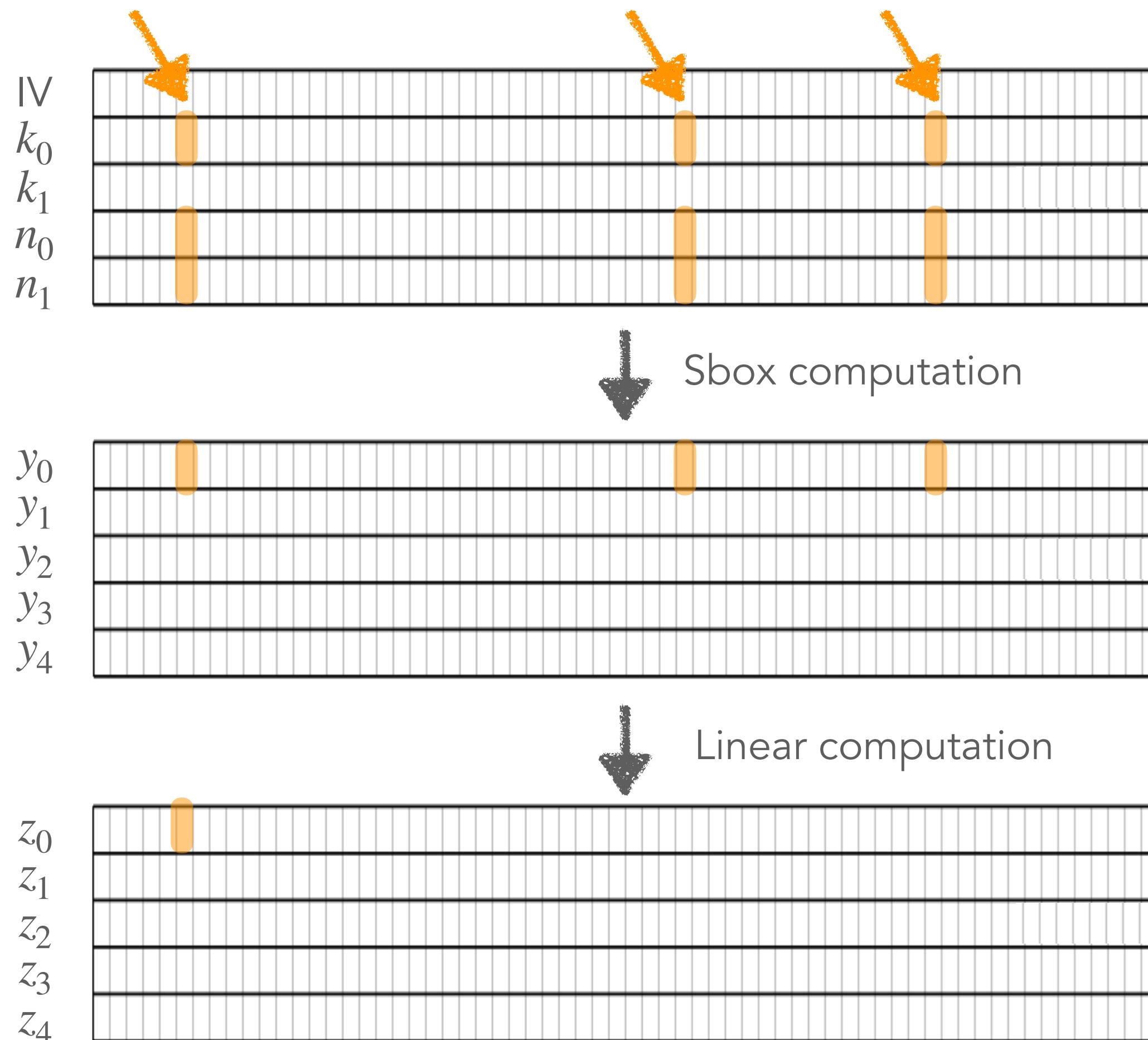
CPA runs for full-key recovery

Each CPA run recovers 3 key bits

How many CPA runs to recover 128 key bits?

Weissbart and Picek, 2023

63 CPA runs



CPA runs for full-key recovery

Each CPA run recovers 3 key bits

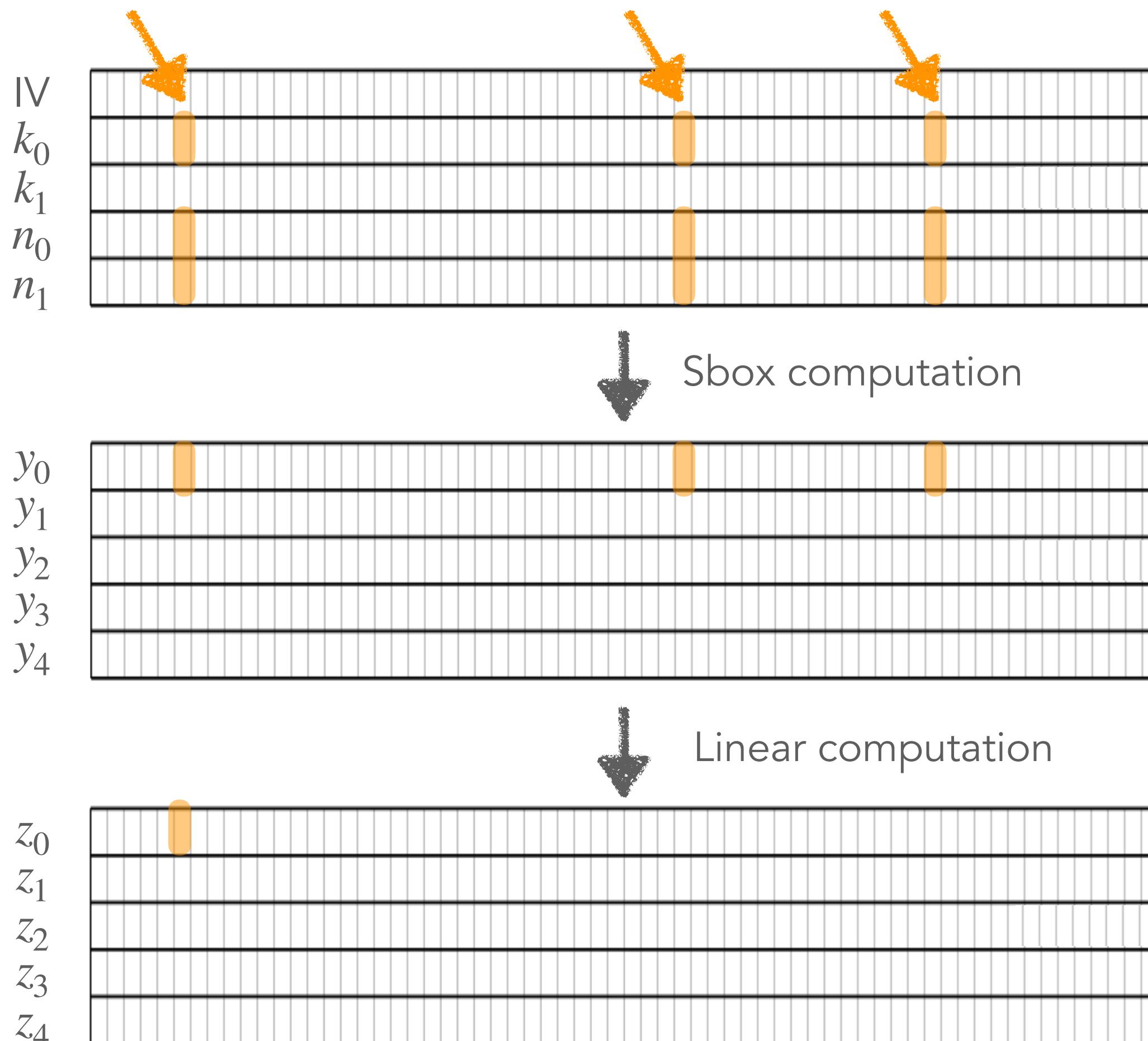
How many CPA runs to recover 128 key bits?

Weissbart and Picek, 2023

63 CPA runs

This work:

- formalizes a *set cover problem*
- uses a SAT solver



CPA runs for full-key recovery

Each CPA run recovers 3 key bits

How many CPA runs to recover 128 key bits?

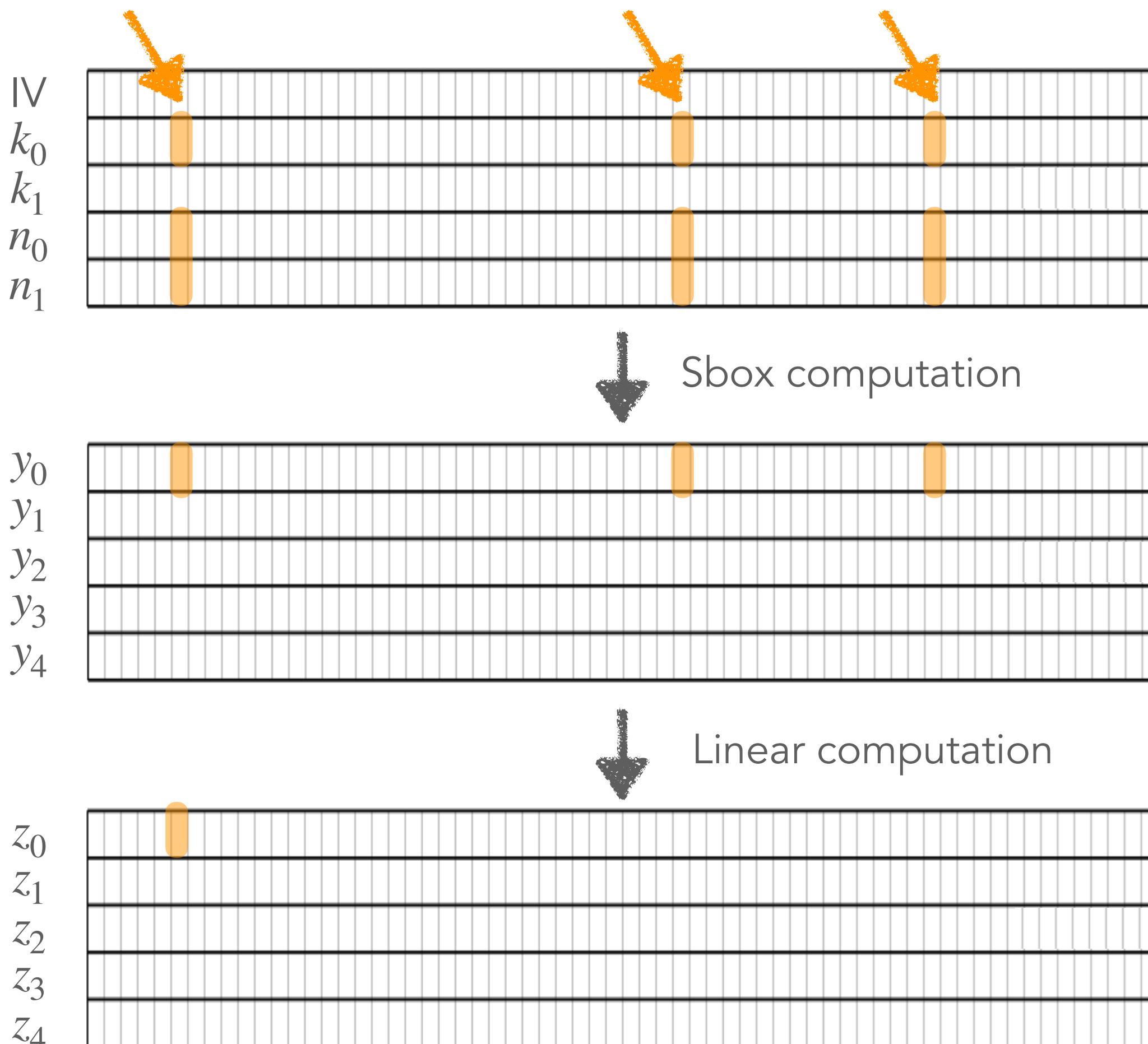
Weissbart and Picek, 2023

63 CPA runs

This work:

- formalizes a *set cover problem*
- uses a SAT solver

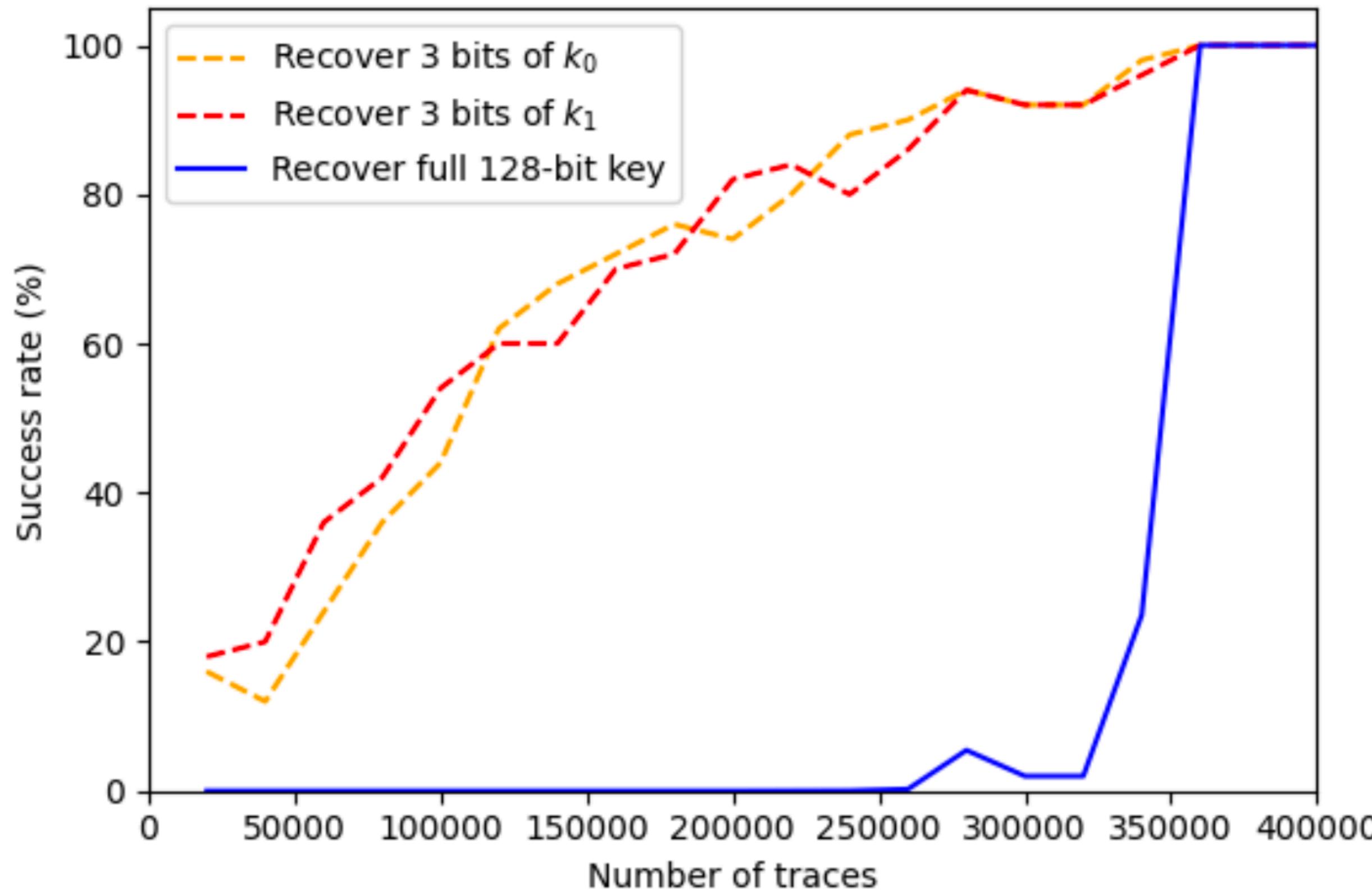
→ 47 CPA runs (optimal)



Full-key recovery

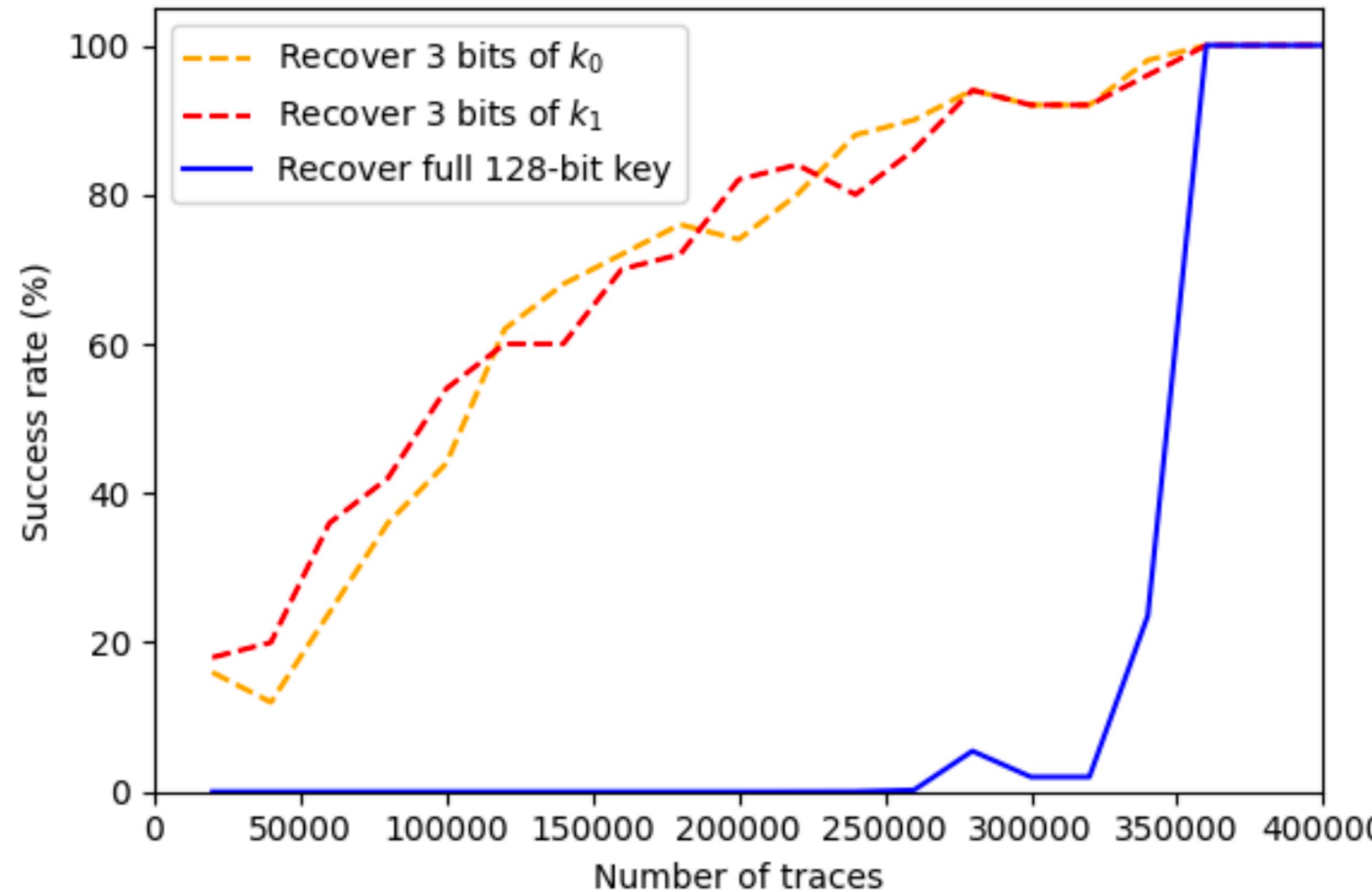
Full-key recovery

Success rates



Full-key recovery

Success rates



360K traces ensure 100% success rates

Recover full key in 4.7 hours

Summary

CPA attack on Ascon

CPA attack on Ascon

- ◆ Quadratic boolean Sbox function
 - ▶ choose the selection function carefully

CPA attack on Ascon

- ◆ Quadratic boolean Sbox function
 - ▶ choose the selection function carefully
- ◆ Optimal number of CPA runs for full-key recovery: 47

CPA attack on Ascon

- ◆ Quadratic boolean Sbox function
 - ▶ choose the selection function carefully
- ◆ Optimal number of CPA runs for full-key recovery: 47
- ◆ First results on practical second-order CPA

Practical Second-Order CPA Attack on Ascon with Proper Selection Function

Viet-Sang Nguyen

joint work with Vincent Grosso and Pierre-Louis Cayrel

CASCADE Conference

Saint-Etienne, 2 April, 2025

