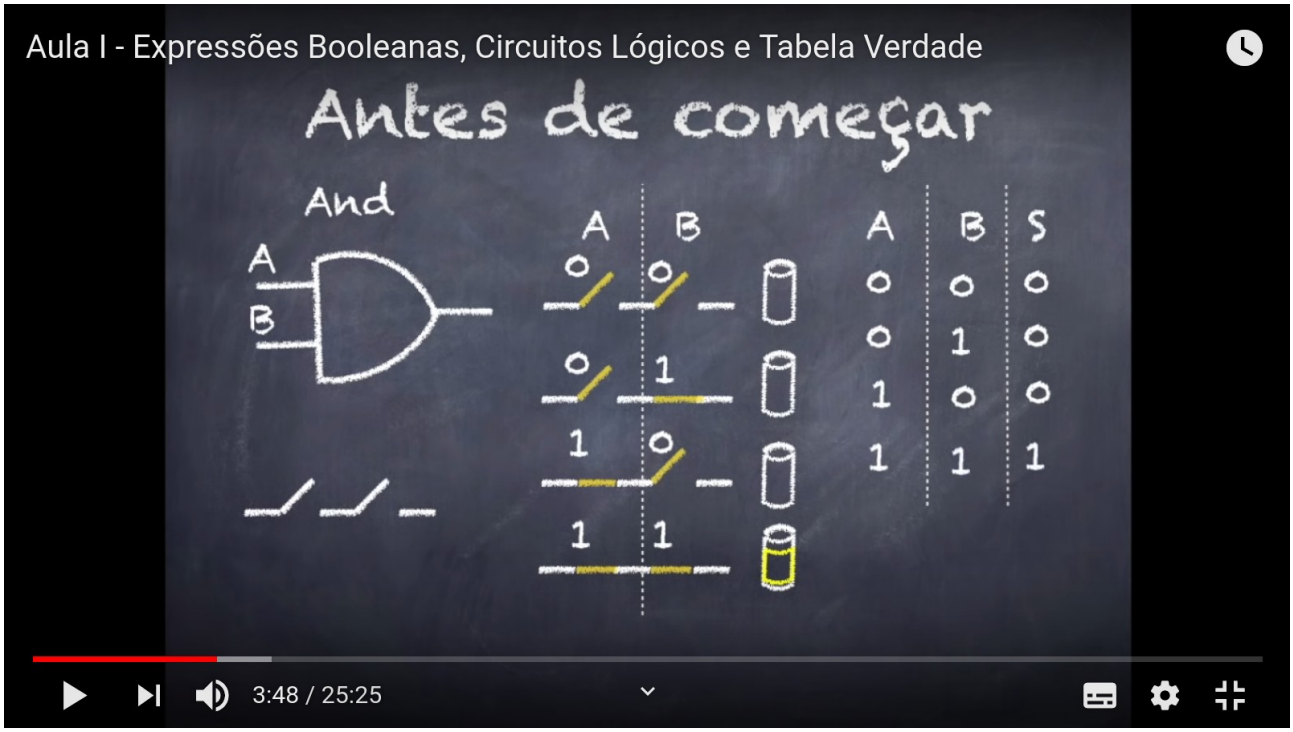
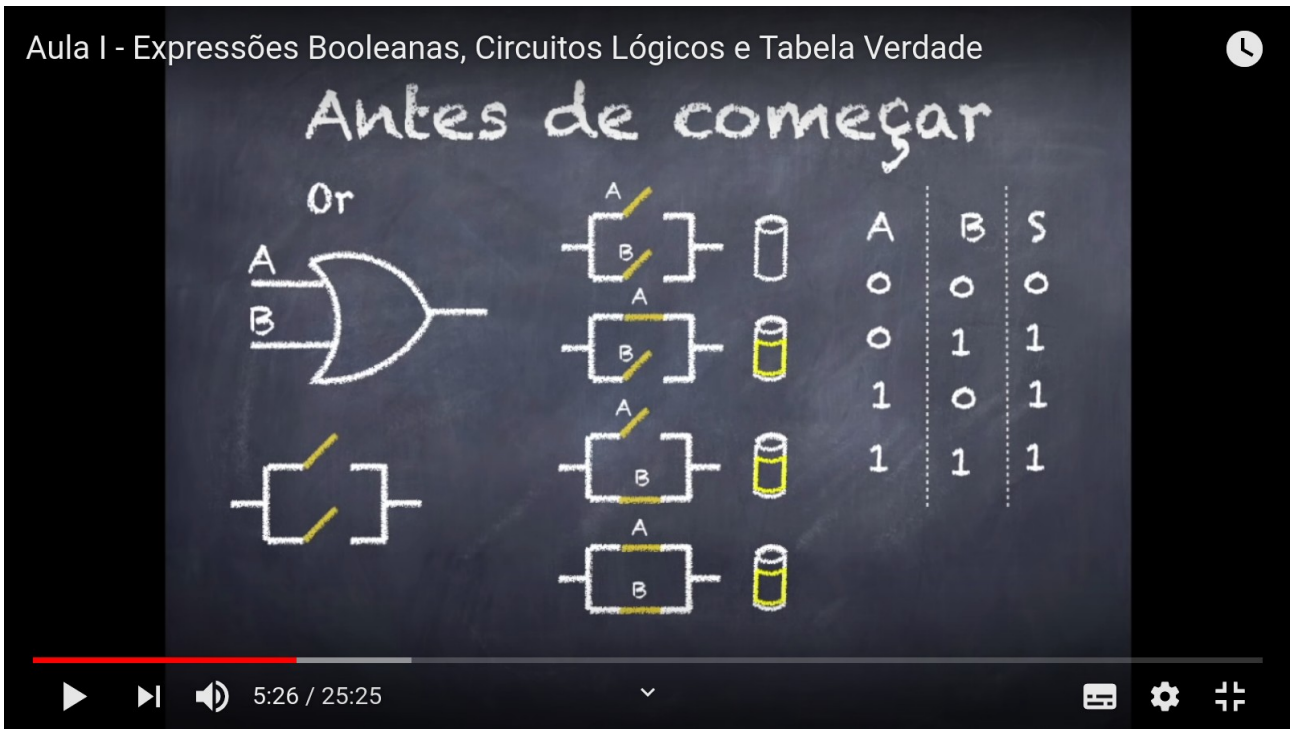
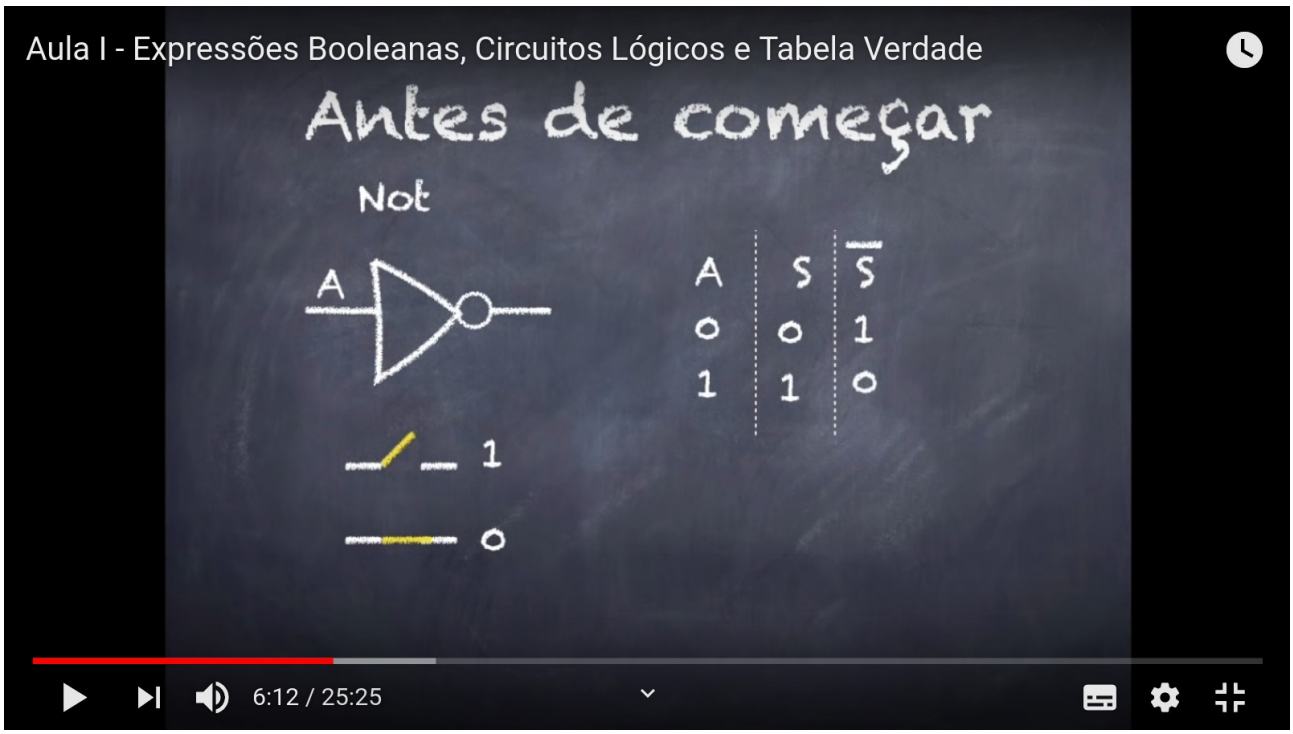
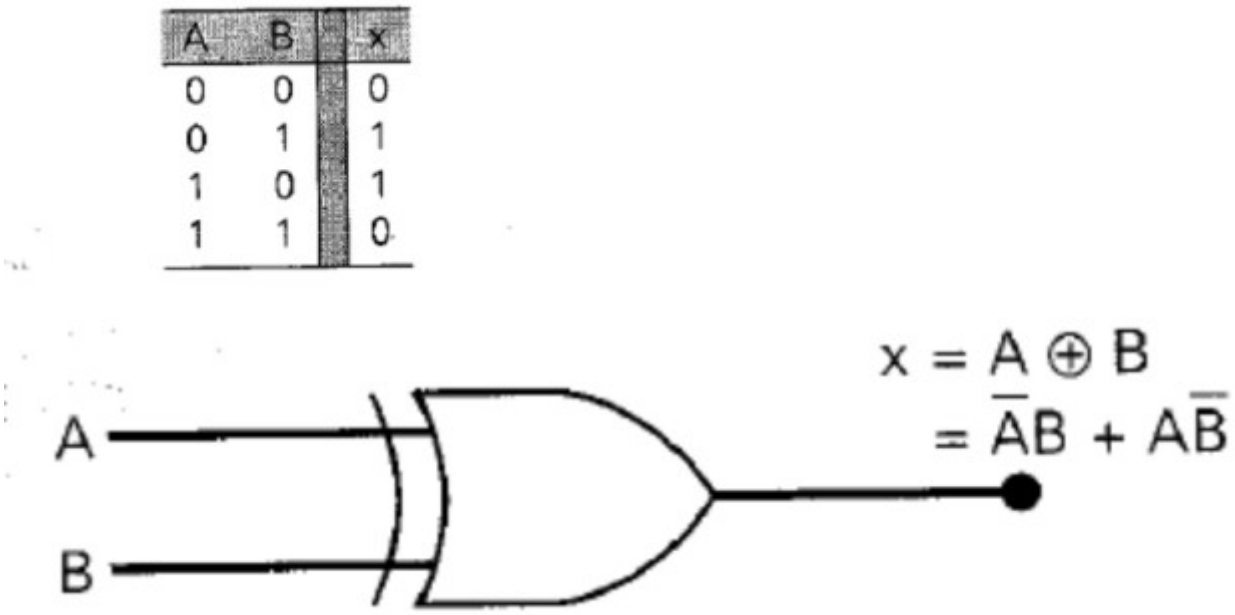
Instituto Federal do Piauí
Tecnologia em Análise e Desenvolvimento de Sistemas
Introdução a Computação
Prof. Ricardo Ramos

Portas Lógicas









Expressão booleana

1. $AB+\overline{(C+D)}$ para valores de A=0, B=1, C=0, D=1

   $0.1+\overline{(0+1)}$

   $0 + (0)$

   0

Circuito lógico

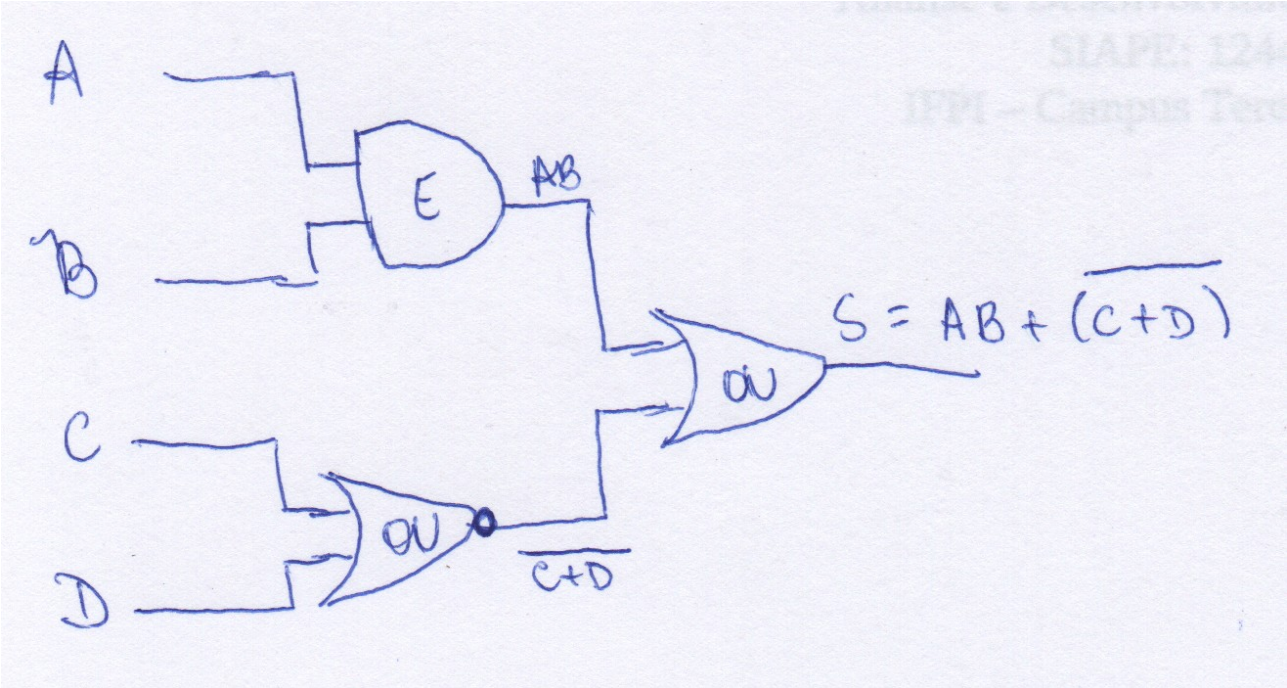


Tabela Verdade

A	B	C	D	AB	C+D	$\overline{C+D}$	S
0	0	0	0	0	0	1	1
0	0	0	1	0	1	0	0
0	0	1	0	0	1	0	0
0	0	1	1	0	1	0	0
0	1	0	0	0	0	1	1
0	1	0	1	0	1	0	0
0	1	1	0	0	1	0	0
0	1	1	1	0	1	0	0
1	0	0	0	0	0	1	1
1	0	0	1	0	1	0	0
1	0	1	0	0	1	0	0
1	0	1	1	0	1	0	0
1	1	0	0	1	0	1	1
1	1	0	1	1	1	0	1
1	1	1	0	1	1	0	1
1	1	1	1	1	1	0	1