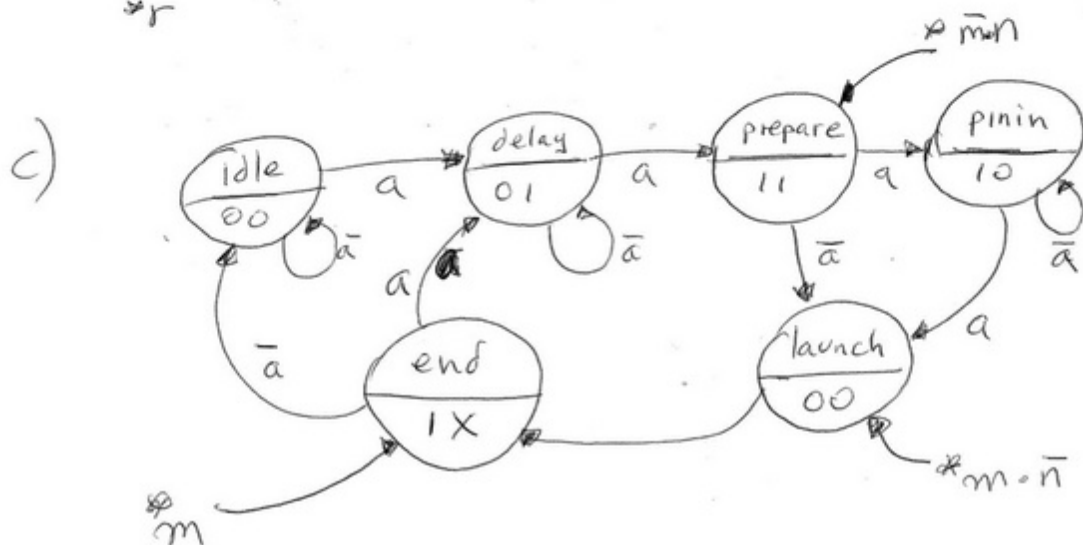
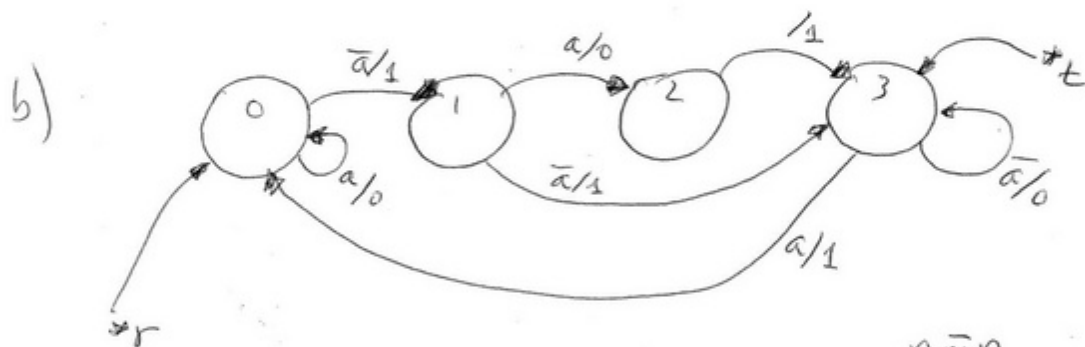
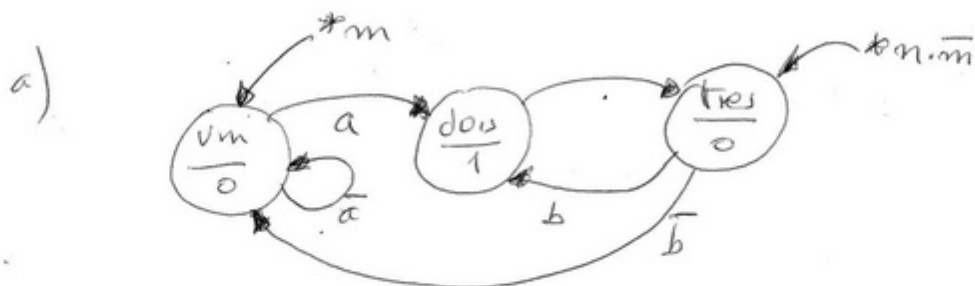


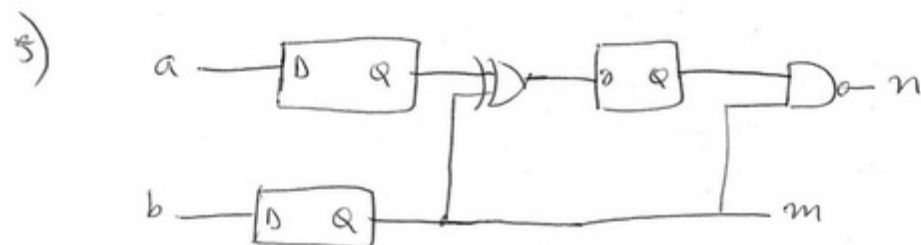
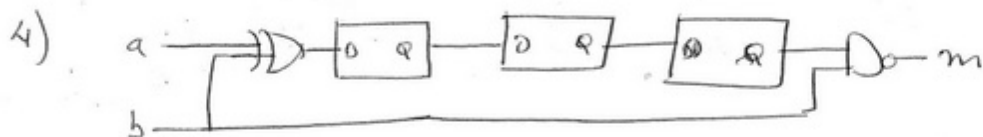
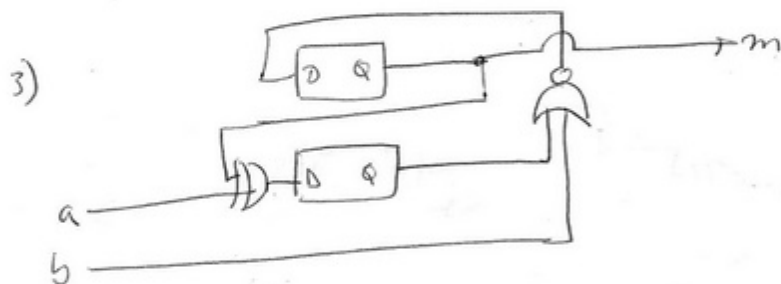
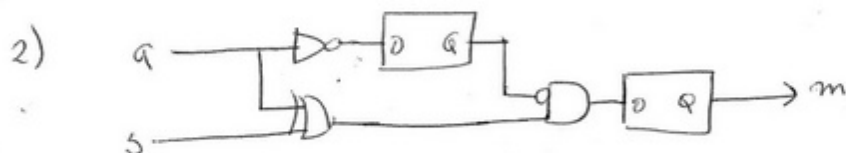
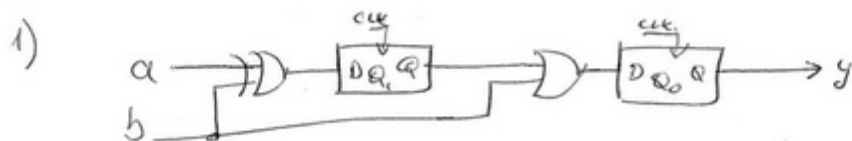
Ex III

Encontre o circuito a partir do diagrama de estado, incluindo a parte assíncrona.



ED III

Encontre o diagrama de estado a partir dos circuitos a seguir: [Todos os FFs tem o mesmo CLK]
[As entradas Set/Reset estão desabilitadas]



EDII

Encontre o diagrama de estado completo (síncrono e parte assíncrona) dos circuitos a seguir.

Assuma que os clocks de todos os FFs estão ligados no mesmo ponto.

