

VINNE MALIK

534 , North Civil Lines,
Saket Road,
Muzaffarnagar (U.P) - 251001
e-mail :- vinne15102110@gmail.com

OBJECTIVE

To pursue a challenging career in the field of IT in an Organization providing stimulating work environment and opportunities for performance oriented career growth.

QUALIFICATION

Pursuing **B.Tech (ECE)** from **JIIT** ,
Sector 62, Noida

Current CGPA - 9.4

AND

GNIIT from **NIIT**, Connaught Place, New Delhi.

12th

86.2%

(Pass out in 2014 , CBSE Board)

10th

9.4 CGPA

(Pass out in 2012 , CBSE Board)

SKILL SET

Programming Language

C, Java, Verilog, System Verilog

Technologies & Tools

Questa, ModelSim, Orcad PSPICE, MATLAB

Projects

Microcontroller, Universal Asynchronous Receiver Transmitter, 8-bit RISC processor (Verilog), LC3 Microcontroller (System Verilog), CMOS optimization using Cuckoo Search Algorithm

TRAINING

Organization

Mentor Graphics India Pvt. Ltd (May-2018 to July-2018)

Designation

Trainee, Higher Education Programme-2018, "Verification of Electronic Design and Systems using System Verilog"

Organization

Ericsson India (January-2018 to May-2018)

Designation

Trainee, ARAN-2018, "Advance Radio Access Network"

PERSONAL INFORMATION

Phone No.

7830000850

Father's Name

Shri Brijender S Malik

Nationality/Status

Indian

To sum up my strengths, I have strong will power, flexibility and rational approach. I believe in beating deadlines and assimilating fundamentals.

Date :

Place : Noida

Vinne Malik